

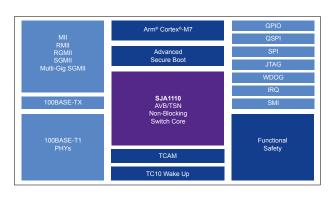
# **SJA1110 TSN ETHERNET SWITCH**

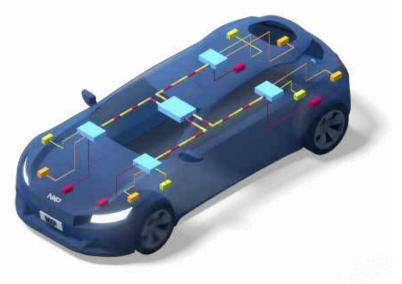
The SJA1110 automotive Ethernet switch family offers innovative and dedicated safety and security features designed for optimal integration in auto ECUs. The four switch variants enable modular ECU design and platforms and support different automotive applications such as gateways, ADAS boxes, and infotainment ECUs.

# **KEY FEATURES**

- Integrated 100BASE-T1 and 100BASE-TX PHYs
- Integrated Arm® Cortex®-M7 based core
- Best-in class packet inspection and DoS prevention capabilities
- Advanced secure boot capabilities
- Purpose built functional safety features
- Support for Wake-over-Ethernet (OPEN TC10)
- $\bullet\,$  Rich set of Time-Sensitive Networking (TSN) standards
- Rich set of NXP original AVB and AUTOSAR® software
- System solution with S32G Vehicle Networking Processor and VR5510 power management unit

# SJA1110 ETHERNET SWITCH BLOCK DIAGRAM





## **ENABLEMENT**

- Production-grade Software Development Kit (SDK)
- Native integration with NXP Design Studio IDE
- Production grade AUTOSAR drivers
- Production grade AVB/802.1AS synchronization protocol middleware
- Evaluation board compatible with NXP's Smart Application Blueprint for Rapid Engineering (SABRE)
- Linux® Drivers



## **NETWORKING APPLICATIONS**

- Optimized NXP chipset solution with S32G processor enables unmatched routing, firewalling, intrusion/ detection/prevention capabilities
- Best-in-class TCAM-based frame inspection for IDPS support, DOS prevention and advanced frames management
- BOM optimization features include compatibility with VR5510 PMIC, four pin-compatible variants and optimized cascaded configuration

# S32G MII RGMII SGMII SGMII Multi-Gig SGMII 100BASE-TX VR5510 (PMIC) Power to S32G + (up to 2x) SJA1110x Arm® Cortex®-M7 GPIO QSPI Advanced SPI Secure Boot JTAG WDOG IRQ AVBRTSN Non-Blocking Switch Core Functional Safety

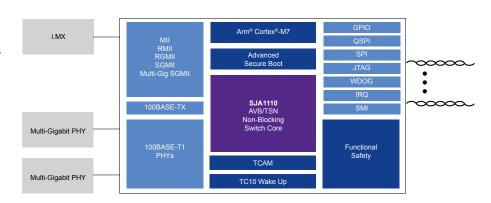
## **ADAS APPLICATIONS**

- Functional safety-dedicated features improving ECU safety design
- Safety manual enable optimized safety design up to ASIL-D ECUs
- Automotive Grade 1 (-40 / +125° C) capability for optimized PCB design
- High-SGMII count for EMC friendly design
- Production-grade AUTOSAR drivers
- Compatible with TTTEch® MotionWise® middleware

### Arm® Cortex®-M7 S32G/ S32V SPI Advanced Secure Boot JTAG WDOG MCU IRQ SJA1110 AVB/TSN 100BASE-TX Non-Blocking MCU Functional TCAM MCU TC10 Wake Up

# INFOTAINMENT/CLUSTER APPLICATIONS

- Multi-gigabit SGMII for external Gigabit and Multi-Gigabit PHYs
- Autonomous operation support avoids dependency from untrusted external host
- Avnu®-Certified\* AVB/gPTP stack for integrated controller
- Support for Wake over Ethernet (OPEN TC10)
- Integrated controller with programmable GPIOs



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