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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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DATA SHEET



Phase-out/Discontinued THYRISTORS 8P2SMA, 8P4SMA

8 A RESIN MOLD TYPE SCR

<R> DESCRIPTION

The 8P2SMA and 8P4SMA are resin mold type SCRs with an average on-state current 8 A (Tc = 88° C), repetitive peak offstate voltage 200 V and 400 V.

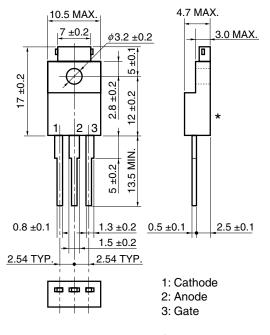
<R> FEATURES

- Can be replaced with TO-220AB package
- High allowable on-current when using a single unit

APPLICATIONS

- Motor speed control for household appliance
- Temperature control for heater and constant temperature box
- · Constant voltage power source and battery charger
- Automotive application such as regulator
- Various solid state relay, etc.

<R> PACKAGE DRAWING (Unit: mm)



*: To test bench-mark

Standard weight: 2 g

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MAXIMUM RATINGS

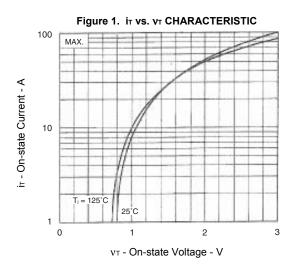
Parameter	Symbol	8P2SMA	8P4SMA	Unit	Remarks	
Non-repetitive Peak Reverse Voltage	Vrsm	300	500	V	_	
Non-repetitive Peak Off-state Voltage	V _{DSM}	300	500	V	_	
Repetitive Peak Reverse Voltage	VRRM	200	400	V	_	
Repetitive Peak Off-state Voltage	V_{DRM}	200	400	V	_	
Average On-state Current	I _{T(AV)}	8 (Tc = 88°C, single pha	Α	Refer to Figure 11		
Effective On-state Current	I _{T(RMS)}	12	Α	and 12 .		
Surge On-state Current	Ітѕм	100 (f = 50 Hz, sine	Α	Refer to Figure 2.		
		110 (f = 60 Hz, sine	110 (f = 60 Hz, sine half wave, 1 cycle)			
Fusing Current	∫i⊤²dt	45 (1 ms ≤	A ² s	_		
Critical Rate Rise of On-state Current	dl⊤/dt	50			_	
Peak Gate Power Dissipation	Рдм	5 (f ≥ 50 Hz, Duty ≤ 10%)			Refer to Figure 3.	
Average Gate Power Dissipation	P _{G(AV)}	0.	W			
Peak Gate Forward Current	Iгдм	2 (f ≥ 50 Hz,	Α	_		
Peak Gate Reverse Voltage	Vrgm	10		V	_	
Junction Temperature	Tj	-40 to +125			_	
Storage Temperature	Tstg	–55 to	°C	_		

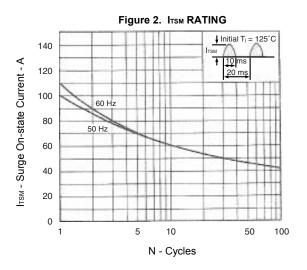
ELECTRICAL CHARACTERISTICS (Tj = 25°C)

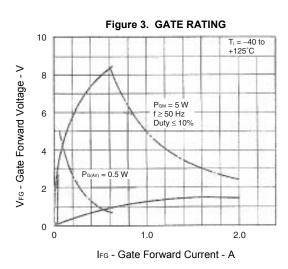
Parameter	Symbol	Conditions		MIN.	TYP.	MAX.	Unit	Remarks
Repetitive Peak Reverse Current	IRRM	V _{RM} = V _{RRM}	T _j = 25°C	1	_	100	μΑ	_
			T _j = 125°C	_	_	2	mA	_
Repetitive Peak Off-state Current	IDRM	V _{DM} = V _{DRM}	T _j = 25°C	_	_	100	μΑ	_
			T _j = 125°C	_	_	2	mA	_
On-state Voltage	Vтм	I _{тм} = 25 А		_	_	1.4	V	Refer to Figure 1.
Gate Trigger Current	Іст	V_{DM} = 6 V, R_L = 100 Ω		_	_	10	mA	Refer to Figure 4.
Gate Trigger Voltage	V _{GT}	V_{DM} = 6 V, R_L = 100 Ω		_	_	1.5	V	
Gate Non-trigger Voltage	V _{GD}	$T_j = 125^{\circ}C, V_{DM} = \frac{1}{2} V_{DRM}$		0.2	_	_	V	_
Holding Current	Ін	V _{DM} = 24 V, I _{TM} = 25 A		_	6	_	mA	-
Critical Rate Rise of Off-state Voltage	dv/dt	$T_j = 125^{\circ}C$, $V_{DM} = \frac{2}{3} V_{DRM}$		_	40	_	V/μs	_
Circuit Commuted Turn-off Time	t _q	T _j = 125°C, I _{TM} = 8 A		-	100	-	μs	-
		$dir/dt = 15 A/\mu s, V_R \ge 25 V,$						
		$V_{DM} = \frac{2}{3} V_{DRM}$, $dV_D/dt = 10 V/\mu s$						
Thermal Resistance Note	Rth(j-c)	Junction to case	e DC	_	_	3.7	°C/W	Refer to Figure 13.
	Rth(j-a)	Junction to amb	ient DC	-	_	60	°C/W	

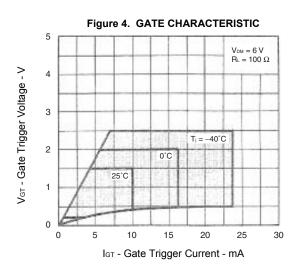


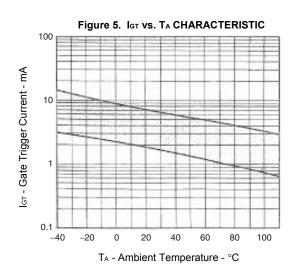
TYPICAL CHARACTERISTICS

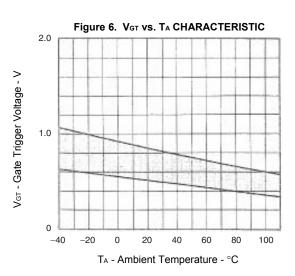




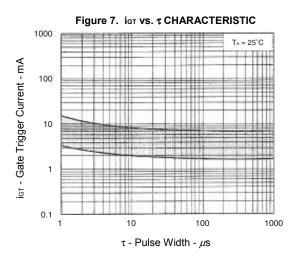


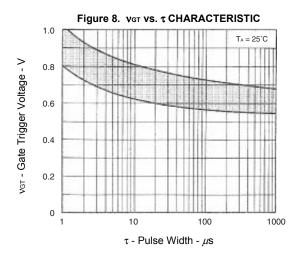


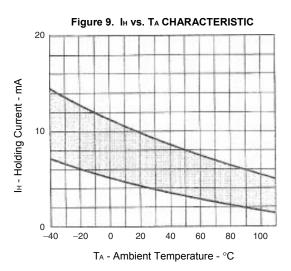


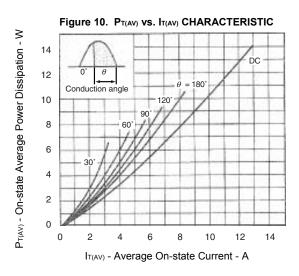


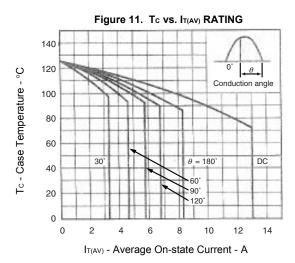


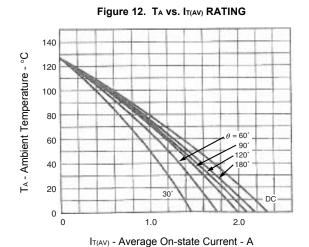




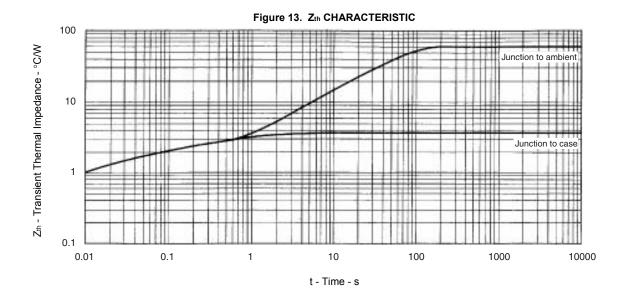












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