

PC713VxNSZX Series

DIP 6 pin Includes Base Terminal Connection Photocoupler



■ Description

PC713VxNSZX Series contains an IRED optically coupled to a phototransistor.

It is packaged in a 6 pin DIP, available in SMT gullwing lead-form option.

Input-output isolation voltage(rms) is 5.0kV.

Collector-emitter voltage is 80V(*) and CTR is 50% to 600% at input current of 5mA.

■ Features

- 1. 6 pin DIP package
- 2. Double transfer mold package (Ideal for Flow Soldering)
- 3. With base terminal
- 4. High collector-emitter voltage (V_{CEO}:80V(*))
- 5. High isolation voltage between input and output $(V_{iso(rms)}: 5.0kV)$
 - (*) Up to Date code "P7" (July 2002) $\ensuremath{V_{\text{CEO}}}$: 35V.

■ Agency approvals/Compliance

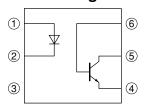
- Recognized by UL1577 (Double protection isolation), file No. E64380 (as model No. PC713V)
- 2. Approved by TÜV (VDE0884) (as an option) file No. R-9151576 (as model No. **PC713V**)
- 3. Package resin: UL flammability grade (94V-0)

Applications

- 1. Home appliances
- 2. Programmable controllers
- 3. Personal computer peripherals



■ Internal Connection Diagram

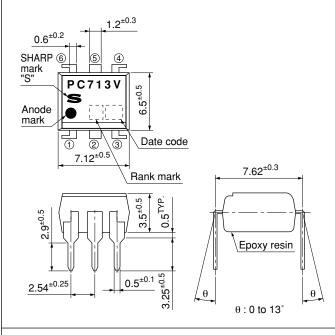


- 1 Anode
- ② Cathode
- ③ NC
- 4 Emitter
- ⑤ Collector
- 6 Base

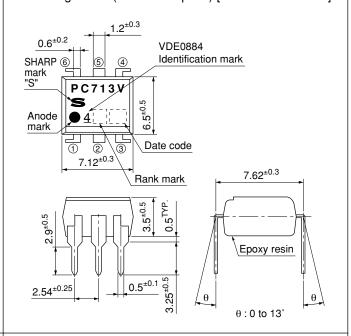
■ Outline Dimensions

(Unit: mm)

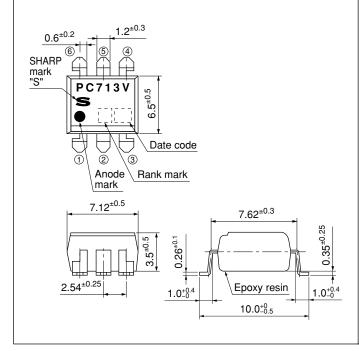




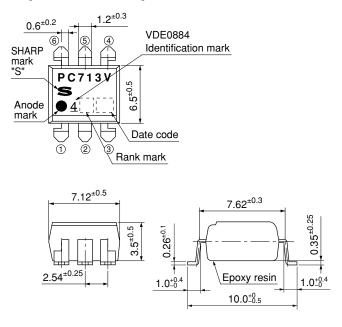
2. Through-Hole (VDE0884 option) [ex. PC713VxYSZX]



3. SMT Gullwing Lead-Form [ex. PC713VxNIPX]



4. SMT Gullwing Lead-Form (VDE0884 option) [ex. **PC713VxYIPX**]



Product mass: approx. 0.36g



Date code (2 digit)

1st digit				2nd digit		
Year of production				Month of production		
A.D.	Mark	A.D	Mark	Month	Mark	
1990	A	2002	P	January	1	
1991	В	2003	R	February	2	
1992	С	2004	S	March	3	
1993	D	2005	T	April	4	
1994	Е	2006	U	May	5	
1995	F	2007	V	June	6	
1996	Н	2008	W	July	7	
1997	J	2009	X	August	8	
1998	K	2010	A	September	9	
1999	L	2011	В	October	0	
2000	M	2012	С	November	N	
2001	N	:	:	December	D	

repeats in a 20 year cycle

Country of origin Japan

Rank mark
Refer to the Model Line-up



■ Absolute Maximum Ratings

■ Absolute Maximum Ratings $(T_a=25^{\circ}G)$						
	Parameter	Symbol	Rating	Unit		
	Forward current	I_{F}	50	mA		
Input	*1 Peak forward current	I_{FM}	1	A		
ΙΪ	Reverse voltage	V_R	6	V		
	Power dissipation	P	70	mW		
	Collector-emitter voltage	V_{CEO}	*4 80	V		
	Emitter-collector voltage	V _{ECO}	6	V		
Output	Collector-base voltage	V_{CBO}	*4 80	V		
Out	Emitter-base voltage	V_{EBO}	6	V		
	Collector current	I_{C}	50	mA		
	Collector power dissipation	P _C	150	mW		
Total power dissipation		P _{tot}	170	mW		
Operating temperature		T_{opr}	-25 to +100	°C		
Storage temperature		T_{stg}	-40 to +125	°C		
*2 Isolation voltage		V _{iso (rms)}	5	kV		
*3 Soldering temperature		T_{sol}	260	°C		

^{*1} Pulse width≤100µs, Duty ratio : 0.001 *2 40 to 60%RH, AC for 1minute, f=60Hz

■ Electro-optical Characteristics

 $(T_a=25^{\circ}C)$

 V V μA pF
V μA
μΑ
pF
nA
V
V
V
mA
V
Ω
pF
kHz
μs
μs
0 2 3 3

^{*5} Up to Date code "P7" (July 2002) BV_{CEO} \geq 35V and BV_{CBO} \geq 35V.

^{*3} For 10s

^{*4} Up to Date code "P7" (July 2002) V_{CEO} : 35V, V_{CBO} : 35V.



■ Model Line-up

Lead Form	Throug	h-Hole	SMT Gullwing				I _C [mA]	
Package		Sle	eve		Taping		Rank mark	$(I_F=5mA,$
Package		50pcs /	sleeve		1 000pcs / reel			$V_{CE}=5V$,
VDE0884		Approved		Approved		Approved		$T_a=25^{\circ}C$
	PC713V0NSZX	PC713V0YSZX	PC713V0NIZX	PC713V0YIZX	PC713V0NIPX	PC713V0YIPX	with or with out	2.5 to 30.0
Model No.	PC713V1NSZX	PC713V1YSZX	PC713V1NIZX	PC713V1YIZX	PC713V1NIPX	PC713V1YIPX	A	4.0 to 8.0
	PC713V2NSZX	PC713V2YSZX	PC713V2NIZX	PC713V2YIZX	PC713V2NIPX	PC713V2YIPX	В	6.5 to 13.0
	PC713V3NSZX	PC713V3YSZX	PC713V3NIZX	PC713V3YIZX	PC713V3NIPX	PC713V3YIPX	С	10.0 to 20.0
	PC713V5NSZX	PC713V5YSZX	PC713V5NIZX	PC713V5YIZX	PC713V5NIPX	PC713V5YIPX	A or B	4.0 to 13.0
	PC713V6NSZX	PC713V6YSZX	PC713V6NIZX	PC713V6YIZX	PC713V6NIPX	PC713V6YIPX	B or C	6.5 to 20.0
	PC713V8NSZX	PC713V8YSZX	PC713V8NIZX	PC713V8YIZX	PC713V8NIPX	PC713V8YIPX	A, B or C	4.0 to 20.0

Please contact a local SHARP sales representative to inquire about production status and Lead-Free options.



Fig.1 Forward Current vs. Ambient Temperature

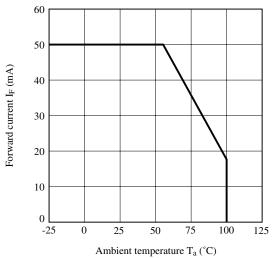


Fig.3 Collector Power Dissipation vs.
Ambient Temperature

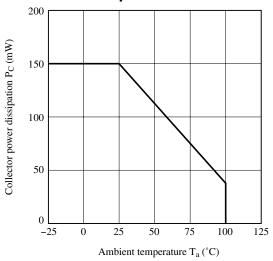


Fig.5 Peak Forward Current vs. Duty Ratio

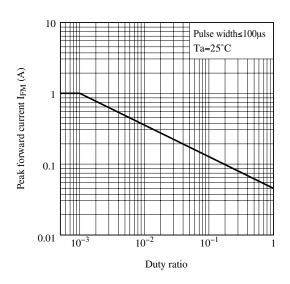


Fig.2 Diode Power Dissipation vs. Ambient Temperature

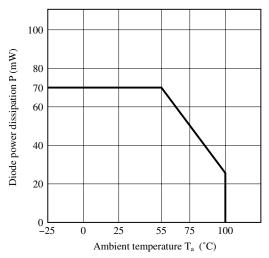


Fig.4 Total Power Dissipation vs. Ambient Temperature

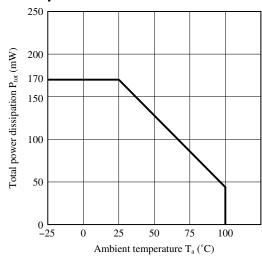


Fig.6 Forward Current vs. Forward Voltage

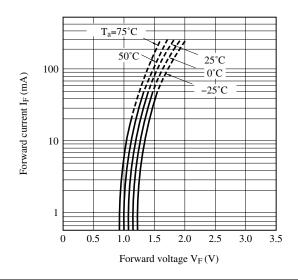




Fig.7 Current Transfer Ratio vs. Forward Current

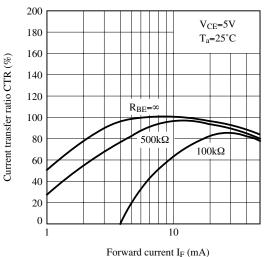


Fig.9 Relative Current Transfer Ratio vs.
Ambient Temperature

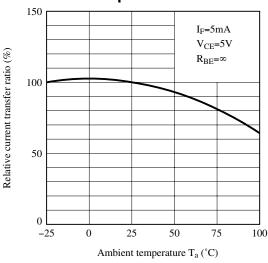


Fig.11 Collector Dark Current vs. Ambient Temperature

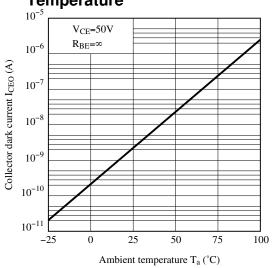


Fig.8 Collector Current vs. Collectoremitter Voltage

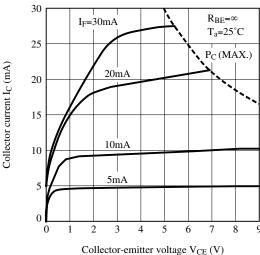


Fig.10 Collector - emitter Saturation Voltage vs. Ambient Temperature

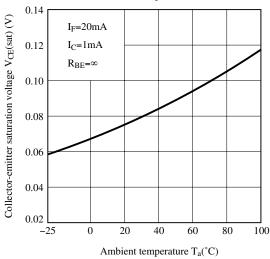


Fig.12 Collector-base Dark Current vs.
Ambient Temperature

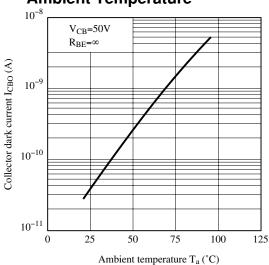




Fig.13 Response Time vs. Load Resistance

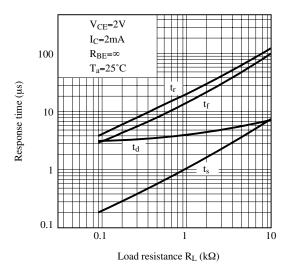


Fig.15 Frequency Response

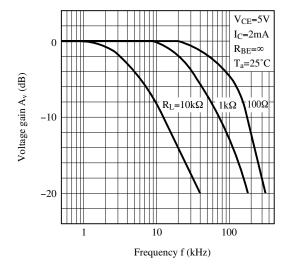
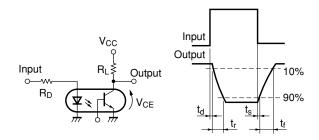
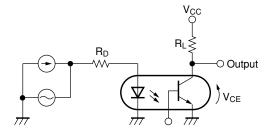


Fig.14 Test Circuit for Response Time



Please refer to the conditions in Fig.13

Fig.16 Test Circuit for Frequency Response



Please refer to the conditions in Fig.15

Remarks: Please be aware that all data in the graph are just for reference and not for guarantee.



■ Design Considerations

Design guide

While operating at I_F<1.0mA, CTR variation may increase.

Please make design considering this fact.

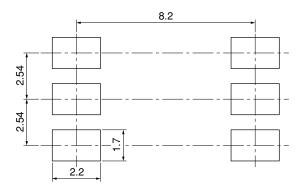
This product is not designed against irradiation and incorporates non-coherent IRED.

Degradation

In general, the emission of the IRED used in photocouplers will degrade over time.

In the case of long term operation, please take the general IRED degradation (50% degradation over 5years) into the design consideration.

Recommended Foot Print (reference)



(Unit: mm)

[☆] For additional design assistance, please review our corresponding Optoelectronic Application Notes.



■ Manufacturing Guidelines

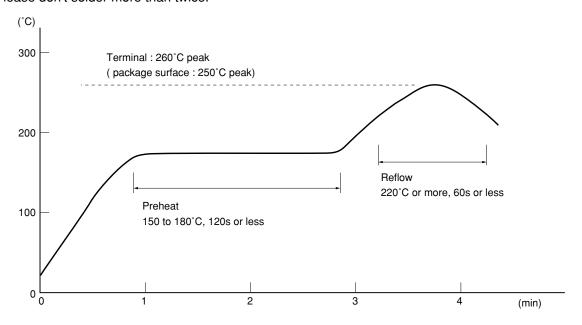
Soldering Method

Reflow Soldering:

Reflow soldering should follow the temperature profile shown below.

Soldering should not exceed the curve of temperature profile and time.

Please don't solder more than twice.



Flow Soldering:

Due to SHARP's double transfer mold construction submersion in flow solder bath is allowed under the below listed guidelines.

Flow soldering should be completed below 270°C and within 10s.

Preheating is within the bounds of 100 to 150°C and 30 to 80s.

Please don't solder more than twice.

Hand soldering

Hand soldering should be completed within 3s when the point of solder iron is below 400°C.

Please don't solder more than twice.

Other notices

Please test the soldering method in actual condition and make sure the soldering works fine, since the impact on the junction between the device and PCB varies depending on the tooling and soldering conditions.



Cleaning instructions

Solvent cleaning:

Solvent temperature should be 45°C or below Immersion time should be 3minutes or less

Ultrasonic cleaning:

The impact on the device varies depending on the size of the cleaning bath, ultrasonic output, cleaning time, size of PCB and mounting method of the device.

Therefore, please make sure the device withstands the ultrasonic cleaning in actual conditions in advance of mass production.

Recommended solvent materials:

Ethyl alcohol, Methyl alcohol and Isopropyl alcohol

In case the other type of solvent materials are intended to be used, please make sure they work fine in actual using conditions since some materials may erode the packaging resin.

Presence of ODC

This product shall not contain the following materials.

And they are not used in the production process for this device.

Regulation substances: CFCs, Halon, Carbon tetrachloride, 1.1.1-Trichloroethane (Methylchloroform)

Specific brominated flame retardants such as the PBBOs and PBBs are not used in this product at all.



■ Package specification

Sleeve package

Package materials

Sleeve: HIPS (with anti-static material)

Stopper: Styrene-Elastomer

Package method

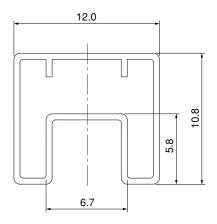
MAX. 50 pcs. of products shall be packaged in a sleeve.

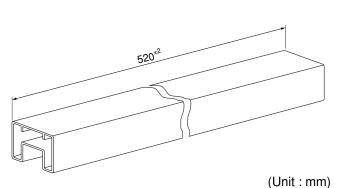
Both ends shall be closed by tabbed and tabless stoppers.

The product shall be arranged in the sleeve with its anode mark on the tabless stopper side.

MAX. 20 sleeves in one case.

Sleeve outline dimensions







● Tape and Reel package

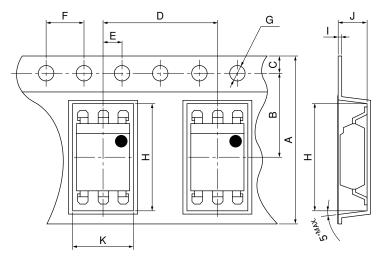
Package materials

Carrier tape: A-PET (with anti-static material)

Cover tape: PET (three layer system)

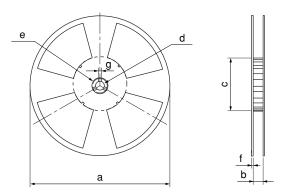
Reel: PS

Carrier tape structure and Dimensions



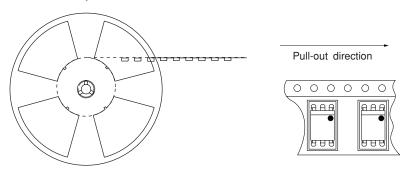
Dimension	ns List					(Unit:mm)
A	В	C	D	Е	F	G
16.0 ^{±0.3}	7.5 ^{±0.1}	1.75 ^{±0.1}	12.0 ^{±0.1}	2.0 ^{±0.1}	4.0 ^{±0.1}	φ1.5 + 8.1
Н	I	J	K			
10.4 ^{±0.1}	0.4 ^{±0.05}	4.2 ^{±0.1}	7.8 ^{±0.1}			

Reel structure and Dimensions



Dimensio	ns List	(Unit: mm)		
a	b	c	d	
330	17.5 ^{±1.5}	100±1.0	13 ^{±0.5}	
e	f	g		
23 ^{±1.0}	2.0±0.5	2.0±0.5		

Direction of product insertion



[Packing: 1 000pcs/reel]



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 - --- Office automation equipment
 - --- Telecommunication equipment [terminal]
 - --- Test and measurement equipment
 - --- Industrial control
 - --- Audio visual equipment
 - --- Consumer electronics
- (ii) Measures such as fail-safe function and redundant design should be taken to ensure reliability and safety when SHARP devices are used for or in connection

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