

32.768kHz Series OX / OY Type

3.2 x 2.5 / 2.5 x 2.0 mm SMD Oscillator

FFATURE

- Conforms to AEC-Q200
- Tight symmetry (45 to 55%) available.
- Operation voltage: 1.8V, 2.5V, 3.3V
- Tri-state enable/disable
- Built-in ASIC enables reduction of current consumption.

TYPICAL APPLICATION

- Typically used for real time clock application.
- Mobile Phone
- DSC, Set-top Box , HDTV
- Car navigation systems.

Actual Size

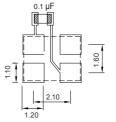


RoHS Compliant

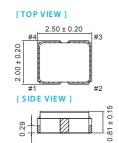
Horio comp

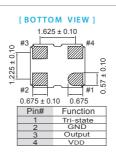
DIMENSION (mm)

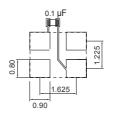
SOLDER PAD LAYOUT (mm) 0.1 µF



To ensure optimal oscillator performance, place a by-pass capacitor of $0.1\mu F$ as close to the part as possible between Vdd and GND pads.







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ELECTRICAL SPECIFICATION

Parameter	3.3V		2.5V		1.8V		Unit
	Min.	Max.	Min.	Max.	Min.	Max.	Offit
Supply Voltage Variation (VDD)	VDD-10%	VDD+10%	VDD-10%	VDD+10%	VDD-10%	VDD+10%	V
Supply Current (@ 15pF load)	_	120	-	120	_	120	uA
(@ no load)	_	80	-	80	_	80	uA
Duty Cycle	45	55	45	55	45	55	%
Output Level (CMOS) Output High (Logic "1")	2.97	_	2.25	_	1.62	_	V
Output Low (Logic "0")	_	0.33	-	0.25	_	0.18	
Transition Time:Rise/Fall Time+	_	50	-	50	_	50	nSec
Start Time	_	2	-	2	_	2	mSec
Tri-State(Input to Pin 1) Enable (High voltage or floating)	2.31	_	1.75	_	1.26		V
Disable (Low voltage or GND)	_	0.99	-	0.75	-	0.54	
Aging (@25 °C 1 st year)	-	±3	-	±3	_	±3	ppm
Storage Temp. Range	-55	125	-55	125	-55	125	℃

Standard frequencies are frequencies which the crystal has been designed and does not imply a stock position

FREQ. STABILITY vs. TEMP. RANGE

Temp. (℃)	<u>±20</u>	<u>±25</u>	±40	±50
-10~+60	0	0	0	0
-20~+70	Δ	0	0	0
-40~+85	×	\triangle	0	0
-40~+105	×	×	0	0
-40~+125	×	×	\triangle	0

^{*} O: Available \(\triangle : Conditional \) X: Not available

⁺Transition times are measured between 10% and 90% of VDD, withan output load of 15pF

^{*}Inclusive of calibration @ 25°C, operating temperature range, input voltage variation, load variation, aging (1st year), shock, and vibration load variation