



150V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
150V	$53m\Omega$ @ $V_{GS} = 10V$	5.2A

Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}), yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- High Frequency Switching
- Synchronous Rectification
- **DC-DC Converters**

Features and Benefits

- High Conversion Efficiency
- Low R_{DS(ON)} Minimizes On-State Losses
- Low Input Capacitance
- Fast Switching Speed
- 100% Unclamped Inductive Switching (UIS) Test in Production -Ensures More Reliable and Robust End Application
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

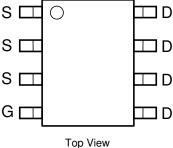
Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 3 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (93)
- Weight: 0.074 grams (Approximate)

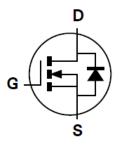
SO-8



Top View







Equivalent Circuit

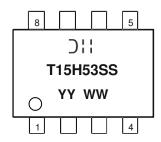
Ordering Information (Note 4)

Part Number	Case	Packaging
DMT15H053SSS-13	SO-8	2,500/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



);; = Manufacturer's Marking T15H53SS = Product Type Marking Code YYWW = Date Code Marking YY or \overline{YY} = Year (ex: 19 = 2019) WW = Week (01 to 53)



Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	V_{DSS}	150	V		
Gate-Source Voltage		V _{GSS}	±20	V	
	T _A = +25°C	I-	5.2	- A	
Continuous Drain Current (Note C) V 10V	T _A = +70°C	ID	4.1		
Continuous Drain Current (Note 6) V _{GS} = 10V	T _C = +25°C	I-	15	۸	
	T _C = +70°C	ID	12	Α	
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%)	I _{DM}	34	Α		
Maximum Continuous Body Diode Forward Current (Note 6)	I _S	13	Α		
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle :	I _{SM}	34	Α		
Avalanche Current, L = 1mH	I _{AS}	11.7	Α		
Avalanche Energy, L = 1mH	Eas	68.4	mJ		

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

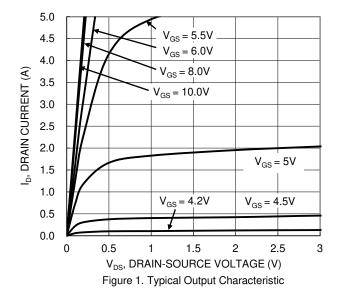
Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P_{D}	1.3	W
Thermal Resistance, Junction to Ambient (Note 5)	$R_{ heta JA}$	94	°C/W
Total Power Dissipation (Note 6)	P _D	2	W
Thermal Resistance, Junction to Ambient (Note 6)	R _{0JA}	59	°C/W
Thermal Resistance, Junction to Case (Note 6)	$R_{ heta JC}$	7	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	150	_	_	V	$V_{GS} = 0V, I_D = 10mA$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μA	V _{DS} = 120V, V _{GS} = 0V	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)	•		•	•			
Gate Threshold Voltage	V _{GS(TH)}	2	_	4	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	41	53	mΩ	$V_{GS} = 10V, I_D = 4.1A$	
Diode Forward Voltage	V _{SD}	_	0.8	1	V	V _{GS} = 0V, I _S = 4.1A	
DYNAMIC CHARACTERISTICS (Note 8)	1	I .					
Input Capacitance	C _{iss}	_	814	_		V _{DS} = 75V, V _{GS} = 0V f = 1MHz	
Output Capacitance	Coss	_	84	_	pF		
Reverse Transfer Capacitance	C_{rss}	_	3.7	_			
Gate Resistance	Rg	_	0.6	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Q_g	_	11.5	_		$V_{DS} = 75V, I_D = 4.1A,$ $V_{GS} = 10V$	
Gate-Source Charge	Q _{gs}	_	4.6	_	nC		
Gate-Drain Charge	Q_{gd}	_	2.8	_			
Turn-On Delay Time	t _{D(ON)}	_	8.5	_			
Turn-On Rise Time	t _R	_	3.4	_		$V_{DS} = 75V, V_{GS} = 10V,$ $I_{D} = 4.1A, R_{g} = 6\Omega$	
Turn-Off Delay Time	t _{D(OFF)}	_	11.9	_	ns		
Turn-Off Fall Time	t _F	_	6.2	_			
Reverse Recovery Time	t _{RR}	_	47	_	ns	1 4 1 A di/dt 100 A /	
Reverse Recovery Charge	Q_{RR}	_	87	_	$_{\rm nC}$ $_{\rm l_F} = 4.1A$, $_{\rm di/dt} = 100A/\mu s$		

 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to product testing. Notes:





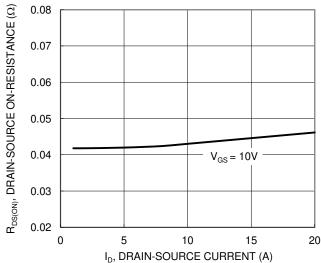


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

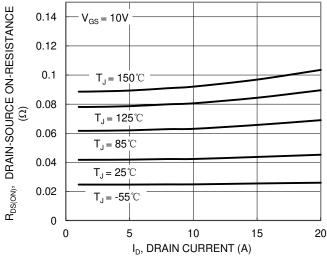


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

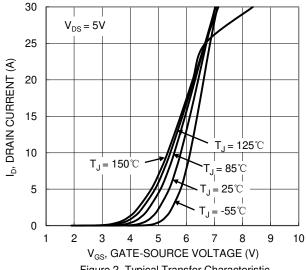


Figure 2. Typical Transfer Characteristic

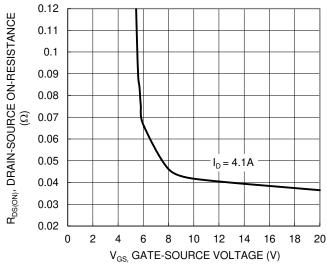


Figure 4. Typical Transfer Characteristic

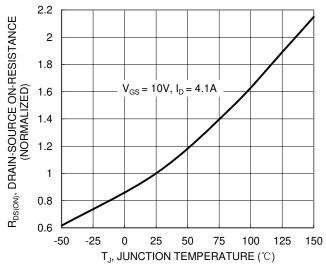
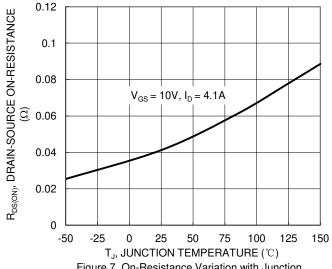
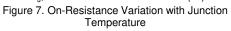


Figure 6. On-Resistance Variation with Junction Temperature









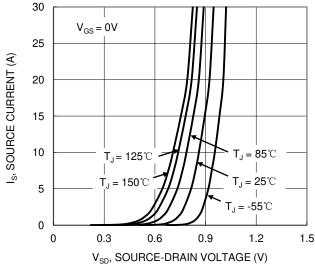


Figure 9. Diode Forward Voltage vs. Current

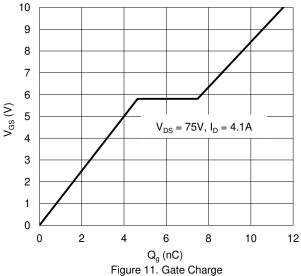


Figure 11. Gate Charge

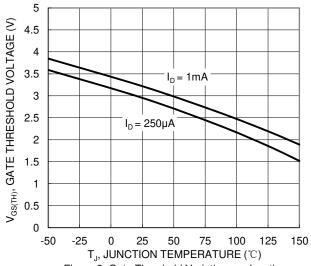


Figure 8. Gate Threshold Variation vs. Junction Temperature

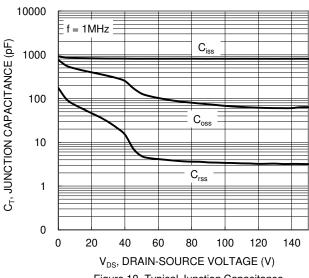


Figure 10. Typical Junction Capacitance

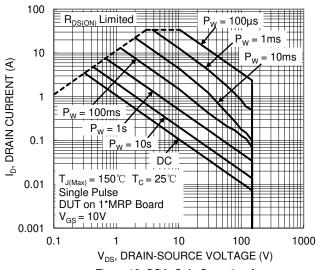


Figure 12. SOA, Safe Operation Area



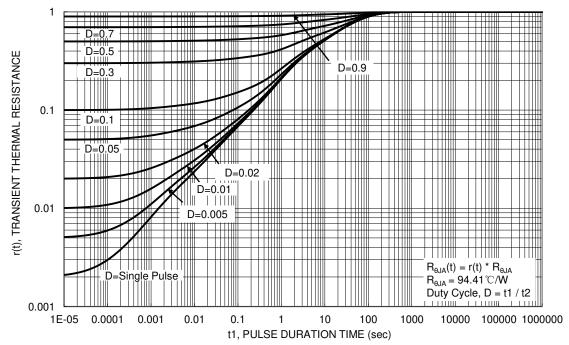


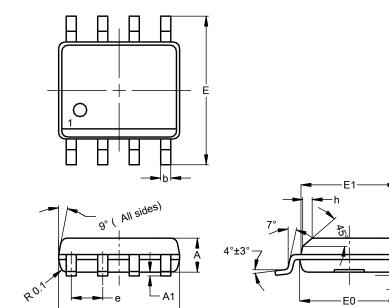
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SO-8



	SO-8					
Dim	Min	Max	Тур			
Α	1.40	1.50	1.45			
A1	0.10	0.20	0.15			
b	0.30	0.50	0.40			
С	0.15	0.25	0.20			
D	4.85	4.95	4.90			
Е	5.90	6.10	6.00			
E1	3.80	3.90	3.85			
E0	3.85	3.95	3.90			
е			1.27			
h			0.35			
L	0.62	0.82	0.72			
Q	0.60	0.70	0.65			
All Dimensions in mm						

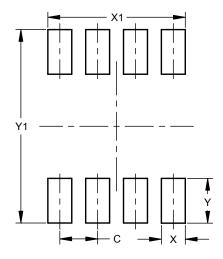
Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SO-8

-Gauge Plane

Seating Plane



Dimensions	Value (in mm)		
С	1.27		
Х	0.802		
X1	4.612		
Υ	1.505		
V1	6.50		



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