PH2729-25M

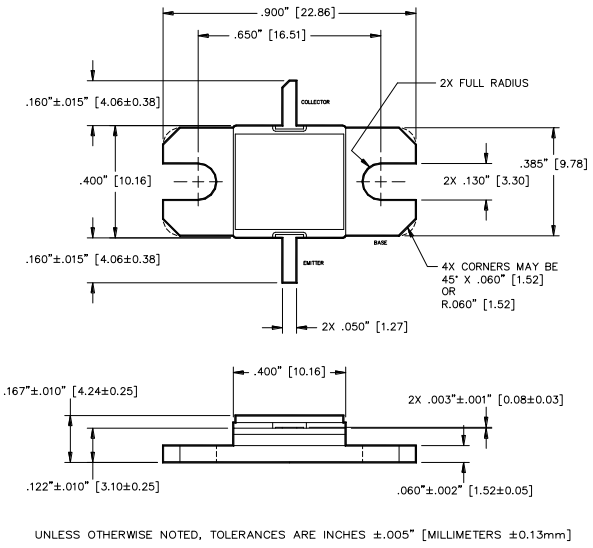
Radar Pulsed Power Transistor
25W, 2.7-2.9 GHz, 100µs Pulse, 10% Duty

Rev. V1

Features

- NPN silicon microwave power transistors
- Common base configuration
- Broadband Class C operation
- High efficiency inter-digitized geometry
- Diffused emitter ballasting resistors
- Gold metallization system
- Internal input and output impedance matching
- Hermetic metal/ceramic package
- RoHS compliant

Outline Drawing



UNLESS OTHERWISE NOTED, TOLERANCES ARE INCHES ±.005" [MILLIMETERS ±0.13mm]

Absolute Maximum Ratings at 25°C

Parameter	Symbol	Rating	Units
Collector-Emitter Voltage	V_{CES}	60	V
Emitter-Base Voltage	V_{EBO}	3.0	V
Collector Current (Peak)	I_C	4.0	A
Power Dissipation @ +25°C	P_{TOT}	70	W
Storage Temperature	T_{STG}	-65 to +200	°C
Junction Temperature	T_J	200	°C

Electrical Specifications: T_C = 25 ± 5°C (Room Ambient)

Parameter	Test Conditions	Frequency	Symbol	Min	Max	Units
Collector-Emitter Breakdown Voltage	I_C = 10mA		BV_{CES}	60	-	V
Collector-Emitter Leakage Current	V_{CE} = 40V		I_{CES}	-	1.5	mA
Thermal Resistance	Vcc = 36V, Pin = 3.0W	F = 2.7, 2.8, 2.9 GHz	$R_{TH(JC)}$	-	1.25	°C/W
Output Power	Vcc = 36V, Pin = 3.0W	F = 2.7, 2.8, 2.9 GHz	P_{OUT}	25	-	W
Power Gain	Vcc = 36V, Pin = 3.0W	F = 2.7, 2.8, 2.9 GHz	G_P	9.2	-	dB
Collector Efficiency	Vcc = 36V, Pin = 3.0W	F = 2.7, 2.8, 2.9 GHz	η_C	45	-	%
Input Return Loss	Vcc = 36V, Pin = 3.0W	F = 2.7, 2.8, 2.9 GHz	RL	-	-6	dB
Load Mismatch Tolerance	Vcc = 36V, Pin = 3.0W	F = 2.7, 2.8, 2.9 GHz	VSWR-T	-	3:1	-
Load Mismatch Stability	Vcc = 36V, Pin = 3.0W	F = 2.7, 2.8, 2.9 GHz	VSWR-S	-	1.5:1	-

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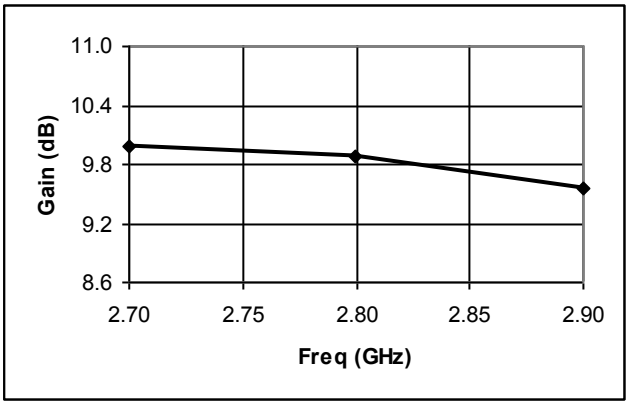
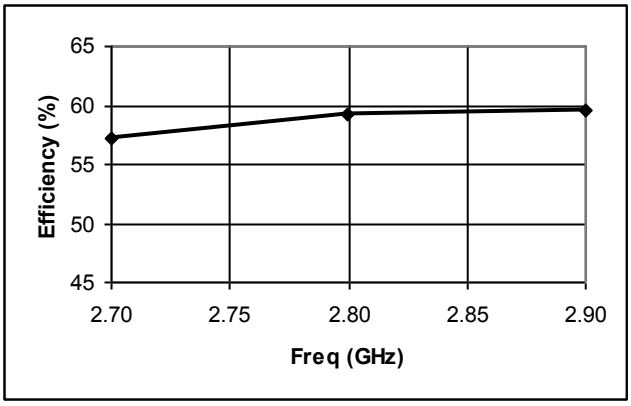
Typical RF Performance

Freq. (GHz)	Pin (W)	Pout (W)	Gain (dB)	Ic (A)	Eff (%)	RL (dB)	VSWR-S (1.5:1)	VSWR-T (3:1)
2.7	3.0	29.9	9.98	1.45	57.2	-13.9	S	P
2.8	3.0	29.1	9.87	1.37	59.2	-19.6	S	P
2.9	3.0	27.1	9.56	1.26	59.6	-19.3	S	P

Gain vs. Frequency

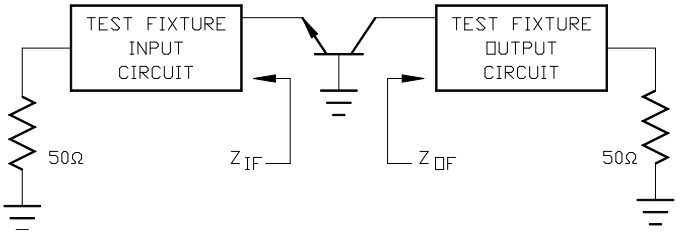


Collector Efficiency vs. Frequency



RF Test Fixture Impedance

F (GHz)	Z_{IF} (Ω)	Z_{OF} (Ω)
2.7	38.0 - j14.4	17.1 - j8.7
2.8	35.0 - j16.3	15.0 - j8.7
2.9	33.0 - j17.8	13.3 - j8.3



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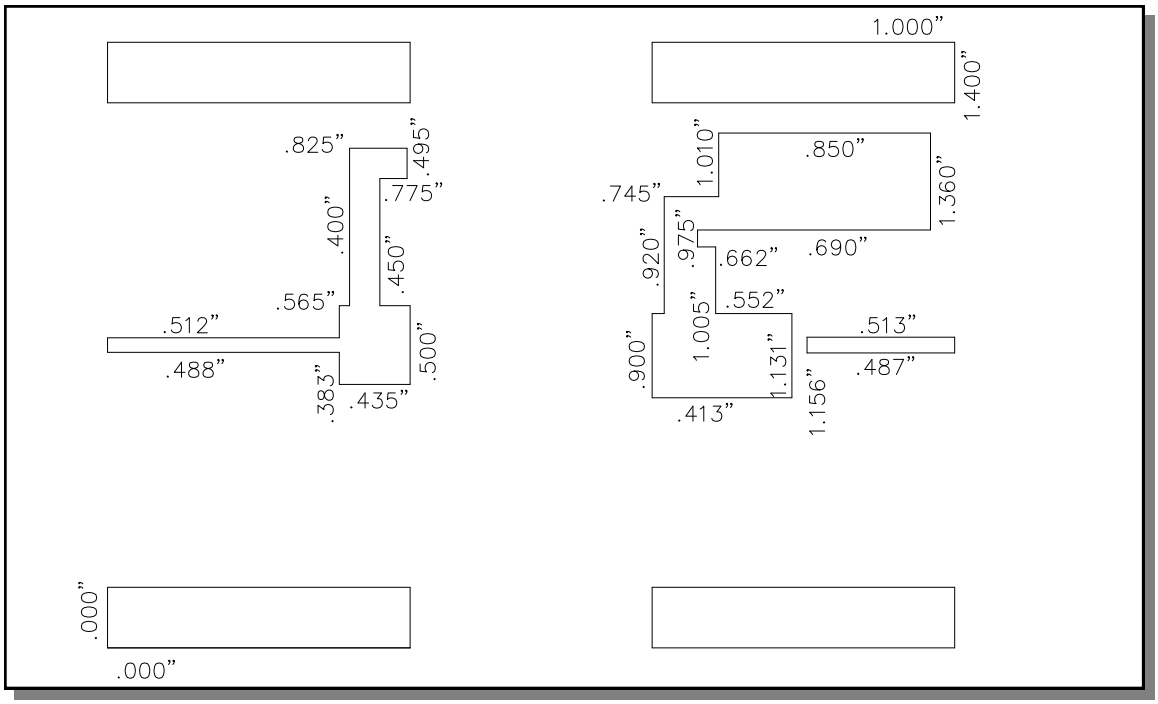
Test Fixture Circuit Dimensions



Test Fixture Assembly