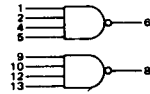
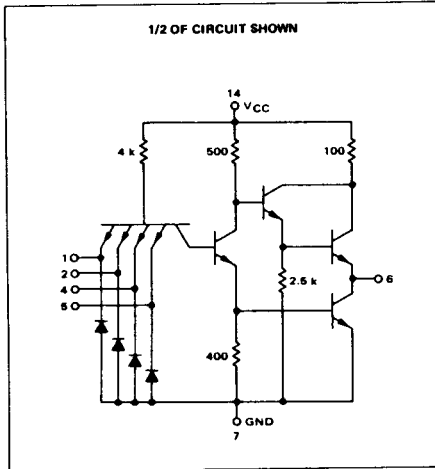


MTTL MC7400P series  
MTTL MC5400L/7400L series

DUAL 4-INPUT "NAND" BUFFER

MC5440L\*  
MC7440P,L\*

This device consists of two 4-input NAND power gate circuits. Each gate is designed for driving high fan-out loads (30).



Positive Logic:  $6 = 1 + 2 + 4 + 5$   
Negative Logic:  $6 = 1 + 2 + 4 + 5$

Input Loading Factor = 1  
Output Loading Factor = 30

Total Power Dissipation = 60 mW typ/pkg  
Propagation Delay Time = 13 ns typ

\*L suffix = TO-116 ceramic package (Case 632)  
P suffix = TO-116 plastic package (Case 606)  
See General Information section for package outline dimensions.

SWITCHING TIME TEST CIRCUIT

VOLTAGE WAVEFORMS AND DEFINITIONS

