

IPS7091(G)(S)PbF

INTELLIGENT POWER HIGH SIDE SWITCH

Features

- Over temperature shutdown (with auto-restart)
- Short circuit protection (current limit)
- Active clamp
- Open load detection
- Logic ground isolated from power ground
- ESD protection
- Ground loss protection
- Status feedback

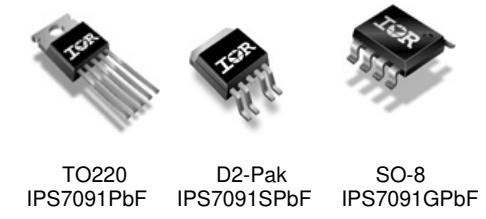
Product Summary

Rds(on)	120mΩ max.
Vclamp	70V
I Limit	5A (typ.)
Open load	3V

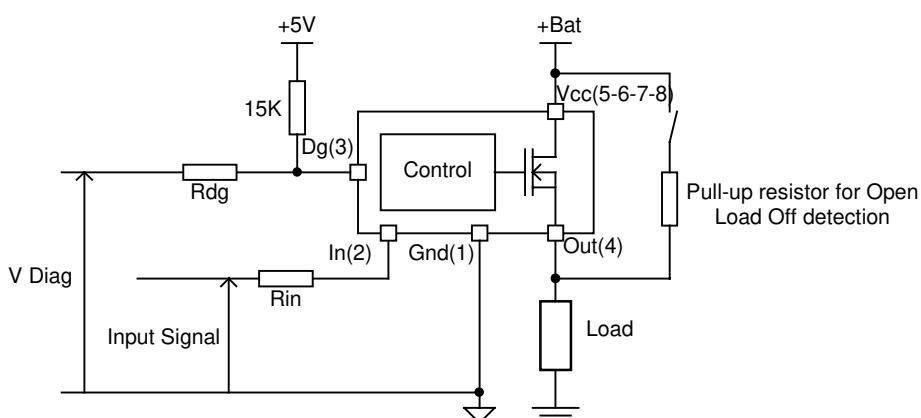
Description

The IPS7091(G)(S)PbF is a five terminal Intelligent Power Switch (IPS) with built in short circuit, over-temperature, ESD protection, inductive load capability and diagnostic feedback. The output current is limited at I_{lim} value. Current limitation is activated until the thermal protection acts. The over-temperature protection turns off the device if the junction temperature exceeds $T_{shutdown}$. It will automatically restart after the junction has cooled 7°C below $T_{shutdown}$. A diagnostic pin is provided for status feedback of short circuit, over-temperature and open load detection. The double level shifter circuitry allows large offsets between the logic ground and the load.

Package



Typical Connection



Absolute Maximum Ratings

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. All voltage parameters are referenced to Ground lead. (T_{ambient}=25°C unless otherwise specified).

Symbol	Parameter	Min.	Max.	Units
V _{out}	Maximum output voltage	V _{cc} -65	V _{cc} +0.3	V
V _{offset}	Maximum logic ground to load ground offset	V _{cc} -65	V _{cc} +0.3	
V _{in}	Maximum input voltage	-0.3	5.5	
V _{cc} max.	Maximum V _{cc} voltage	—	65	
V _{cc} cont.	Maximum continuous V _{cc} voltage	—	35	
I _{IN} max.	Maximum IN current	-1	10	mA
I _{DG} max.	Maximum diagnostic output current	-1	10	
V _{DG}	Maximum diagnostic output voltage	-0.3	5.5	V
P _d	Maximum power dissipation (internally limited by thermal protection) R _{th} =100°C/W	—	1.25	W
I _{SD} cont.	Maximum continuous diode current (R _{th} =100°C/W)	—	1.8	A
ESD1	Electrostatic discharge voltage (Human body) 100pF, 1500Ω	—	4	kV
ESD2	Electrostatic discharge voltage (Machine Model) C=200pF, R=0Ω, L=10μH	—	0.5	
T _j max.	Max. storage & operating temperature junction temperature	-40	+150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Units
R _{th1}	Thermal resistance junction to ambient SO8 std. footprint	100	—	°C/W
R _{th1}	Thermal resistance junction to ambient TO220 free air	60	—	
R _{th1}	Thermal resistance junction to ambient D2Pak std. footprint	60	—	
R _{th2}	Thermal resistance junction to ambient D2Pak 1" sqrt. footprint	40	—	
R _{th3}	Thermal resistance junction to case D2pak/TO220	4	—	

Recommended Operating Conditions

These values are given for a quick design. For operation outside these conditions, please consult the application notes.

Symbol	Parameter	Min.	Max.	Units
V _{IH}	High level input voltage	4	5.5	V
V _{IL}	Low level input voltage	-0.3	0.9	
I _{OUT}	Continuous drain current, T _{amb} =85°C, T _j =125°C, V _{in} =5V, R _{th} =100°C/W	—	1.5	
R _{IN}	Recommended resistor in series with IN pin	10	20	kΩ
R _{DGS}	Recommended resistor in series with DG pin	10	20	
R _{OL}	Recommended pull-up resistor for open load detection	5	100	

Protection Characteristics

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
Ilim	Internal current limit	2	5	8	A	Vout=0V
Tsd+	Over temperature high threshold	150 ⁽¹⁾	165	—	°C	See Fig. 2
Tsd-	Over temperature low threshold	—	158	—		
Vsc	Short-circuit detection voltage ⁽²⁾	2	3	4	V	
Vopen load	Open load detection threshold	2	3	4		

⁽¹⁾ Guaranteed by design

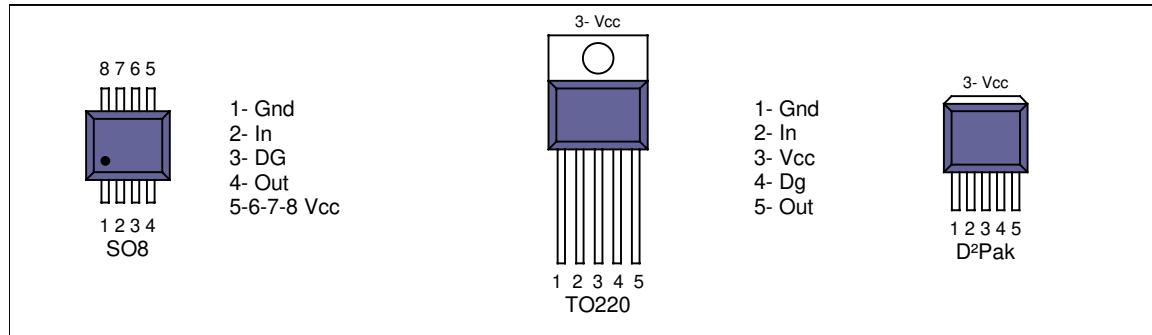
⁽²⁾ Reference to Vcc

Truth Table

Operating Conditions	IN	OUT	DG pin
Normal	H	H	H
Normal	L	L	L
Open Load	H	H	H
Open Load ⁽³⁾	L	H	H
Short circuit to Gnd	H	L (limiting)	L
Short circuit to Gnd	L	L	L
Over-temperature	H	L (cycling)	L
Over-temperature	L	L	L

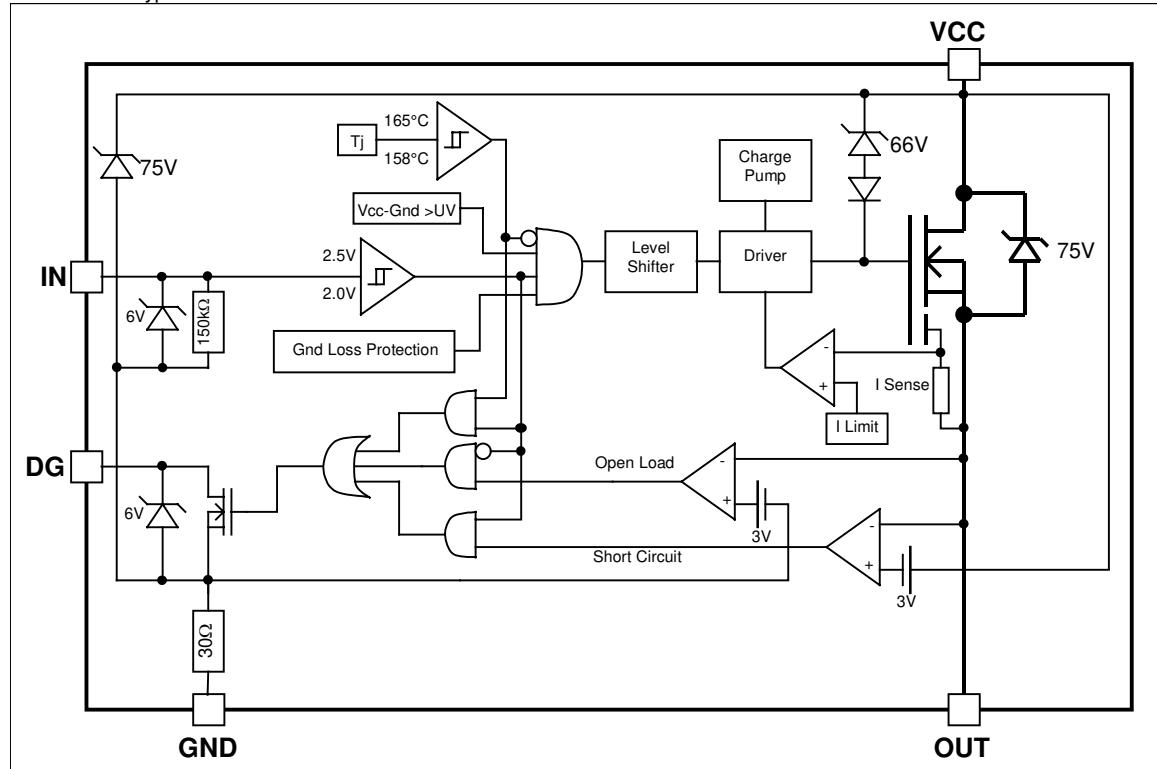
⁽³⁾ With a pull-up resistor connected between the output and Vcc.

Lead Assignments



Functional Block Diagram

All values are typical



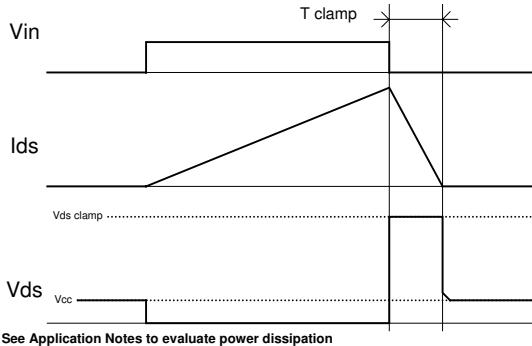


Figure 1 – Active clamp waveforms

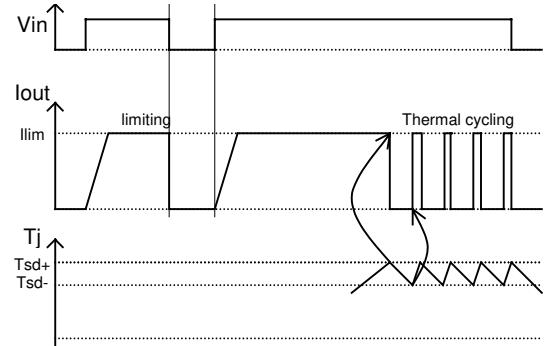


Figure 2 – Protection timing diagram

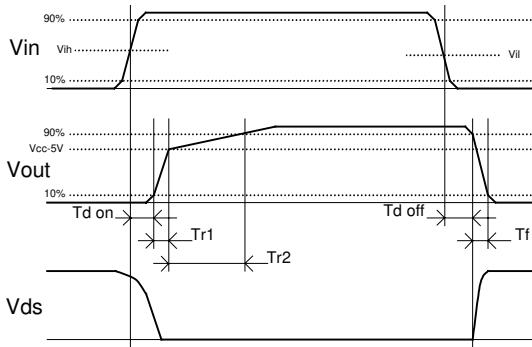


Figure 3 – Switching times definition

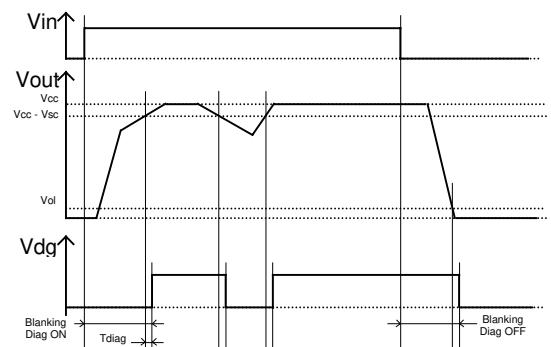


Figure 4 – Diagnostic delay definition

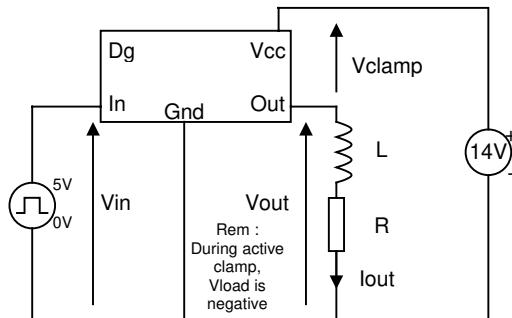


Figure 5 – Active clamp test circuit

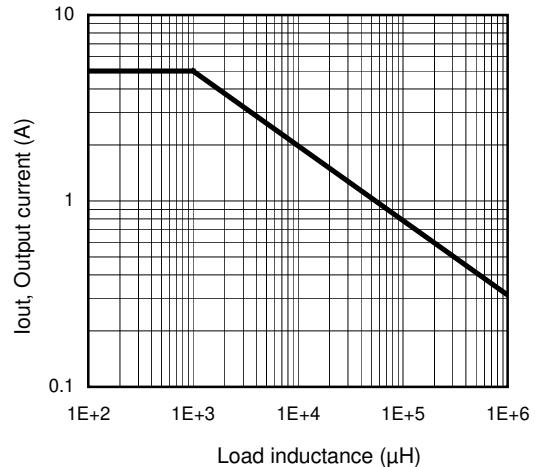


Figure 6 – Max. Output current (A) Vs Load inductance (μH)

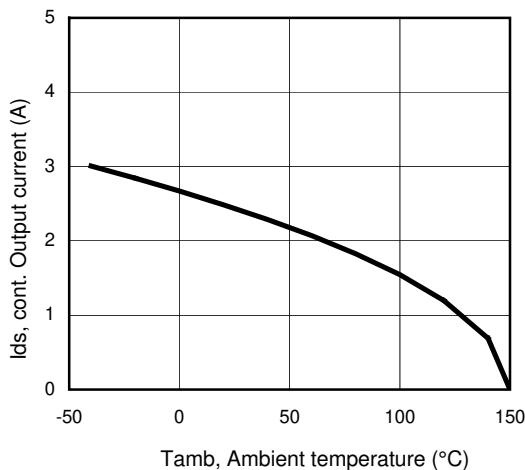


Figure 7 – Max. output current (A) Vs Ambient temperature (°C) $R_{th}=100\text{ }^{\circ}\text{C/W}$

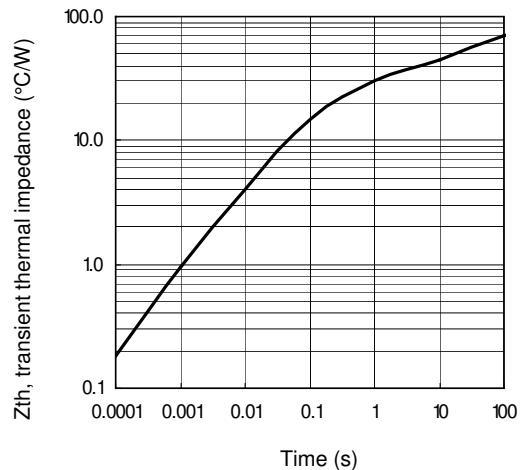
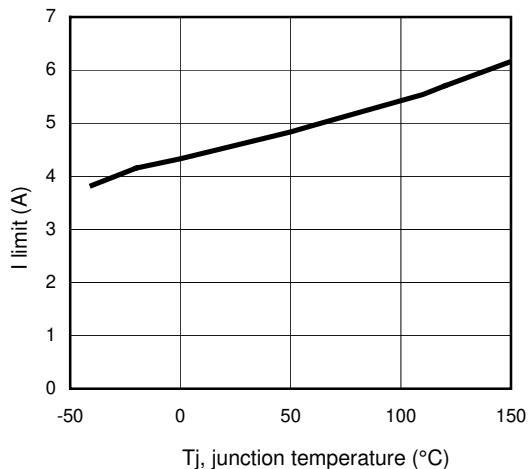
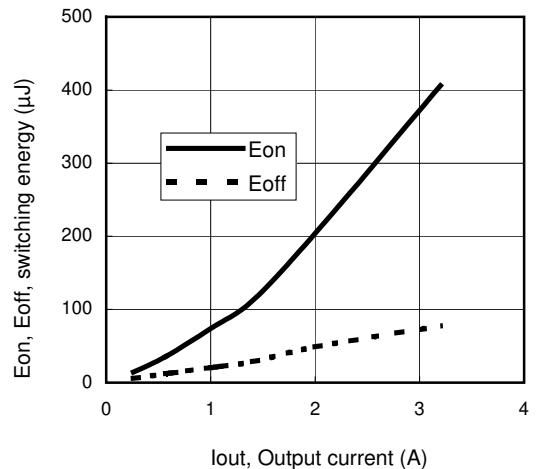


Figure 8 – Transient thermal impedance (°C/W) Vs time (s)



**Figure 9 –I limit (A)
Vs junction temperature (°C)**



**Figure 10 – Switching energy (μJ)
Vs Output current (A)**

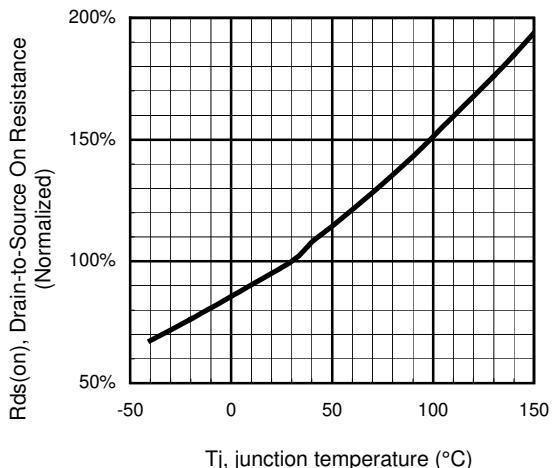
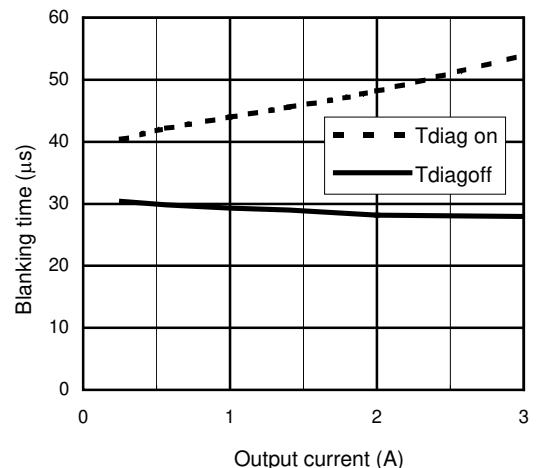


Figure 11 - Normalized R_{ds(on)} (%) Vs T_j (°C)



**Figure 12 – Diagnosis Blanking time (μs)
Vs Output current (A)**

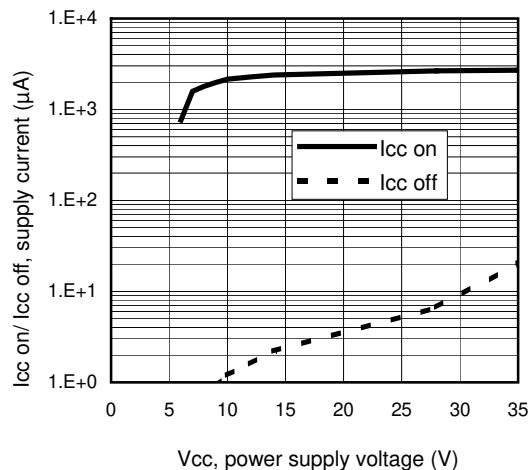


Figure 13 – Icc on/ Icc off (μA) Vs V_{CC} (V)

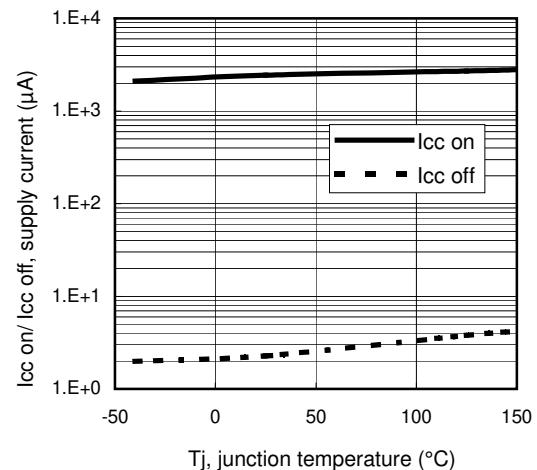
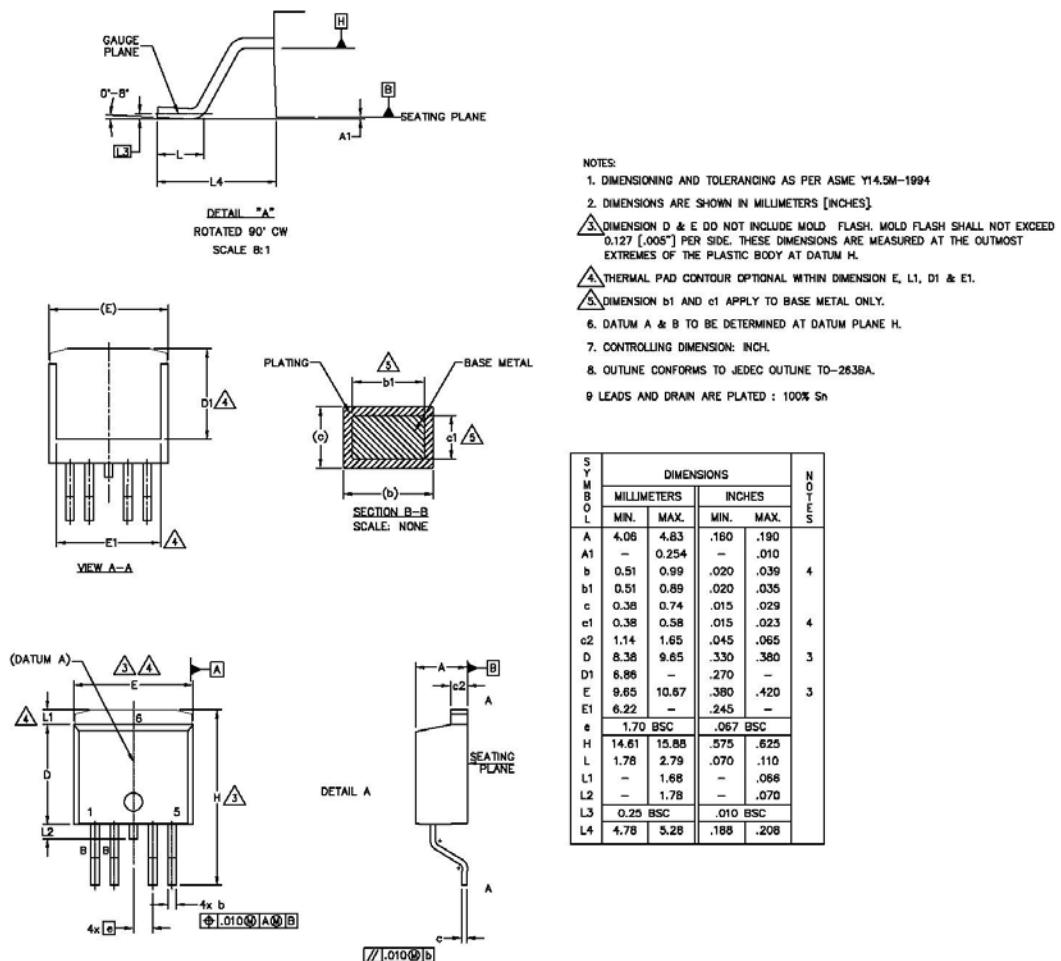
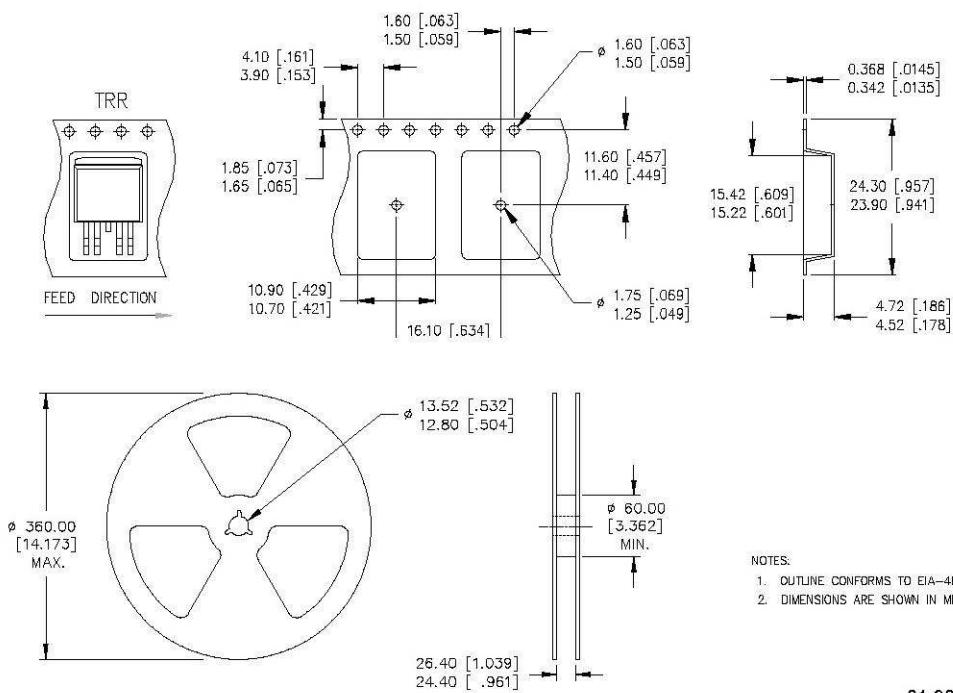


Figure 14 – Icc on/ Icc off (μA) Vs T_J (°C)

Case outline – D²Pak



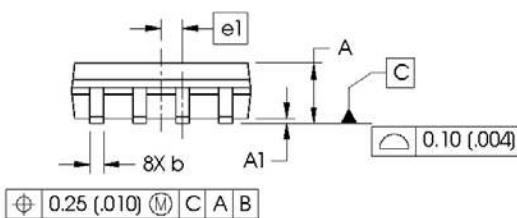
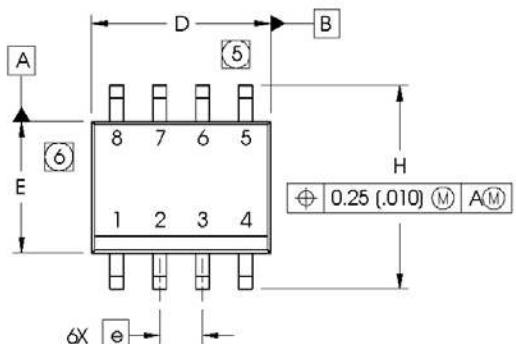
Tape and reel – D²Pak



01-3071 00 / 01-3072 00

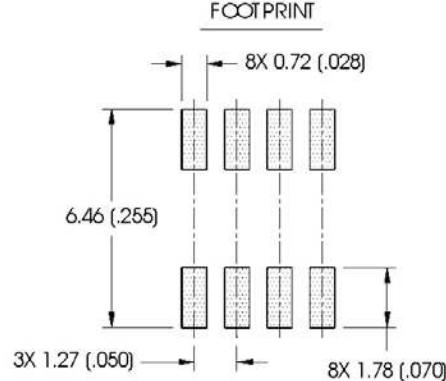
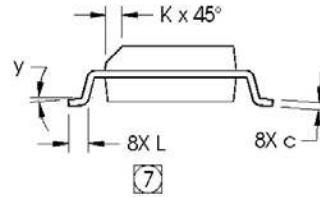
Case Outline - SO-8

Dimensions are shown in millimeters (inches)



(6) .025 (.010) C A B

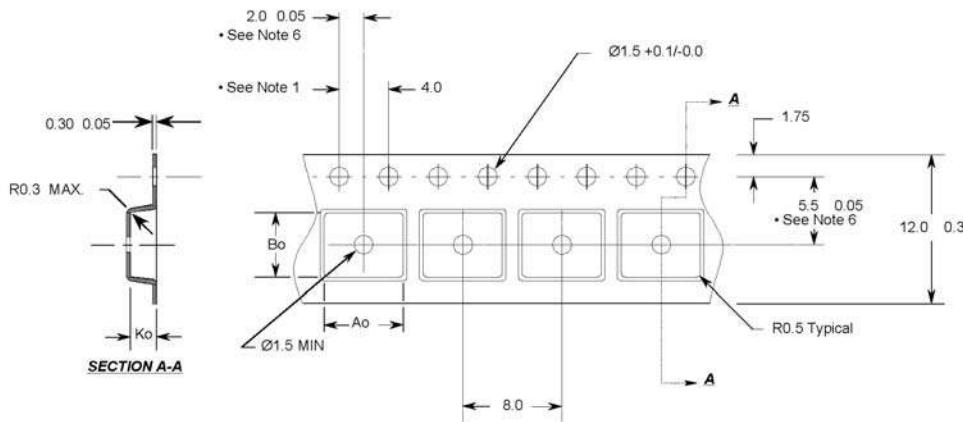
DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.0532	.0688	1.35	1.75
A1	.0040	.0098	0.10	0.25
b	.013	.020	0.33	0.51
c	.0075	.0098	0.19	0.25
D	.189	.1968	4.80	5.00
E	.1497	.1574	3.80	4.00
e	.050 BASIC		1.27 BASIC	
e1	.025 BASIC		0.635 BASIC	
H	.2284	.2440	5.80	6.20
K	.0099	.0196	0.25	0.50
L	.016	.050	0.40	1.27
Y	0°	8°	0°	8°



NOTES:

1. DIMENSIONING & TOLERANCING PER ASME Y14.5M-1994.
2. CONTROLLING DIMENSION: MILLIMETER
3. DIMENSIONS ARE SHOWN IN MILLIMETERS (INCHES).
4. OUTLINE CONFORMS TO JEDEC OUTLINE MS-012AA.
- (5) DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS.
MOLD PROTRUSIONS NOT TO EXCEED 0.15 (.006).
- (6) DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS.
MOLD PROTRUSIONS NOT TO EXCEED 0.25 (.010).
- (7) DIMENSION IS THE LENGTH OF LEAD FOR SOLDERING TO
A SUBSTRATE.

Tape & Reel - SO-8



Notes:

1. 10 sprocket hole pitch cumulative tolerance 0.2
2. Camber not to exceed 1mm in 100mm
3. Material: Black Conductive Advantek Polystyrene
4. Ao and Bo measured on a plane 0.3mm above the bottom of the pocket
5. Ko measured from a plane on the inside bottom of the pocket to the top surface of the carrier.
6. Pocket position relative to sprocket hole measured as true position of pocket, not pocket hole.

$Ao = 6.4 \text{ mm}$
 $Bo = 5.2 \text{ mm}$
 $Ko = 2.1 \text{ mm}$

- All Dimensions in Millimeters -

IR WORLD HEADQUARTERS: 233 Kansas St., El Segundo, California 90245 Tel: (310) 252-7105
 Data and specifications subject to change without notice.
 TO220 and D2PaK are MSL1 qualified. SO8 is MSL2 qualified.
This product has been designed and qualified for the Automotive [Q100] market. 09/09/2008

REVISION HISTORY
IPS7091G

Revision No.	Date	PD No. & Rev. Letter	Page No.	Description	Reason of Change
1		60291_B	all	Add new package version : TO220 / D2pak	
2	09/09/08	60291_C	7	Add Rth=100°C/W figure 7	Information missing