

# SIOV metal oxide varistors

# Housed (ThermoFuse) varistors, AdvanceD series

Series/Type:T14 seriesDate:April 2018

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#### ThermoFuse varistors

## Construction

- Round varistor element, leaded
- Coating: epoxy resin, flame-retardant to UL 94 V-0
- Terminals: tinned copper wire, metal compound wire
- Housing: thermoplastic, flame-retardant to UL 94 V-0

### Features

- Wide operating voltage range 130 ... 420 V<sub>RMS</sub>
- Self-protected under abnormal overvoltage conditions
- High-energy AdvanceD series E2

### Approvals

- UL 1449 (file number E321126)
- IEC (certificate number 101-QA-10 IECQ)
- VDE (certificate number 40031102)

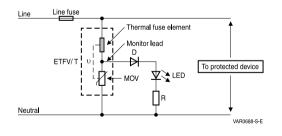
#### Applications

- Houshold appliances
- Power supply units
- Inverters in solar power systems
- Lighting applications
- Communication and data systems
- Transient voltage surge suppressors (TVSS)
- Electronic metering

### **Delivery mode**

Tray packing

### **Typical applications**



### General technical data

Climatic category	to IEC 60068-1	40/85/56	
Operating temperature	to IEC 61051	-40 + 85	°C
Storage temperature		-40 + 85	°C
Electric strength	to IEC 61051	≥ 2.5	kV <sub>RMS</sub>
Insulation resistance	to IEC 61051	≥ 100	MΩ



### ThermoFuse varistors

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# Electrical specifications and ordering codes

Maximum ratings (T<sub>A</sub> = 85 °C)

Ordering code	Туре	V <sub>RMS</sub>	V <sub>DC</sub>	i <sub>max</sub>	<b>I</b> <sub>n</sub> <sup>1)</sup>	W <sub>max</sub>	P <sub>max</sub>
	(untaped)			(8/20 µs)	(8/20 µs)	(2 ms)	
					15 times		
	SIOV-	V	V	А	Α	J	W
B72214T2131K105	T14K130E2	130	170	6000	3000	50	0.6
B72214T2151K105	T14K150E2	150	200	6000	3000	60	0.6
B72214T2171K105	T14K175E2	175	225	6000	3000	70	0.6
B72214T2231K105	T14K230E2	230	300	6000	3000	90	0.6
B72214T2251K105	T14K250E2	250	320	6000	3000	100	0.6
B72214T2271K105	T14K275E2	275	350	6000	3000	110	0.6
B72214T2301K105	T14K300E2	300	385	6000	3000	125	0.6
B72214T2321K105	T14K320E2	320	420	6000	3000	136	0.6
B72214T2351K105	T14K350E2	350	460	6000	3000	150	0.6
B72214T2381K105	T14K385E2	385	505	6000	3000	165	0.6
B72214T2421K105	T14K420E2	420	560	6000	3000	180	0.6

 $^{1)}$  Note: Nominal discharge current  $I_n$  according to UL 1449, 4th edition.

# Characteristics (T<sub>A</sub> = 25 °C)

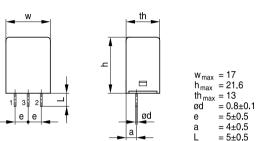
Ordering code	Туре	Vv	$\Delta V_{v}$	V <sub>c,max</sub>	i <sub>c</sub>	C <sub>typ</sub>
•	(untaped)	(1 mA)	(1 mA)	(i <sub>c</sub> )		(1 kHz)
	SIOV-	V	%	V	А	pF
B72214T2131K105	T14K130E2	205	±10	340	50	880
B72214T2151K105	T14K150E2	240	±10	395	50	750
B72214T2171K105	T14K175E2	270	±10	455	50	670
B72214T2231K105	T14K230E2	360	±10	595	50	530
B72214T2251K105	T14K250E2	390	±10	650	50	490
B72214T2271K105	T14K275E2	430	±10	710	50	440
B72214T2301K105	T14K300E2	470	±10	775	50	400
B72214T2321K105	T14K320E2	510	±10	840	50	370
B72214T2351K105	T14K350E2	560	±10	910	50	340
B72214T2381K105	T14K385E2	620	±10	1025	50	315
B72214T2421K105	T14K420E2	680	±10	1120	50	290





#### Dimensional drawings in mm



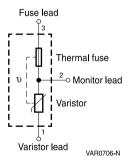


VAR0705-M

## Weight

Nominal diameter	V <sub>RMS</sub>	Weight
14	v 130 420	9 4.1 6.1

# Lead configuration



Please read Cautions and warnings and Important notes at the end of this document.



T14 series



# Housed varistors

## ThermoFuse varistors

Reliability	data
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Test	Test methods/ conditions	Requirement
Varistor voltage	The voltage between two terminals with the specified measuring current applied is called $V_V$ (1 mA <sub>DC</sub> @ 0.2 2 s).	To meet the specified value
Clamping voltage	The maximum voltage between two terminals with the specified standard impulse current (8/20 µs) applied.	To meet the specified value
Endurance at upper category temperature	1000 h at UCT After having continuously applied the maximum allowable AC voltage at UCT $\pm 2$ °C for 1000 h, the specimen shall be stored at room temperature and normal humidity for 1 to 2 h. Thereafter, the change of V <sub>V</sub> shall be measured.	lΔV/V (1 mA)l ≤10%
Surge current derating, 8/20 μs	10 surge currents (8/20 $\mu$ s), unipolar, interval 30 s, amplitude corresponding to derating curve for 10 impulses at 20 $\mu$ s	l∆V/V (1 mA)l ≤10% (measured in direction of surge current) No visible damage
Surge current derating, 2 ms	10 surge currents (2 ms), unipolar, interval 120 s, amplitude corresponding to derating curve for 10 impulses at 2 ms	IΔV/V (1 mA)I ≤10% (measured in direction of surge current) No visible damage
Electric strength	IEC 61051-1, test 4.9.2 Metal balls method, 2500 $V_{RMS}$ , 60 s The varistor is placed in a container holding 1.6 ±0.2 mm diameter metal balls such that only the terminations of the varistor are protruding. The specified voltage shall be applied between both terminals of the specimen connected together and the electrode inserted between the metal balls.	No breakdown





# Housed varistors ThermoFuse varistors

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Test	Test methods/ conditions	Requirement
Climatic sequence	The specimen shall be subjected to: a) dry heat at UCT, 16 h, IEC 60068-2-2, test Ba b) damp heat, 1st cycle: 55 °C, 93% r. H., 24 h, IEC 60068-2-30, test Db. c) cold, LCT, 2 h, IEC 60068-2-1, test Aa. d) damp heat, additional 5 cycles: 55  °C/25  °C, 93%  r. H., 24  h/cycle, IEC 60068-2-30, test Db.	IΔV/V (1 mA)I ≤10% R <sub>ins</sub> ≥100 MΩ
	Then the specimen shall be stored at room temperature and normal humidity for 1 to 2 h. Thereafter, the change of V <sub>v</sub> shall be measured. Thereafter, insulation resis- tance R <sub>ins</sub> shall be measured at V = 500 V.	
Rapid change of temperature	IEC 60068-2-14, test Na, LCT/UCT, dwell time 30 min, 5 cycles	l∆V/V (1 mA)l ≤5% No visible damage
Damp heat, steady state	IEC 60068-2-78, test Ca The specimen shall be subjected to $40 \pm 2 ^{\circ}$ C, 90 to 95% r. H. for 56 days without load / with 10% of the maxi- mum continuous DC operating voltage V <sub>DC</sub> . Then stored at room temperature and normal humidity for 1 to 2 h. Thereafter, the change of V <sub>V</sub> shall be measured. Thereafter, insulation resis- tance R <sub>ins</sub> shall be measured at V = 500 V (insulated varistors only).	İΔV/V (1 mA)l ≤10% R <sub>ins</sub> ≥100 MΩ

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Test	Test methods/ conditions	Requirement
Solderability	IEC 60068-2-20, test Ta, method 1 with modified conditions for lead-free solder alloys: 245 °C, 3 s: After dipping the terminals to a depth of approximately 3 mm from the body in a soldering bath of 245 °C for 3 s, the terminals shall be visually examined.	The inspection must be carried out under adequate light with normal eyesight or with the assistance of a magnifier capable of giving a magnification of 4 to 10 times. The dipped surface must be covered with a smooth and bright solder coating with no more than small amounts of scattered imperfections such as pinholes or un-wetted or de-wetted areas. These imperfections must not be concentrated in one area.
Resistance to soldering heat	IEC 60068-2-20, test Tb, method 1A, 260 °C, 10 s: Each lead shall be dipped into a solder bath having a temperature of $260 \pm 5$ °C to a point 2.0 to 2.5 mm from the body of the specimen, be held there for $10 \pm 1$ s and then be stored at room temperature and normal humidity for 1 to 2 h. The change of V <sub>v</sub> shall be measured and the specimen shall be visually examined.	l∆V/V (1 mA)l ≤5% No visible damage
Tensile strength	IEC 60068-2-21, test Ua1 After gradually applying the force specified below and keeping the unit fixed for 10 s, the terminal shall be visually examined for any damage. Force for wire diameter: 0.6 mm = 10 N 0.8 mm = 10 N 1.0 mm = 20 N	I∆V/V (1 mA)I ≤5% No break of solder joint, no wire break





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Test	Test methods/ c	onditions		Re	quirement	
Vibration	IEC 60068-2-6,	IEC 60068-2-6, test Fc, method B4			//V (1 mA)l ≤5%	
		Frequency range: 10 55 Hz			No visible damage	
	Amplitude:0.75 mm or 98 m/s²					
	Duration: $6 h (3 \cdot 2 h)$					
	Pulse: sine wave					
		After repeatedly applying a single				
		harmonic vibration according to the table				
	above.					
	The change of \					
	the specimen sh		lly examined.			
Bump	IEC 60068-2-29			IΔV	//V (1 mA)l ≤5%	
		Pulse duration: 6 ms			visible damage	
		Max. acceleration: 400 m/s <sup>2</sup>				
	Number of bum					
<u> </u>	Pulse:	half sine		-		
Fire hazard	IEC 60695-11-5	`	ne test)	5 s max.		
	Severity: vertica				<u></u>	
Abnormal	The device is de				None of the following	
overvoltage test	limited current abnormal overvoltage				enomena shall be observed,	
		condition, outlined in section 39.4 of			this specimen will be judged	
	UL 1449, 4 <sup>th</sup> edition.			as 1.	failed part:	
		Detailed test voltage applied onto the			,	
		device for different types as in the			metal, glowing or flaming	
	following table:				particles through any	
	Туре	Device	Test		openings (pre-existing or	
		rating	voltage		created as a result of the	
		V <sub>RMS</sub>	V <sub>RMS</sub>	2.	test) in the product.	
	T14K130E2	130	260	2.	Charring, glowing, or flaming of the supporting	
	T14K150E2	150	300		surface, tissue paper, or	
	T14K175E2	175	350		cheesecloth.	
	T14K230E2	230	415	3.		
	T14K250E2	250	500	4.	0	
	T14K275E2	275	480	Τ.	in the enclosure that	
	T14K300E2	300	600		result in accessibility of	
	T14K320E2	320	600		live parts, when	
	T14K350E2	350	600		evaluated in accordance	
	T14K385E2	385	600		with accessibility of live	
	T14K420E2	420	600		parts test in section 58.2	
					of UL1449, 4 <sup>th</sup> edition.	

# Note:

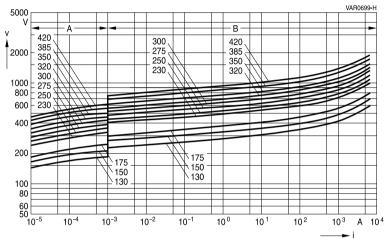
UCT = Upper category temperature, LCT = Lower category temperature,  $R_{ins}$  = Insulation resistance. All electrical tests should be performed between pin 1 and pin 3.





## v/i characteristics

v = f (i) for explanation of the characteristics refer to "General technical information", chapter 1.6.3 A = Leakage current, B = Protection level } for worst-case varistor tolerances

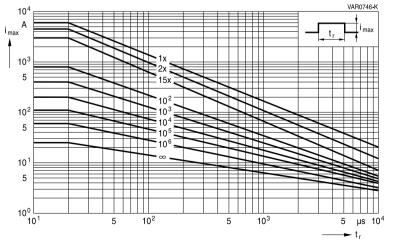




# **Derating curves**

Maximum surge current  $i_{max} = f(t_r, pulse train)$ 

For explanation of the derating curves refer to "General technical information", section 1.8.1



SIOV-T14 ... E2

Please read Cautions and warnings and Important notes at the end of this document.



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## Cautions and warnings

### General

- EPCOS metal oxide varistors are designed for specific applications and should not be used for purposes not identified in our specifications, application notes and data books unless otherwise agreed with EPCOS during the design-in-phase.
- 2. Ensure suitability of SIOVs through reliability testing during the design-in phase. SIOVs should be evaluated taking into consideration worst-case conditions.
- 3. For applications of SIOVs in line-to-ground circuits based on various international and local standards there are restrictions existing or additional safety measures required.

### Storage

- 1. Store SIOVs only in original packaging. Do not open the package prior to processing.
- Recommended storage conditions in original packaging: Storage temperature: -25 °C ... +45 °C, Relative humidity: <75% annual average, <95% on maximum 30 days a year. Dew precipitation: is to be avoided.
- 3. Avoid contamination of an SIOV's during storage, handling and processing.
- 4. Avoid storage of SIOVs in harmful environments that can affect the function during long-term operation (examples given under operation precautions).
- 5. The SIOV type series should be soldered after shipment from EPCOS within the time specified:

SIOV-S, -Q, -LS, -B, -SNF 24 months ETFV/ T series, -CU 12 months.

## Handling

- 1. SIOVs must not be dropped.
- 2. Components must not be touched with bare hands. Gloves are recommended.
- 3. Avoid contamination of the surface of SIOV electrodes during handling, be careful of the sharp edge of SIOV electrodes.

### Soldering (where applicable)

- 1. Use rosin-type flux or non-activated flux.
- 2. Insufficient preheating may cause ceramic cracks.
- 3. Rapid cooling by dipping in solvent is not recommended.
- 4. Complete removal of flux is recommended.
- Temperatures of all preheat stages and the solder bath must be strictly controlled especially for T series (T14 and T20).



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### Mounting

- 1. Potting, sealing or adhesive compounds can produce chemical reactions in the SIOV ceramic that will degrade the component's electrical characteristics.
- 2. Overloading SIOVs may result in ruptured packages and expulsion of hot materials. For this reason SIOVs should be physically shielded from adjacent components.

## Operation

- 1. Use SIOVs only within the specified temperature operating range.
- 2. Use SIOVs only within the specified voltage and current ranges.
- Environmental conditions must not harm SIOVs. Use SIOVs only in normal atmospheric conditions. Avoid use in deoxidizing gases (chlorine gas, hydrogen sulfide gas, ammonia gas, sulfuric acid gas etc), corrosive agents, humid or salty conditions.Contact with any liquids and solvents should be prevented.

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### Symbols and terms

Symbol	Term
С	Capacitance
C <sub>typ</sub>	Typical capacitance
i	Current
i <sub>c</sub>	Current at which $V_{c, max}$ is measured
I <sub>leak</sub>	Leakage current
i <sub>max</sub>	Maximum surge current (also termed peak current)
I <sub>max</sub>	Maximum discharge current
l <sub>n</sub>	Nominal discharge current to UL 1449
LCT	Lower category temperature
L <sub>typ</sub>	Typical inductance
P <sub>max</sub>	Maximum average power dissipation
R <sub>ins</sub>	Insulation resistance
R <sub>min</sub>	Minimum resistance
T <sub>A</sub>	Ambient temperature
t <sub>r</sub>	Duration of equivalent rectangular wave
UCT	Upper category temperature
v	Voltage
V <sub>clamp</sub>	Clamping voltage
V <sub>c, max</sub>	Maximum clamping voltage at specified current $i_{\rm c}$
V <sub>DC</sub>	DC operating voltage
$V_{jump}$	Maximum jump start voltage
V <sub>max</sub>	Maximum voltage
V <sub>op</sub>	Operating voltage
V <sub>RMS</sub>	AC operating voltage, root-mean-square value
$V_{\text{RMS, op, max}}$	Root-mean-square value of max. DC operating voltage incl. ripple current
V <sub>surge</sub>	Super imposed surge voltage
Vv	Varistor voltage
$\Delta V_V$	Tolerance of varistor voltage
W <sub>LD</sub>	Maximum load dump
W <sub>max</sub>	Maximum energy absorption
e	Lead spacing
	Leau spacing

All dimensions are given in mm.

The commas used in numerical values denote decimal points.



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