IHCL-4040DZ-5A 2.2 μH

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Vishay Dale

Low-Profile, High-Current Coupled Inductor



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DESIGN SUPPORT TOOLS

Models Available

Design Tools Available

STANDARD ELECTRICAL SPECIFICATIONS											
L ₀ INDUCTANCE ± 20 % AT 100 kHz, 0.25 V, 0 A (μH)	DCR NOM. 25 °C (mΩ)			SATURATION CURRENT DC TYP. (A) ⁽²⁾							
2.2	16.3	17.5	8.4	11.0							
2.2	16.3	17.5	8.0	15.0							
9.0	32.6	35.0	5.4	5.5							
0.1	32.6 35.0		5.4	See note ⁽³⁾							
ommon Mode (1-3 and 2.2 4 shorted)		8.7	13.3	12.5							
0.1	8.2	8.7	13.3	See note ⁽³⁾							
	L ₀ INDUCTANCE ± 20 % AT 100 kHz, 0.25 V, 0 A (μH) 2.2 2.2 9.0 0.1 2.2	L₀ DCR INDUCTANCE DCR ± 20 % AT D0 kHz, 100 kHz, 25 °C (µH) 25 °C 2.2 16.3 2.2 16.3 9.0 32.6 0.1 32.6 2.2 8.2	L₀ DCR DCR ± 20 % AT DCR DCR 100 kHz, 0.25 V, 0 A 25 °C (mΩ) 2.2 16.3 17.5 9.0 32.6 35.0 0.1 32.6 35.0 2.2 8.2 8.7	L₀ L₀ HEAT iNDUCTANCE DCR DCR MAX. ± 20 % AT DCR DCR MAX. 100 kHz, 25 °C (mΩ) (mΩ) (MAX. 0.25 V, 0 A 16.3 17.5 8.4 2.2 16.3 17.5 8.0 9.0 32.6 35.0 5.4 0.1 32.6 35.0 5.4 2.2 8.2 8.7 13.3							

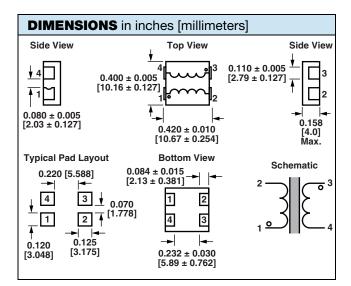
- All test data is referenced to 25 °C ambient
- Operating temperature range -55 °C to +155 °C
- The part temperature (ambient + temp. rise) should not exceed 155 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application
- Rated operating voltage (across inductor) = 50 V
- SEPIC operation can generate up to 2x the input or output voltage across the inductor. Please limit V_{IN} and V_{OUT} to 25 V max. for SEPIC operation
- (1) DC current (A) that will cause an approximate ΔT of 40 °C
- (2) DC current (A) that will cause L₀ to drop approximately 20 %
- (3) In this configuration, current flowing opposite directions through coils cancels and the 0.1 µH inductance is very stable with varying current. Observe the heat rating current to avoid excessive temperature rise in this configuration

FEATURES

- High temperature, up to 155 °C
- Shielded construction
- Frequency range up to 5.0 MHz
- Lowest DCR/µH in this package size
- Handles high transient current spikes without saturation
- Ultra low buzz noise, due to composite construction
- Coupling is > 90 % optimized for SEPIC converters
- AEC-Q200 gualified
- Patent pending
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

APPLICATIONS

- SEPIC converters
- DC/DC converters
- · Common mode applications
- LED lighting



DESCRIPTION	N								
IHCL-4040DZ-5A	2.2 µH		± 20 %		ER		e3		
MODEL	INDUCTANCE	VALUE	INDUCTANCE TOLERANCE		PACKAGE CO	ODE JEDEC [®] LEA	JEDEC [®] LEAD (Pb)-FREE STANDARD		
GLOBAL PAR	T NUMBER								
ІНС	L 4	0	4 0	DZ	ER	2 R :	2 M	5 A	
PRODUCT FAI	MILY		SIZE		PACKAGE CODE	INDUCTANCE VALUE	TOL.	SERIES	
Revision: 09-Feb-18				1			Document	Number: 34355	

RoHS COMPLIANT

HALOGEN

FREE

GREEN

(5-2008)

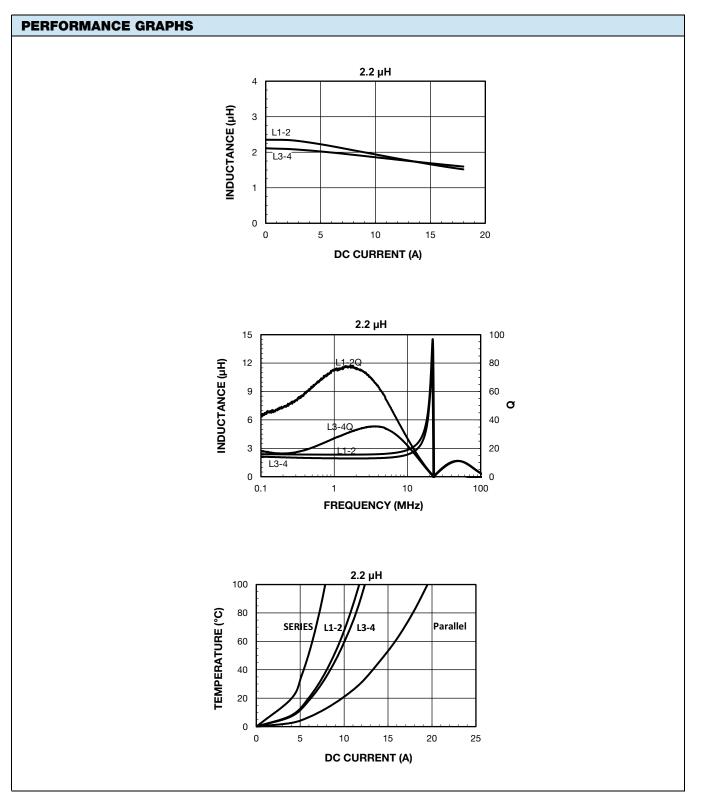
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