N0600N MOS FIELD EFFECT TRANSISTOR

Description

The N0600N is N-channel MOS Field Effect Transistor designed for high current switching applications.

Features

- Low on-state resistance
 - ---- $R_{DS(on)1} = 25 \text{ m}\Omega \text{ MAX.} (V_{GS} = 10 \text{ V}, I_D = 15 \text{ A})$
 - ---- $R_{DS(on)2} = 36 \text{ m}\Omega \text{ MAX.} (V_{GS} = 4.5 \text{ V}, I_D = 15 \text{ A})$
- Low input capacitance
 - -- C_{iss} = 1380 pF TYP. (V_{DS} = 10 V, V_{GS} = 0 V)

Ordering Information

Part No.	Lead Plating	Packing	Package
N0600N-S17-AY *1	Pure Sn (Tin)	Tube 50p/tube	Isolated TO-220 typ. 2.2 g

Note: *1. Pb-free (This product does not contain Pb in the external electrode and other parts.)

Absolute Maximum Ratings ($T_A = 25^{\circ}C$)

Item	Symbol	Ratings	Unit
Drain to Source Voltage ($V_{GS} = 0 V$)	V _{DSS}	60	V
Gate to Source Voltage (V _{DS} = 0 V)	V _{GSS}	±20	V
Drain Current (DC)	I _{D(DC)}	±30	A
Drain Current (pulse) *1	I _{D(pulse)}	±60	A
Total Power Dissipation ($T_C = 25^{\circ}C$)	P _{T1}	20	W
Total Power Dissipation (T _A = 25°C)	P _{T2}	2.0	W
Channel Temperature	T _{ch}	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C
Single Avalanche Current *2	I _{AS}	9.2	A
Single Avalanche Energy*2	E _{AS}	12.5	mJ

Thermal Resistance

Channel to Case (Drain) Thermal Resistance	R _{th(ch-C)}	6.25	°C/W
Channel to Ambient Thermal Resistance *2	R _{th(ch-A)}	62.5	°C/W

Notes: *1. PW \leq 10 μ s, Duty Cycle \leq 1%

*2. Starting T_{ch} = 25°C, R_G = 25 Ω , V_{DD} = 30 V, V_{GS} = 20 \rightarrow 0 V

Data Sheet



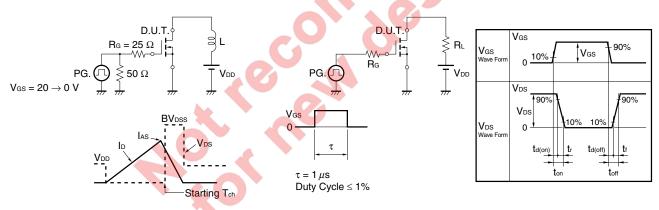
Electrical Characteristics	(T _A = 25°C)
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Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Zero Gate Voltage Drain Current	I _{DSS}			1	μA	V _{DS} = 60 V, V _{GS} = 0 V
Gate Leakage Current	I _{GSS}			±100	nA	V _{GS} = ±20 V, V _{DS} = 0 V
Gate to Source Cut-off Voltage	V _{GS(off)}	1.5	2.0	2.5	V	V _{DS} = 10 V, I _D = 1 mA
Forward Transfer Admittance *1	y _{fs}	4			S	V _{DS} = 10 V, I _D = 15 A
Drain to Source On-state	R _{DS(on)1}		17.5	25	mΩ	V _{GS} = 10 V, I _D = 15 A
Resistance ^{*1}	R _{DS(on)2}		22.3	36	mΩ	V _{GS} = 4.5 V, I _D = 15 A
Input Capacitance	C _{iss}		1380		pF	V _{DS} = 10 V,
Output Capacitance	C _{oss}		186		pF	V _{GS} = 0 V,
Reverse Transfer Capacitance	C _{rss}		109		pF	f = 1 MHz
Turn-on Delay Time	t _{d(on)}		5.7		ns	V _{DD} = 30 V, I _D = 15 A,
Rise Time	tr		6.3		ns	V _{GS} = 10 V,
Turn-off Delay Time	t _{d(off)}		33.2		ns	R _G = 0 Ω
Fall Time	t _f		3.9		ns	
Total Gate Charge	Q _G		29.8		nC	V _{DD} = 48 V,
Gate to Source Charge	Q _{GS}		4.2		nC	V _{GS} = 10 V,
Gate to Drain Charge	Q _{GD}		9.0		nC	I _D = 30 A
Body Diode Forward Voltage *1	V _{F(S-D)}		0.92	1.5	V	I _F = 30A, V _{GS} = 0 V
Reverse Recovery Time	trr		30		ns	I _F = 30 A, V _{GS} = 0 V,
Reverse Recovery Charge	Qrr		39.6		nC	di/dt = 100 A/μs

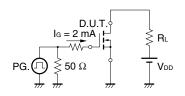
Note: *1. Pulsed

TEST CIRCUIT 1 AVALANCHE CAPABILITY

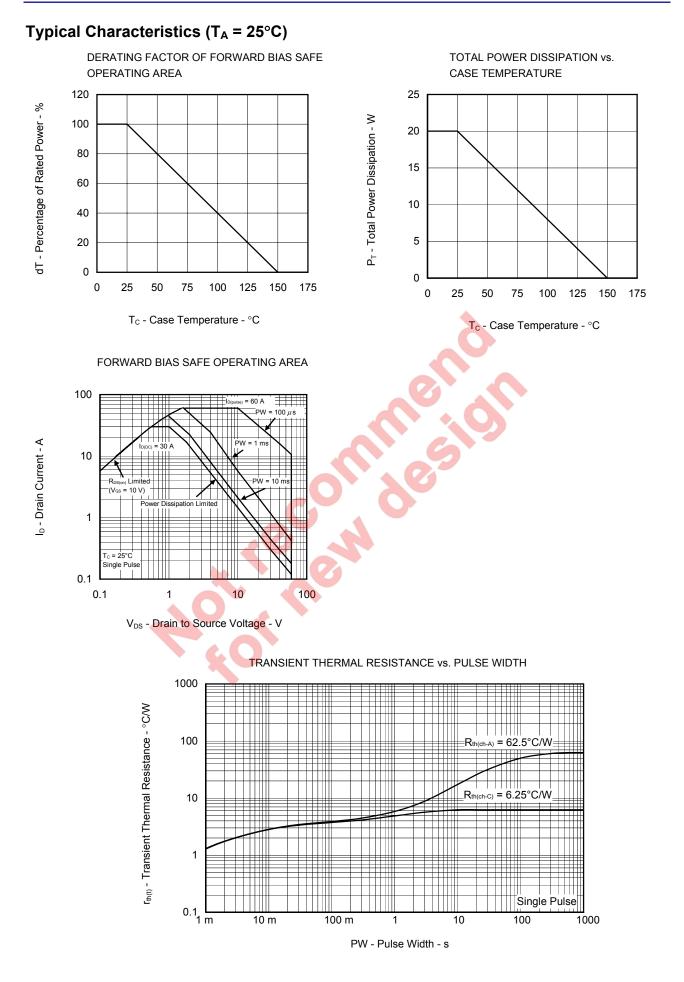
TEST CIRCUIT 2 SWITCHING TIME



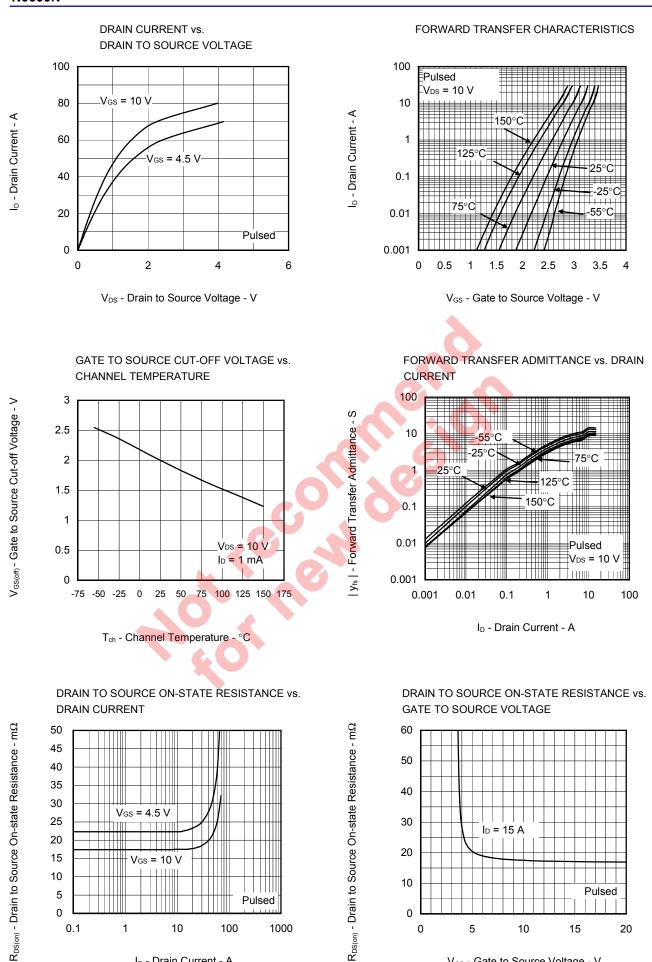
TEST CIRCUIT 3 GATE CHARGE











10

15

0.1

1

10

I_D - Drain Current - A

100

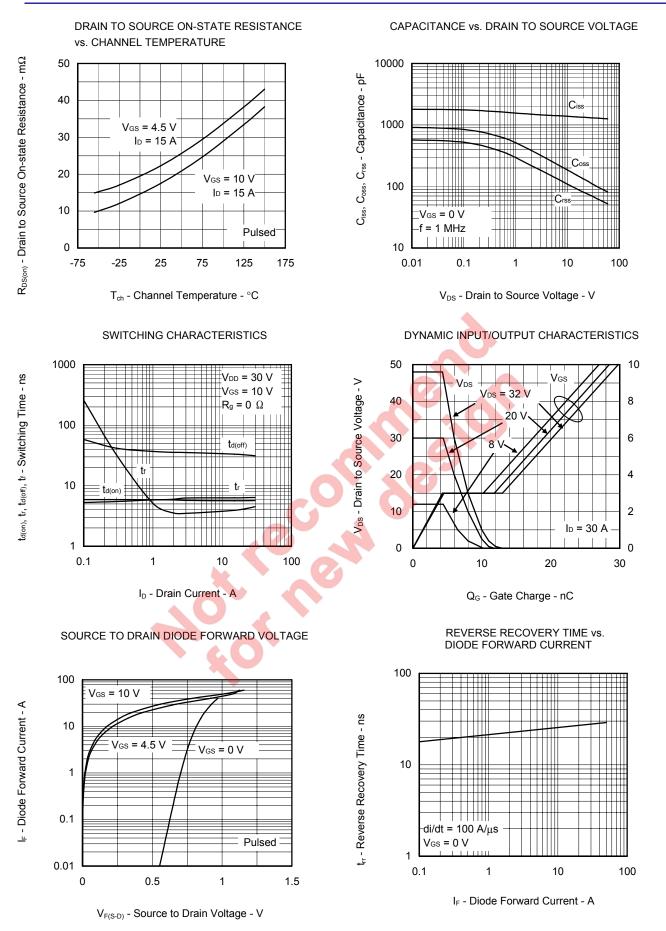
1000



0

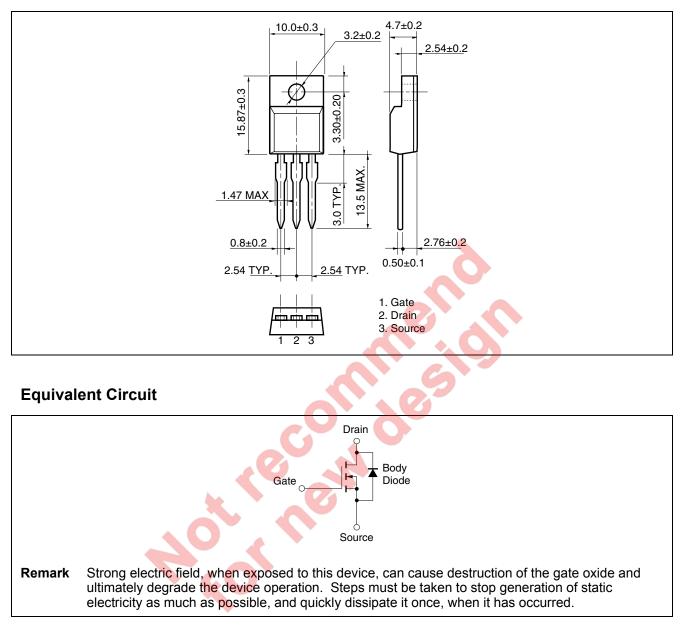
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Package Drawings (Unit: mm)

Isolated TO-220





Revision I	History
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		Description		
Rev.	Date	Page	Summary	
1.00	Jan 25, 2011	-	First Edition Issued	



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