# **N0600N** MOS FIELD EFFECT TRANSISTOR

# Description

The N0600N is N-channel MOS Field Effect Transistor designed for high current switching applications.

# Features

- Low on-state resistance
  - ----  $R_{DS(on)1} = 25 \text{ m}\Omega \text{ MAX.} (V_{GS} = 10 \text{ V}, I_D = 15 \text{ A})$
  - ----  $R_{DS(on)2} = 36 \text{ m}\Omega \text{ MAX.} (V_{GS} = 4.5 \text{ V}, I_D = 15 \text{ A})$
- Low input capacitance
  - -- C<sub>iss</sub> = 1380 pF TYP. (V<sub>DS</sub> = 10 V, V<sub>GS</sub> = 0 V)

# **Ordering Information**

Part No.	Lead Plating	Packing	Package
N0600N-S17-AY *1	Pure Sn (Tin)	Tube 50p/tube	Isolated TO-220 typ. 2.2 g

Note: \*1. Pb-free (This product does not contain Pb in the external electrode and other parts.)

# Absolute Maximum Ratings ( $T_A = 25^{\circ}C$ )

Item	Symbol	Ratings	Unit
Drain to Source Voltage ( $V_{GS} = 0 V$ )	V <sub>DSS</sub>	60	V
Gate to Source Voltage (V <sub>DS</sub> = 0 V)	V <sub>GSS</sub>	±20	V
Drain Current (DC)	I <sub>D(DC)</sub>	±30	A
Drain Current (pulse) *1	I <sub>D(pulse)</sub>	±60	A
Total Power Dissipation ( $T_C = 25^{\circ}C$ )	P <sub>T1</sub>	20	W
Total Power Dissipation (T <sub>A</sub> = 25°C)	P <sub>T2</sub>	2.0	W
Channel Temperature	T <sub>ch</sub>	150	°C
Storage Temperature	T <sub>stg</sub>	-55 to +150	°C
Single Avalanche Current *2	I <sub>AS</sub>	9.2	A
Single Avalanche Energy*2	E <sub>AS</sub>	12.5	mJ

# **Thermal Resistance**

Channel to Case (Drain) Thermal Resistance	R <sub>th(ch-C)</sub>	6.25	°C/W
Channel to Ambient Thermal Resistance *2	R <sub>th(ch-A)</sub>	62.5	°C/W

Notes: \*1. PW  $\leq$  10  $\mu$ s, Duty Cycle  $\leq$  1%

\*2. Starting T<sub>ch</sub> = 25°C, R<sub>G</sub> = 25  $\Omega$ , V<sub>DD</sub> = 30 V, V<sub>GS</sub> = 20  $\rightarrow$  0 V

**Data Sheet** 



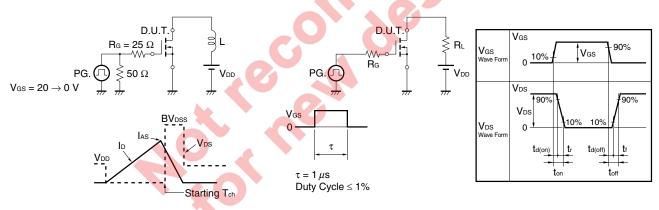
<b>Electrical Characteristics</b>	(T <sub>A</sub> = 25°C)
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Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Zero Gate Voltage Drain Current	I <sub>DSS</sub>			1	μA	V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V
Gate Leakage Current	I <sub>GSS</sub>			±100	nA	V <sub>GS</sub> = ±20 V, V <sub>DS</sub> = 0 V
Gate to Source Cut-off Voltage	V <sub>GS(off)</sub>	1.5	2.0	2.5	V	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA
Forward Transfer Admittance *1	y <sub>fs</sub>	4			S	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 15 A
Drain to Source On-state	R <sub>DS(on)1</sub>		17.5	25	mΩ	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 15 A
Resistance <sup>*1</sup>	R <sub>DS(on)2</sub>		22.3	36	mΩ	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 15 A
Input Capacitance	C <sub>iss</sub>		1380		pF	V <sub>DS</sub> = 10 V,
Output Capacitance	C <sub>oss</sub>		186		pF	V <sub>GS</sub> = 0 V,
Reverse Transfer Capacitance	C <sub>rss</sub>		109		pF	f = 1 MHz
Turn-on Delay Time	t <sub>d(on)</sub>		5.7		ns	V <sub>DD</sub> = 30 V, I <sub>D</sub> = 15 A,
Rise Time	tr		6.3		ns	V <sub>GS</sub> = 10 V,
Turn-off Delay Time	t <sub>d(off)</sub>		33.2		ns	R <sub>G</sub> = 0 Ω
Fall Time	t <sub>f</sub>		3.9		ns	
Total Gate Charge	Q <sub>G</sub>		29.8		nC	V <sub>DD</sub> = 48 V,
Gate to Source Charge	Q <sub>GS</sub>		4.2		nC	V <sub>GS</sub> = 10 V,
Gate to Drain Charge	Q <sub>GD</sub>		9.0		nC	I <sub>D</sub> = 30 A
Body Diode Forward Voltage *1	V <sub>F(S-D)</sub>		0.92	1.5	V	I <sub>F</sub> = 30A, V <sub>GS</sub> = 0 V
Reverse Recovery Time	trr		30		ns	I <sub>F</sub> = 30 A, V <sub>GS</sub> = 0 V,
Reverse Recovery Charge	Qrr		39.6		nC	di/dt = 100 A/μs

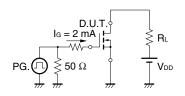
Note: \*1. Pulsed

#### TEST CIRCUIT 1 AVALANCHE CAPABILITY

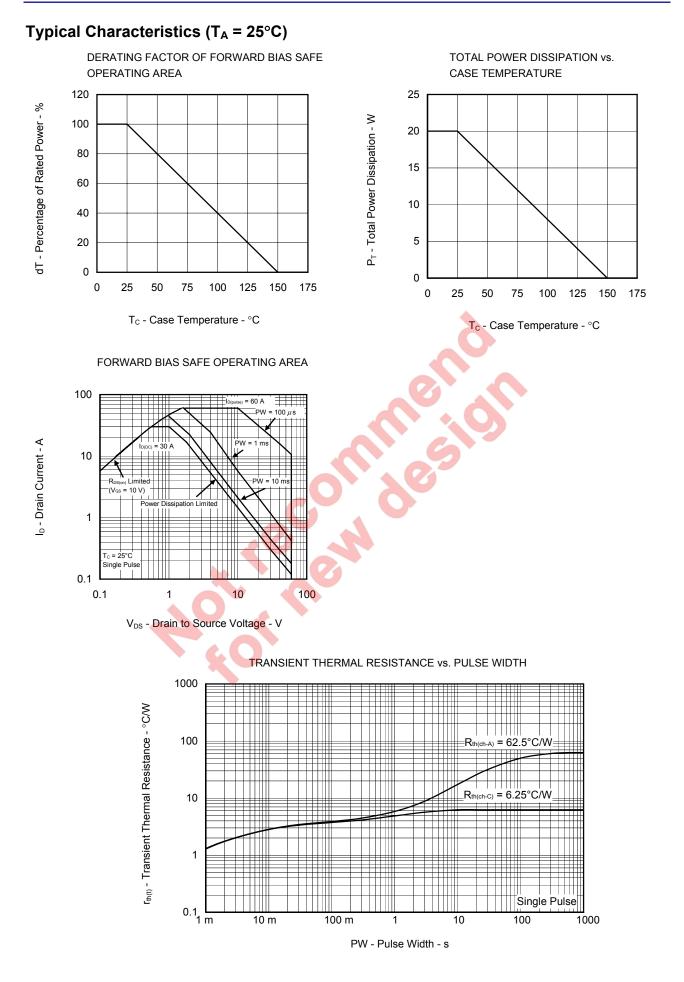
TEST CIRCUIT 2 SWITCHING TIME



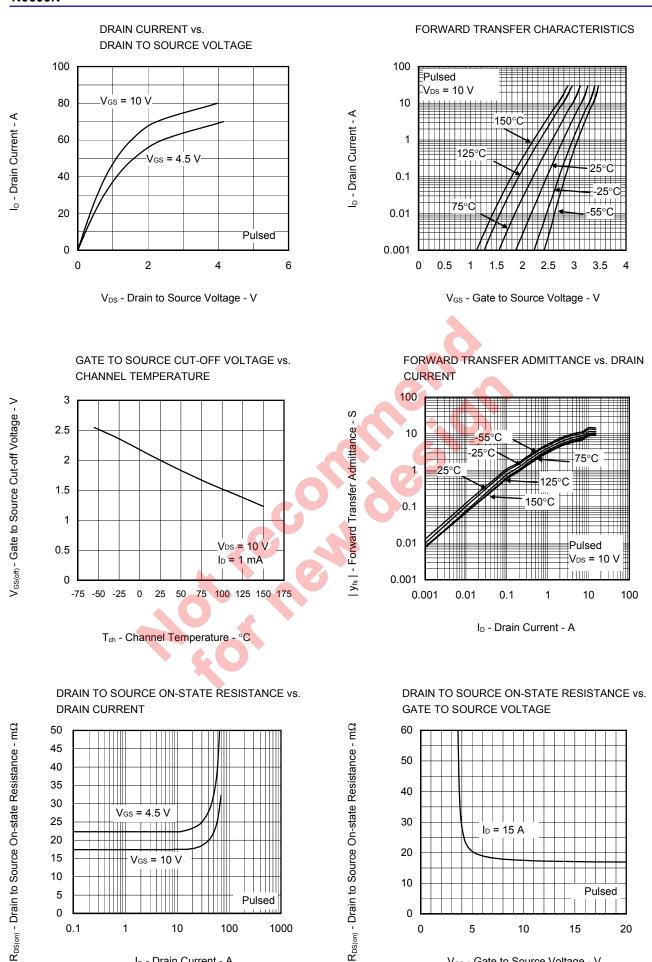
### TEST CIRCUIT 3 GATE CHARGE











10

15

0.1

1

10

I<sub>D</sub> - Drain Current - A

100

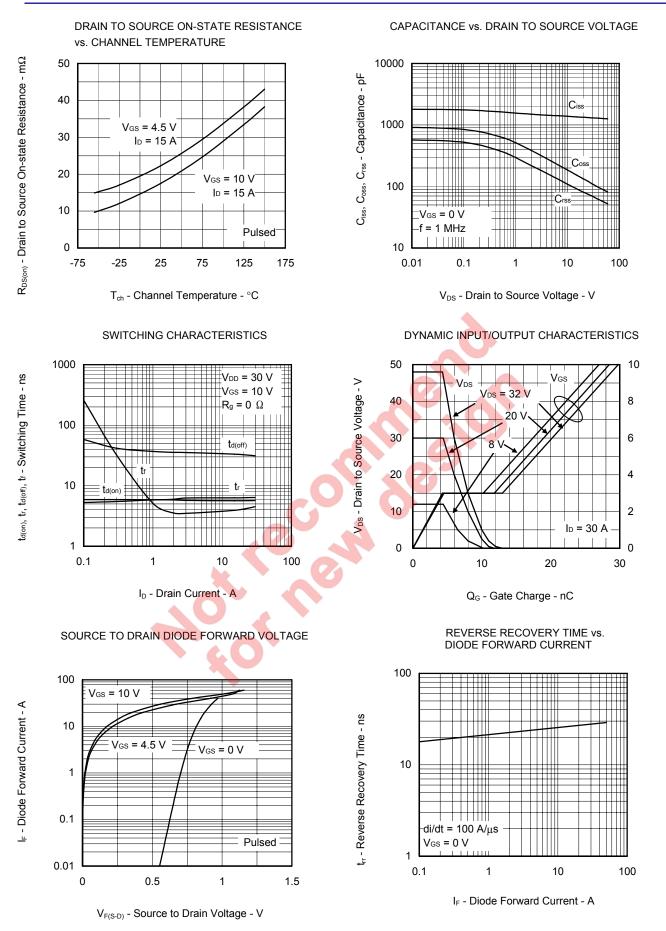
1000



0

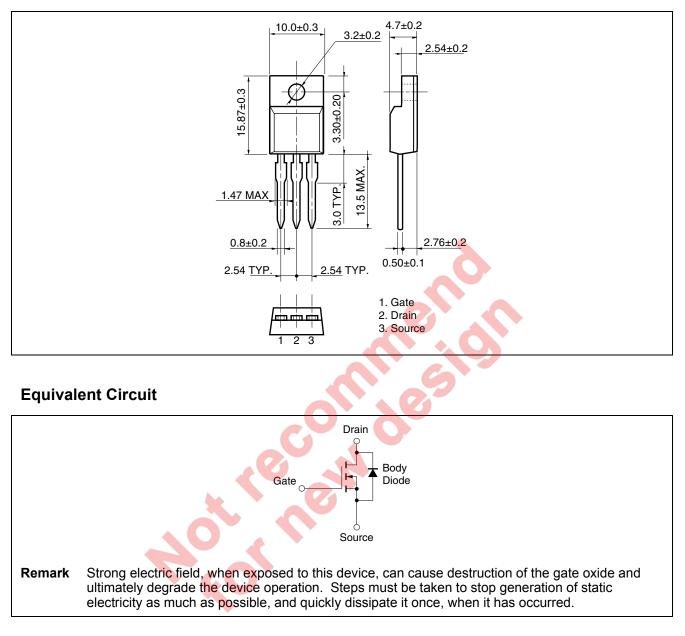
5

20



# Package Drawings (Unit: mm)

## Isolated TO-220





Revision I	History
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		Description		
Rev.	Date	Page	Summary	
1.00	Jan 25, 2011	-	First Edition Issued	



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