100-W 8.5-A Programmable Isolated DC/DC Converter

EXCALIBUR

Features

- 18V to 36V Input Range
- -40° to +100°C Operating Temp
- 1500 VDC Isolation
- 88.5% Efficiency
- Programmable Output Voltage 6.5V to 17.5V
- Remote On/Off
- Differential Remote Sense
- N+1 Current Sharing
- Over-Current Protection
- Over-Temperature Protection
- Over-Voltage Protection
- Solderable Copper Case

Description

The PT4475 Excalibur[™] module combines state-of-the-art power conversion technology with unparalleled flexibility. Operating off a standard 24V telecom input, the PT4475 provides a full 100W output at load currents up to 8.5A, and over the programmable output voltage range of 6.5V to 17.5V.

The PT4475 features high efficiency, ultra-fast transient response, and the capability for true N+1 current sharing. This product also includes output short circuit and over-temperature protection.

Standard Application +V_{IN} + V_{OUT} 8-13 PT4475 0 -V_{in} 14-19 - V _{ol} A D С, -٧, VID0 - VID4 Inhib 21 22 23 24 20 -V_{out} -V_{IN} PROGRAMMING PINS

• C₁ = Optional 33µF, 100V electrolytic capacitor

- C₂ = Optional 1µF, 100V ceramic capacitor
- Programming pins, VID0–VID4, are shown configured for Vo =12.0V
- For normal operation, pin 3 (Inhibit) must be connected to –Vin.
- For operation in N+1 configuration, consult the related application note.
- Pins 6 & 26 are used for N+1 configurations only.

Specifications

Characteristics	Symbols	Conditions	PT4475			
(T _a =25°C unless noted)		$(V_{in}=24V, V_0=12.0V \text{ unless noted})$	Min Typ		Max	Units
Output Current	Io	Over V _{in} range (100W max)	0.1	-	8.5 (1)	А
Current Limit	I _{cl}	$V_{in} = 18V$		10		А
Current Sharing		Single line referenced to -V _{sense}		_	±10	%
Input Voltage Range	Vin	$I_o = 0$ to I_omax	18	24	36	V
Output Voltage Tolerance	ΔV_{o}	Over V_{in} range, $I_o = I_o max$ $T_a = -40$ to +100°C case	_	±1.0	±2.0	%Vo
Line Regulation	Reg _{line}	Over V _{in} range @ max I _o		±0.1	±1.0	$%V_{o}$
Load Regulation	Regload	0 to 100% of I _o max		±0.5	±1.0	$%V_{o}$
V _o Ripple/Noise	V_n	I _o =I _o max		120	150	mV_{pp}
Transient Response	t _{tr}	50% to 75% I _o max @ 0.1A/μs V _o over/undershoot (no ext caps)	_	N/A 1.0	_	μSec %Vo
		50% to 100% I _o max @1.0A/µs V _o over/undershoot (no ext. caps)	_	200 5	_	μSec %Vo
V _o Rise Time	Votr	At turn-on	_	_	10	mSec
Efficiency	η	$I_0 = 5A$		89		%
Switching Frequency	$f_{ m o}$	—	_	300		kHz
Remote On/Off	Off On	referenced to -V _{in}	+2.5 -0.5	_	+75.0 +0.8	VDC
Over-Voltage Protection	OVP	Shutdown and latch off		125	_	$%V_{o}$
Isolation	_	—	1500	_	_	VDC
Operating Temperature Range	T _c	Measured at center of case	-40	_	+100	°C
Over-Temperature Shutdown	OTP	Case temperature - Auto reset		+110		°C
Storage Temperature	Ts	_	-40	_	+125	°C
Reliability	MTBF	Per Bellcore TR-332 50% stress, t =40°C, ground benign	1.7	_	_	10^6 Hr
Mechanical Shock	_	Per Mil-STD-883D, Method 2002.3, 1mS, Half-sine, mounted to a fixture	_	500	_	G's
Mechanical Vibration (Mil-STD-883D,20-2000Hz)	_	Vertical (Suffix N) Horizontal (Suffixes A, C)	_	10 (2) 20 (2)	_	G's
Weight	_	_		90	_	grams

Notes: (1) The maximum output current is limited to 8.5A or $100/V_{OUT}$, Whichever is less.

(2) Only the case pins on the through-hole package types (suffixes N & A) must be soldered. For more information see the applicable package outline drawing.



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Pin-Out Information

Pin	Function	Pin	Function
1	+Vin	14	-V _{out}
2	-V _{in}	15	-V _{out}
3	Inhibit	16	-V _{out}
4	Do not connect	17	-V _{out}
5	Do not connect	18	-V _{out}
6	Sync	19	-V _{out}
7	+V _{sense}	20	-V _{sense}
8	+V _{out}	21	VID0
9	+V _{out}	22	VID1
10	$+V_{out}$	23	VID2
11	+V _{out}	24	VID3
12	$+V_{out}$	25	VID4
13	+V _{out}	26	Share

Programming Information

VID3	VID2	VID1	VIDO	VID4=1 Vout	VID4=0 Vout
1	1	1	1	10.0	6.5
1	1	1	0	10.5	6.75
1	1	0	1	11.0	7.0
1	1	0	0	11.5	7.25
1	0	1	1	12.0	7.5
1	0	1	0	12.5	7.75
1	0	0	1	13.0	8.0
1	0	0	0	13.5	8.25
0	1	1	1	14.0	8.5
0	1	1	0	14.5	8.75
0	1	0	1	15.0	9.0
0	1	0	0	15.5	9.25
0	0	1	1	16.0	9.5
0	0	1	0	16.5	9.75
0	0	0	1	17.0	10.0
0	0	0	0	17.5	10.25

Logic 0 = Pin 20 potential (remote sense gnd) Logic 1 = Open circuit (no pull-up resistors) VID4 may not be changed while the unit is operating.

Ordering Information

 $PT 4475 \square =+6.5V \text{ to } 17.5V$

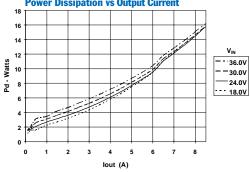
PT Series Suffix (PT1234x)

Case/Pin Configuration	Order Suffix	Package Code
Vertical	N	(EKD)
Horizontal	Α	(EKA)
SMD	C	(EKC)

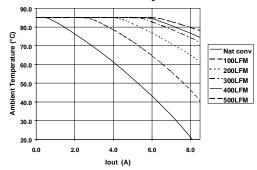
(Reference the applicable package code drawing for the dimensions and PC board layout)

TYPICAL CHARACTERISTICS

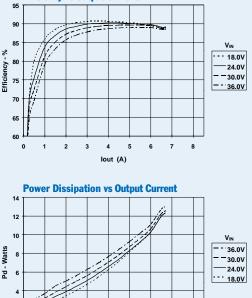
PT4475, Vo =12.0V (See Note A) **Efficiency vs Output Current** 9 90 -----85 VIN Efficiency - % - 18.0V 80 -24.0V -30.0V 75 - 36.0V 70 65 60 2 3 7 8 0 6 1 4 5 lout (A) **Power Dissipation vs Output Current**



Safe Operating Area, V = 12V (See Note B)









2

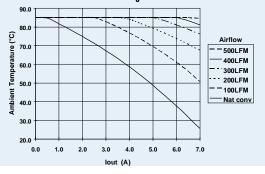
0 1 2

Safe Operating Area, V_a =15V (See Note B)

7

8

6



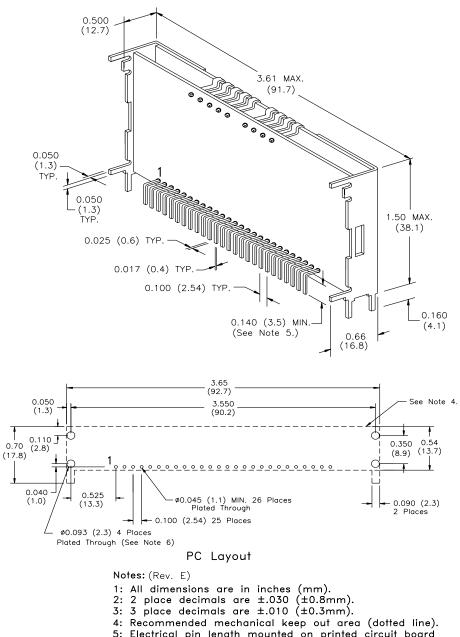
Note A: All data listed in the above graphs has been developed from actual products tested at 25° C. This data is considered typical data for the DC/DC Converter. **Note B:** SOA curves represent operating conditions at which the temperature of the metal case is at or below the maximum specified 100° C

Suffix N

(Revised 6/30/2000)

PACKAGE INFORMATION AND DIMENSIONS

Vertical Through-Hole Mount (Suffix N)



- Electrical pin length mounted on printed circuit board seating plane to pin end.
 Option (Electrically connect heatsink pins to secondary ground.)

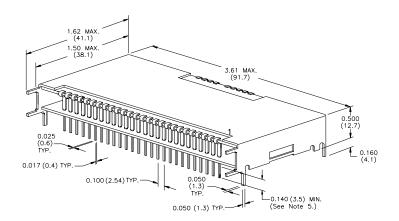


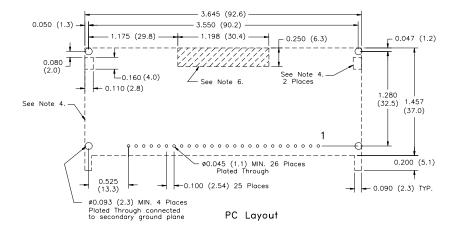
Suffix A

(Revised 1/17/2001)

PACKAGE INFORMATION AND DIMENSIONS

Horizontal Through-Hole Mount (Suffix A)





Notes: (Rev. G)

1: All dimensions are in inches (mm).

2: 2 place decimals are \pm .030 (\pm 0.8mm). 3: 3 place decimals are \pm .010 (\pm 0.3mm).

4: Recommended mechanical keep out area (dotted lines).

Electrical pin length mounted on printed circuit board seating plane to pin end.
 No copper, power or signal traces in this area.

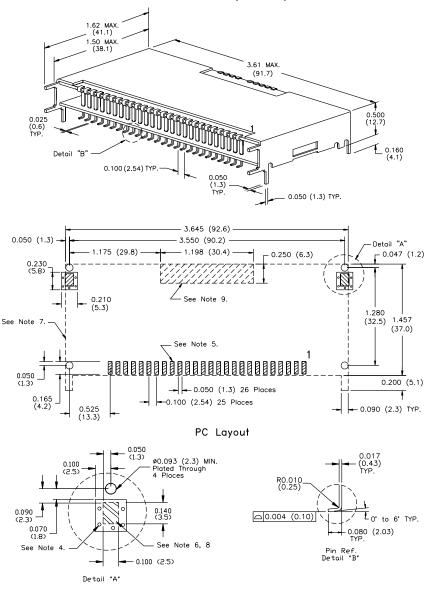


Suffix C

(Revised 1/17/2001)

PACKAGE INFORMATION AND DIMENSIONS

Horizontal Surface Mount (Suffix C)



Notes: (Rev. E)

- 1: All dimensions are in inches (mm).
- 2: 2 place decimals are ±.030 (±0.8mm).
- 3: 3 place decimals are ±.010 (±0.3mm).
- 4: Vias are recommended to improve copper adhesion.
- 5: Power pin connections should utilize two or more
- vias per input, ground and output pin.
- 6: Solder mask openings to copper island for solder joints to mechanical pins.
- 7: Recommended mechanical keep out area (dotted lines).
- 8: Electrically connected case to secondary ground plane.
- 9: No copper, power or signal traces in this are.

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