

# GP2W1001YP

## Low Profile Type IrDA Transceiver Module Compliant with IrDA1.1

### ■ Features

1. Integrated package of light emitter and receiver.  
(10.0×4.4×H3.5 mm)
2. Low profile type (Height:3.5 mm)
3. Compliant with IrDA1.0 and IrDA1.1
4. Low voltage operation type  
(Supply voltage:2.4V to 5.5V)
5. Low dissipation current thanks to power down mode  
(Dissipation current at shut-down mode:Max. 1μA)
6. Applicable for reflow soldering
7. With shield case

### ■ Applications

1. Personal computers
2. Personal information tools

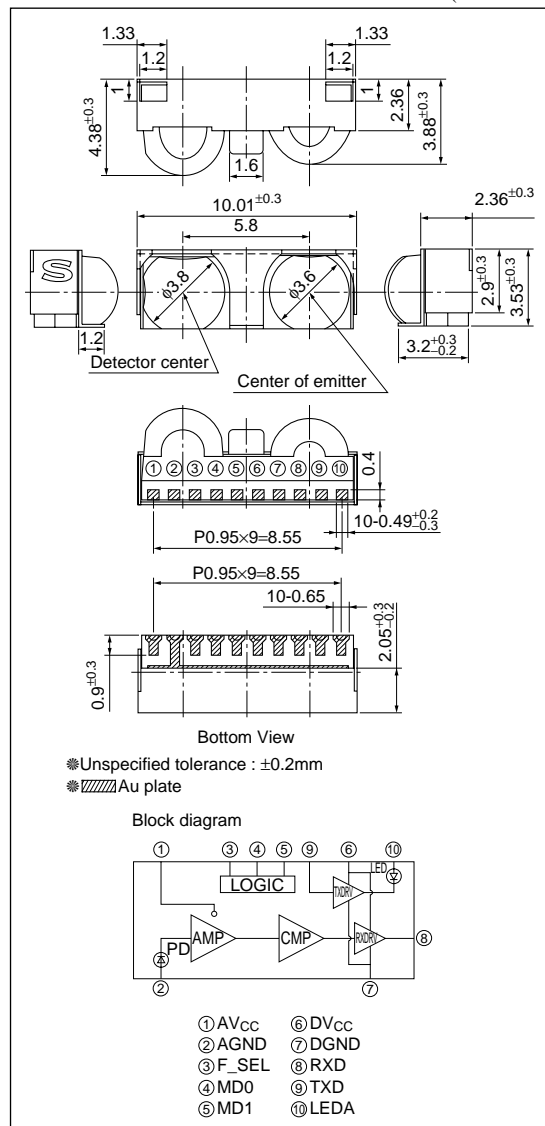
### ■ Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	6	V
Transmission signal duty ratio	TXduty	50	%
Operating temperature	T <sub>opr</sub>	-10 to +70	°C
Storage temperature	T <sub>stg</sub>	-20 to +85	°C

Note) Transmission signal duty ratio show the time share of H level of transmission wave at TX terminal. The frequency shall be 1kHz or more.

### ■ Outline Dimensions

(Unit : mm)



## ■ Recommended Operating Conditions (Ta=25°C)

Parameter	Symbol	Rating	Unit
Operating Supply voltage	V <sub>CC</sub>	2.7 to 5.5	V
Operating temperature	T <sub>opr</sub>	0 to +70	°C
SIR bit rate	–	9.6 to 115.2	kbps
FIR bit rate	–	1.152/4	Mbps

## ■ Electro-optical Characteristics (Ta=25±3°C, V<sub>CC</sub>=3.3±0.1V, 5±0.1V, Ambient illuminance of detecting face: 100lx or less)

Parameter		Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Receiver side	Dissipation current at no input signal	I <sub>CC</sub>	RXOUT:H, TXIN:L, V <sub>CC</sub> =3.3V	–	–	1.2	mA
			RXOUT:H, TXIN:L, V <sub>CC</sub> =5.0V	–	–	1.5	mA
	S/D dissipation current	I <sub>CCS</sub>	RXOUT:H, TXIN:L, V <sub>CC</sub> =3.3V	–	0.1	1	μA
			RXOUT:H, TXIN:L, V <sub>CC</sub> =5.0V	–	0.2	2	μA
	Dissipation current	I <sub>CCP</sub>	Peak Value, V <sub>CC</sub> =3.3V (2.4Ω), 5.0V (6.8Ω)	–	400	600	mA
	Switching time (Shut down to stand by)	trSD		–	–	1.5	ms
	Receiver sensitivity (–15°≤θ≤+15°)	S <sub>Rxθ</sub>	SIR:9.6k to 115.2kbps	0.04	–	5 000	W/m <sup>2</sup>
			FIR:1.152M/4Mbps	0.01	–	5 000	W/m <sup>2</sup>
	Low level output voltage	V <sub>OL</sub>	V <sub>CC</sub> =3.3, 5.0V	–	–	0.4	V
	High level output voltage	V <sub>OH</sub>	V <sub>CC</sub> =3.3, 5.0V	V <sub>CC</sub> -0.4	–	–	V
	Rise time	t <sub>rrA</sub>	SIR:9.6k to 115.2kbps	–	–	200	ns
		t <sub>rrB</sub>	FIR:1.152M/4Mbps	–	–	40	ns
	Fall time	t <sub>frA</sub>	SIR:9.6k to 115.2kbps	–	–	200	ns
		t <sub>frB</sub>	FIR:1.152M/4Mbps	–	–	40	ns
	Peak sensitivity wavelength	λ <sub>sx</sub>		–	940	–	nm
	Low level pules width	t <sub>w1</sub>	SIR:Pulse width 19.53μs(9.6kbps), Duty 3/16	1	–	22	μs
		t <sub>w2</sub>	SIR:Pulse width 1.63μs(115.2kHz), Duty 3/16	1	–	3	μs
		t <sub>w3</sub>	FIR:Pulse width 217ns(1.152Mbps), Duty 1/4	110	–	500	ns
t <sub>w4</sub>		FIR:Pules width Single 125ns(4Mbps), Duty 1/4	85	–	165	ns	
t <sub>w5</sub>		FIR:Pules width Double 250ns(4Mbps), Duty 1/4	210	–	290	ns	
Peak emission wavelength	λ <sub>tx</sub>		850	880	900	nm	
Radiant intensity	φ=0°	I <sub>ELO</sub>	Low Power	–	15	–	mW/sr
		I <sub>EMO</sub>	Middle Power	–	60	–	mW/sr
	–15°≤φ≤+15°	I <sub>EFθ</sub>	Full Power	100	–	500	mW/sr
Low level input voltage	V <sub>IL</sub>	V <sub>CC</sub> =3.3, 5.0V	–	–	V <sub>CC</sub> ×0.2	V	
High level input voltage	V <sub>IH</sub>	V <sub>CC</sub> =3.3, 5.0V	V <sub>CC</sub> ×0.2	–	–	V	
TXin terminal input current	I <sub>IH</sub>	TXIN=V <sub>CC</sub> =5.0V	–	–	50	μA	
	I <sub>IL</sub>	TXIN=GND	–0.1	0	0.1	μA	
Transmission signal rise time	t <sub>rx</sub>		–	–	40	ns	
Transmission signal fall time	t <sub>fx</sub>		–	–	40	ns	

## ■ Truth Table

Input		Output	
TXD (Transmitter)	*1 Receiver	State of LED (Transmitter)	RXD terminal
High	–	ON	X
Low	ON	OFF	Low
Low	OFF	OFF	High

X:Do not care

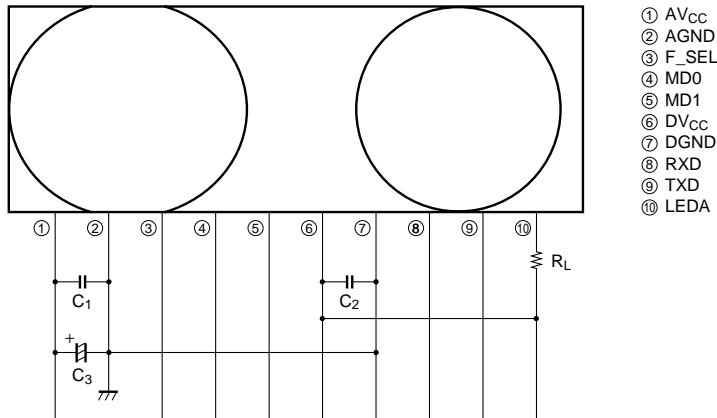
\*1 External optical signal receiving state of photodiode

■ Input Output Logic Table

MD0	MD1	F_SEL	TXD	RXD terminal mode	TXO *2	Remarks
1	0	X	0	Shut down	Shut down	—
	0	1	0	RXA	X	Latch TXD *1
	0	1	1	RXB	X	Latch TXD *1
0	0	0	X	RXA	HPW	TXO Output High Power mode
0	1	0	X	↓	MPW	TXO Output Middle Power mode
1	1	0	X	↓	LPW	TXO Output Low Power mode
0	0	1	X	RXB	HPW	TXO Output High Power mode
0	1	1	X	↓	MPW	TXO Output Middle Power mode
1	1	1	X	↓	LPW	TXO Output Low Power mode

\*1 F\_SFL → 0:reset latching state of TXD, and turn to RXA channel.  
 RXA:RXA channel mode:115kbps or less (SIR 115.2kbps, 9 600bps)  
 RXB:RXB channel mode:115kbps or more (FIR 1.152Mbps, 4Mbps)  
 \*2 LED operating mode

Fig.1 Recommended External Circuit



Components	Recommended values
C1•C2	1μF±10% (Note 1)
C3	4.7μF±10% (Note 2)
RL	2.4Ω±5% 1/2W (VCC=3.3V)
	6.8Ω±5% 1/2W (VCC=5V)

(Note 1) Please locate nearby this transceiver choosing the ceramic capacitor with higher frequency feature

(Note 2) Please choose the most suitable CX according to the noise level and noise frequency of power supply

Fig.2 Output Waveform Specification(Receiver side)(CL≤10pF)

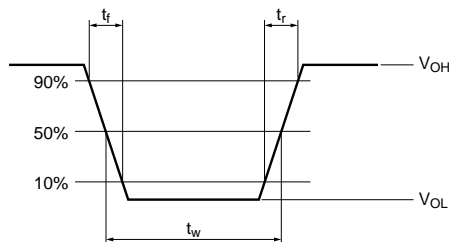
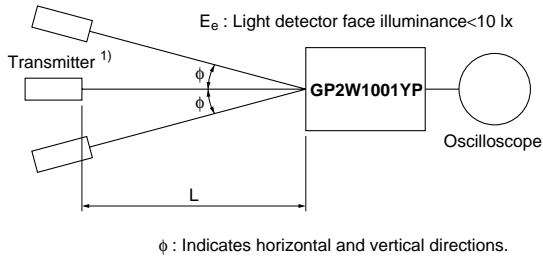


Fig.3 Standard Optical System(Receiver side)



Transfer rate	T <sub>1</sub>	T <sub>2</sub>	T <sub>2</sub> /T <sub>1</sub>	Radiant intensity
9.6kbps	104μs	19.53μs	3/16	40mW/sr
115.2kbps	8.68μs	1.63μs	3/16	40mW/sr
1.152Mbps	868ns	217ns	1/4	100mW/sr
4Mbps (S)	500ns	125ns	1/4	100mW/sr
4Mbps (W)	1 000ns	250ns	1/4	100mW/sr

The light emitting diode (SHARP GL710, λ<sub>p</sub>=850 to 900nm) is used as the transmitter, where the following continuous signals are transmitted.  
 In Fig.3, output signal shall be complete receiver side electro-optical characteristics.

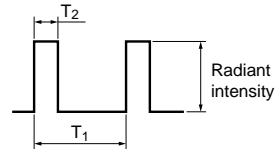


Fig.4 Output Waveform Specification(Transmitter side)

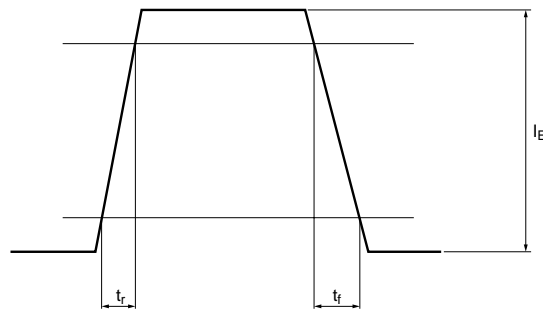


Fig.5 Standard Optical System(Transmitter side)

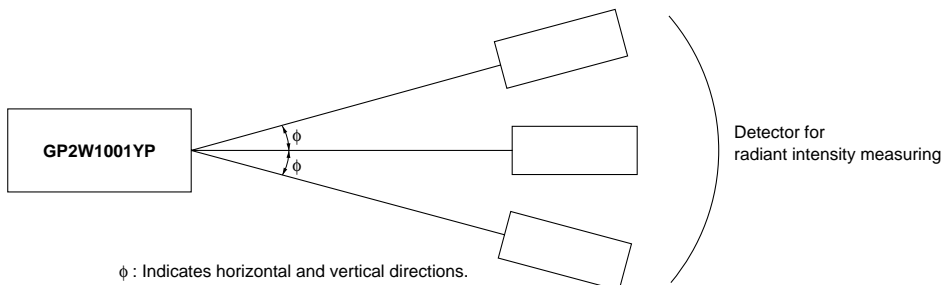


Fig.6 Recommended Circuit of Transmitter side

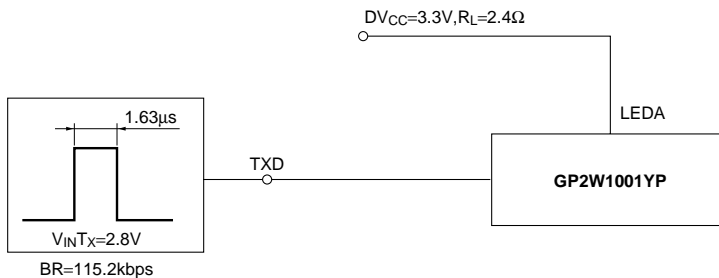


Fig.7 Peak Forward Current vs. Ambient Temperature

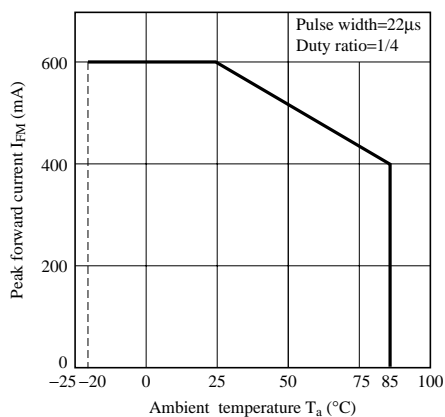
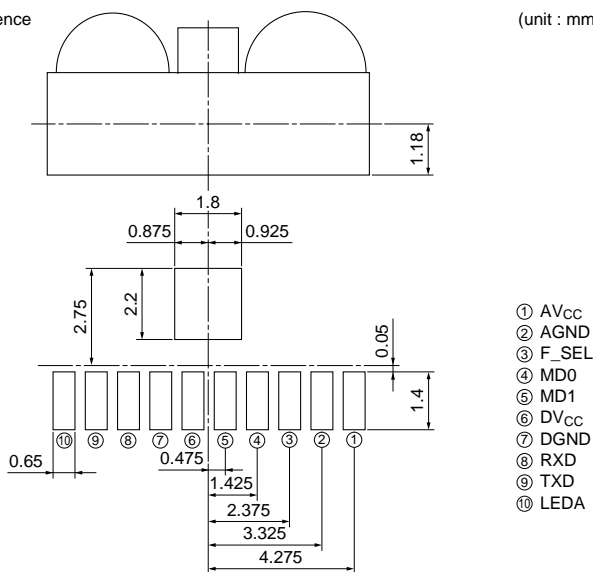


Fig.8 Recommended PCB Foot Pattern

Dimensions are shown for reference

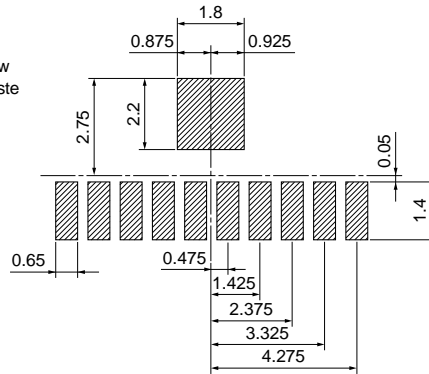
(unit : mm)



**Fig.9 Recommended Size of Solder Creamed Paste (Reference)**

Dimensions are shown for reference.  
Please open the solder mask as below  
so that the size of solder creamed paste  
for this device before reflow soldering  
must be as large as one of the foot  
pattern land indicated at Fig.8

(unit : mm)



▨ : Solder paste area

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