

## Features

- RoHS compliant\*
- Protects one line
- ESD protection 30 kV max.

## **Applications**

- RS-232, RS-422 & RS-423 data lines
- Portable electronics
- Wireless bus protection
- Control & monitoring systems

## CDSOT23-T03LC~T36LC - Low Capacitance TVS Diode Array Series

#### **General Information**

Portable communications, computing and video equipment manufacturers are challenging the semiconductor industry to develop increasingly smaller electronic components.

Bourns offers Transient Voltage Suppressor Array Diodes for surge and ESD protection applications, in compact chip package SOT23 size format. The Transient Voltage Supressor Array series offers a choice of voltage types ranging from 3 V to 36 V. Bourns<sup>®</sup> Chip Diodes conform to JEDEC standards, are easy to handle on standard pick and place equipment and their flat configuration minimizes roll away.

The Bourns<sup>®</sup> device will meet IEC 61000-4-2 (ESD), IEC 61000-4-4 (EFT) and IEC 61000-4-5 (Surge) requirements.

## Thermal Characteristics (@ T<sub>A</sub> = 25 °C Unless Otherwise Noted)

Parameter	Symbol	Value	Unit
Operating Temperature	Т <sub>Ј</sub>	-55 to +150	°C
Storage Temperature	T <sub>STG</sub>	-55 to +150	°C

### **Additional Information**

Click these links for more information:



## Electrical Characteristics (@ T<sub>A</sub> = 25 °C Unless Otherwise Noted)

Parameter	Symbol	T03LC	T05LC	T08LC	T12LC	T15LC	T24LC	T36LC	Unit
Breakdown Voltage @ 1 mA	V <sub>BR</sub>	4.0	6.0	8.5	13.3	16.7	26.7	40.0	V
Working Peak Voltage	V <sub>WM</sub>	3.3	5.0	8.0	12.0	15.0	24.0	36.0	V
Maximum Clamping Voltage $V_C @ I_P^1$	V <sub>F</sub>	8.0	9.8	13.4	19.0	24.0	43.0	51.0	v
Maximum Clamping Voltage @ 8/20 $\mu$ s V <sub>C</sub> @ I <sub>PP</sub> <sup>1</sup>	V <sub>F</sub>	10.9 V @ 43 A	13.5 V @ 42 A	16.9 V @ 34 A	25.9 V @ 27 A	30.0 V @ 17 A	49.0 V @ 12 A	76.8 V @ 9 A	v
Maximum Leakage Current @ V <sub>WM</sub>	I <sub>D</sub>	125	20	10	2	1	1	1	μA
Typical Capacitance Bidirectional @ 0 V, 1 MHz	C <sub>j(SD)</sub>				5				pF
ESD Protection (per IEC 61000-4-2) Contact - Min. Contact - Max. Air - Min. Air - Max.	ESD	±8 ±30 ±15 ±30				kV			
Peak Pulse Power $(t_p = 8/20 \ \mu s)^2$	P <sub>PP</sub>				500				W

Notes:

1. See Pulse Wave Form.

2. See Peak Pulse Power vs. Pulse Time.

3. Positive Potential is applied from Pin 1 to Pin 2 with Pin 2 as ground.

4. Do not test or surge from Pin 2 to Pin 1.



\*RoHS Directive 2015/863, Mar 31, 2015 and Annex.

Specifications are subject to change without notice.

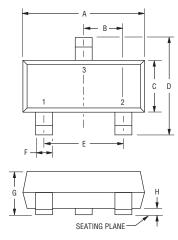
Users should verify actual device performance in their specific applications.

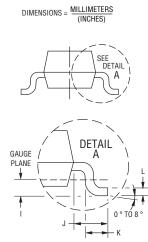
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#### CDSOT23-T03LC~T36LC - Low Capacitance TVS Diode Array Series OURNÉ

## **Product Dimensions**

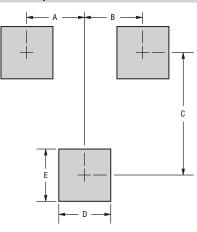
This is a molded JEDEC SOT23-6 package with lead free 100 % Sn plating on the lead frame. It weighs approximately 0.6 g and has a flammability rating of UL 94V-0.





Dimensions				
А	<u>2.80 - 3.00</u> (0.110 - 0.118)			
В	0.95 (0.037) BSC			
С	<u>1.20 - 1.40</u> (0.047 - 0.055)			
D	<u>2.10 - 2.49</u> (0.083 - 0.098)			
E	1.90 (0.075) BSC			
F	<u>0.30 - 0.50</u> (0.012 - 0.019)			
G	<u>0.89 - 1.17</u> (0.035 - 0.046)			
н	<u>0.05 - 0.015</u> (0.002 - 0.006)			
I	<u>0.25</u> (0.010) BSC			
J	<u>0.46 - 0.64</u> (0.018 - 0.025)			
к	<u>0.40 - 0.58</u> (0.016 - 0.023)			
L	<u>0.08 - 0.20</u> (0.003 - 0.008)			

#### **Recommended Footprint**



DIMENSIONS =  $\frac{\text{MILLIMETERS}}{(\text{INCHES})}$ 

Dimensions			
А	<u>0.95</u> (0.037)		
В	<u>0.95</u> (0.037)		
С	<u>2.00</u> (0.079)		
D	<u>0.85</u> (0.033)		
E	<u>0.85</u> (0.033)		

#### How to Order

	CD 30123 -	1 03 LC
Common Code CD = Chip Diode		
Package SOT23 = SOT23 Package		
Model T = Transient Voltage Suppressor		
Working Peak Voltage		
Suffix		

CD COTOS

T 02 1 C

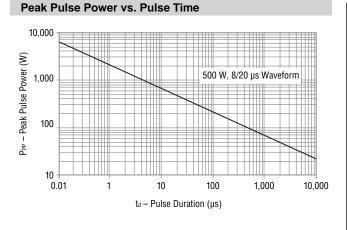
LC = Low Capacitance Diode

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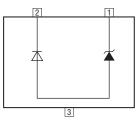
# CDSOT23-T03LC~T36LC - Low Capacitance TVS Diode Array Series **BOURNS**®

#### **Performance Graphs**

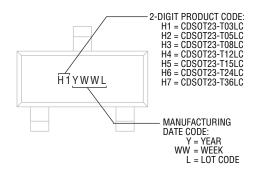


## **Block Diagram**

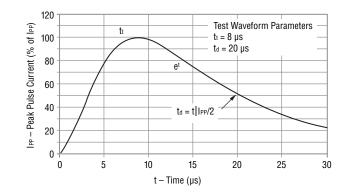
The device block diagram below includes the pin names and basic electrical connections.

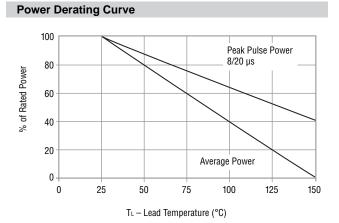


### **Typical Part Marking**



Pulse Waveform





#### Environmental Specifications

Moisture Sensitivity Level	1
ESD Classification (HBM)	3B

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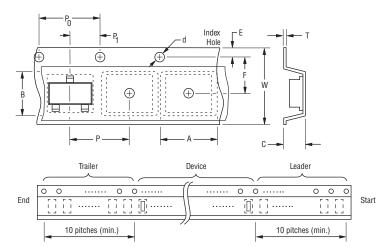
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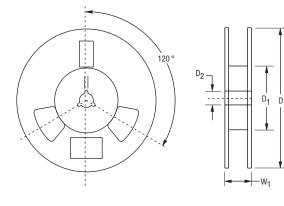
# CDSOT23-T03LC~T36LC - Low Capacitance TVS Diode Array Series **BOURNS**®

#### **Packaging Information**

The surface mount product is packaged in a 12 mm x 8 mm tape and reel format per EIA-481 standard.

Direction of Feed





 $DIMENSIONS = \frac{MILLIMETERS}{(INCHES)}$ 

Item	Symbol	SOT23
Carrier Width	А	$2.25 \pm 0.10$
		$(0.088 \pm 0.004) \\ 2.34 \pm 0.10$
Carrier Length	В	$\frac{2.34 \pm 0.10}{(0.092 \pm 0.004)}$
Corrier Donth	0	1.22 ± 0.10
Carrier Depth	С	$(0.048 \pm 0.004)$
Sprocket Hole	d	$1.55 \pm 0.05$
	-	(0.061 ± 0.002) 178
Reel Outside Diameter	D	(7.008)
		Min.
Reel Inner Diameter	D <sub>1</sub>	(1.969) Min.
Feed Hole Diameter	D <sub>2</sub>	13.0 ± 0.20
	D2	(0.512 ± 0.008)
Sprocket Hole Position	E	$\frac{1.75 \pm 0.10}{(0.000 \pm 0.004)}$
•		$(0.069 \pm 0.004)$ $3.50 \pm 0.05$
Punch Hole Position	F	$\frac{0.00 \pm 0.00}{(0.138 \pm 0.002)}$
Durah Hala Ditah	Р	4.00 ± 0.10
Punch Hole Pitch	P	$(0.157 \pm 0.004)$
Sprocket Hole Pitch	P0	4.00 ± 0.10
	.0	(0.157 ± 0.004)
Embossment Center	P <sub>1</sub>	$\frac{2.00 \pm 0.05}{(0.070 \pm 0.002)}$
	•	$\frac{(0.079 \pm 0.002)}{0.20 \pm 0.10}$
Overall Tape Thickness	Т	$\frac{0.20 \pm 0.10}{(0.008 \pm 0.004)}$
Tana Width	W	8.00 ± 0.20
Tape Width	VV	$(0.315 \pm 0.008)$
Reel Width	W <sub>1</sub>	<u>14.4</u> Max. (0.567)
Quantity per Reel	_	3,000

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