LDMOS avionics radar power transistor

Rev. 05 — 1 September 2015



## 1. Product profile

### 1.1 General description

500 W LDMOS power transistor intended for avionics transmitter applications in the 960 MHz to 1215 MHz range such as Mode-S, TCAS, JTIDS, DME and TACAN.

#### Table 1. Test information

Typical RF performance at  $T_{case} = 25 \ ^{\circ}C$ ;  $t_p = 128 \ \mu s$ ;  $\delta = 10 \ ^{\circ}$ ;  $I_{Dq} = 100 \ mA$ ; in a class-AB production test circuit.

Mode of operation	f	V <sub>DS</sub>	P <sub>L</sub>	G <sub>p</sub>	η <sub>D</sub>	t <sub>r</sub>	t <sub>f</sub>
	(MHz)	(V)	(W)	(dB)	(%)	(ns)	(ns)
pulsed RF	960 to 1200	50	450	17	50	20	6

#### CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Therefore care should be taken during transport and handling.

## 1.2 Features and benefits

- Typical pulsed RF performance at a frequency of 960 MHz to 1215 MHz, a supply voltage of 50 V, an  $I_{Dq}$  of 100 mA, a  $t_p$  of 128  $\mu$ s with  $\delta$  of 10 %:
  - Output power = 450 W
  - Power gain = 17 dB
  - Efficiency = 50 %
- Easy power control
- Integrated ESD protection
- High flexibility with respect to pulse formats
- Excellent ruggedness
- High efficiency
- Excellent thermal stability
- Designed for broadband operation (960 MHz to 1215 MHz)
- Internally matched for ease of use
- Compliant to Directive 2002/95/EC, regarding Restriction of Hazardous Substances (RoHS)

## **1.3 Applications**

 A-band power amplifiers for radar applications in the 960 MHz to 1215 MHz frequency range

# 2. Pinning information

Pin	Description	Simplified outline	Graphic symbol
1	drain		
2	gate		1 لــــا
3	source		2 – – – 3 sym112

[1] Connected to flange.

## 3. Ordering information

Table 3. Ordering information	Table 3.	Ordering information
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Type number	Package	'ackage			
	Name	Description	Version		
BLA6H0912-500	-	flanged ceramic package; 2 mounting holes; 2 leads	SOT634A		

# 4. Limiting values

#### Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Мах	Unit
V <sub>DS</sub>	drain-source voltage		-	100	V
V <sub>GS</sub>	gate-source voltage		-0.5	+13	V
I <sub>D</sub>	drain current		-	54	А
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		-	200	°C

## 5. Thermal characteristics

Table 5.	Thermal characteristics			
Symbol	Parameter	Conditions	Тур	Unit
Z <sub>th(j-c)</sub>	transient thermal impedance from	T <sub>case</sub> = 85 °C; P <sub>L</sub> = 450 W		
	junction to case	t <sub>p</sub> = 32 μs; δ = 2 %	0.03	K/W
		t <sub>p</sub> = 128 μs; δ = 10 %	0.08	K/W
		$t_p$ = 2400 $\mu$ s; $\delta$ = 6.4 %	0.2	K/W

## 6. Characteristics

#### Table 6. DC characteristics

 $T_j = 25 \ ^{\circ}C$  unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>(BR)DSS</sub>	drain-source breakdown voltage	$V_{GS}$ = 0 V; I <sub>D</sub> = 2.7 mA	100	-	-	V
V <sub>GS(th)</sub>	gate-source threshold voltage	$V_{DS}$ = 10 V; $I_{D}$ = 270 mA	1.3	1.8	2.2	V
I <sub>DSS</sub>	drain leakage current	$V_{GS}$ = 0 V; $V_{DS}$ = 50 V	-	-	3.6	μA
I <sub>DSX</sub>	drain cut-off current	$\label{eq:VGS} \begin{array}{l} V_{GS} = V_{GS(th)} + 3.75 \; V; \\ V_{DS} = 10 \; V \end{array}$	53.5	64	-	A
I <sub>GSS</sub>	gate leakage current	$V_{GS}$ = 11 V; $V_{DS}$ = 0 V	-	-	360	nA
<b>g</b> <sub>fs</sub>	forward transconductance	$V_{DS}$ = 10 V; I <sub>D</sub> = 405 mA	2.50	3.5	4.55	S
R <sub>DS(on)</sub>	drain-source on-state resistance	$V_{GS} = V_{GS(th)} + 3.75 V;$ $I_D = 14.18 A$	-	70	85	mΩ

#### Table 7. RF characteristics

Mode of operation: pulsed RF; f = 960 MHz to 1215 MHz;  $t_p = 128 \ \mu s$ ;  $\delta = 10 \ \%$ ; RF performance at  $V_{DS} = 50 \ V$ ;  $I_{Dq} = 100 \ m$ A;  $T_{case} = 25 \ ^{\circ}C$ ; unless otherwise specified, in a class-AB production test circuit.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
PL	output power		-	450	-	W
V <sub>DS</sub>	drain-source voltage	P <sub>L</sub> = 450 W	-	-	50	V
G <sub>p</sub>	power gain	P <sub>L</sub> = 450 W	16	17	-	dB
RL <sub>in</sub>	input return loss	P <sub>L</sub> = 450 W	7	11	-	dB
$\eta_D$	drain efficiency	P <sub>L</sub> = 450 W	45	50	-	%
P <sub>droop(pulse)</sub>	pulse droop power	P <sub>L</sub> = 450 W	-	0	0.3	dB
t <sub>r</sub>	rise time	P <sub>L</sub> = 450 W	-	20	50	ns
t <sub>f</sub>	fall time	P <sub>L</sub> = 450 W	-	6	50	ns

## 6.1 Ruggedness in class-AB operation

The BLA6H0912-500 is capable of withstanding a load mismatch corresponding to VSWR = 10 : 1 through all phases under the following conditions: f = 960 MHz, 1030 MHz, 1090 MHz or 1215 MHz.  $V_{DS}$  = 50 V;  $I_{Dq}$  = 100 mA;  $P_L$  = 450 W;  $t_p$  = 128 µs;  $\delta$  = 10 %.

BLA6H0912-500#5

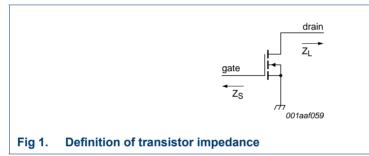
# 7. Application information

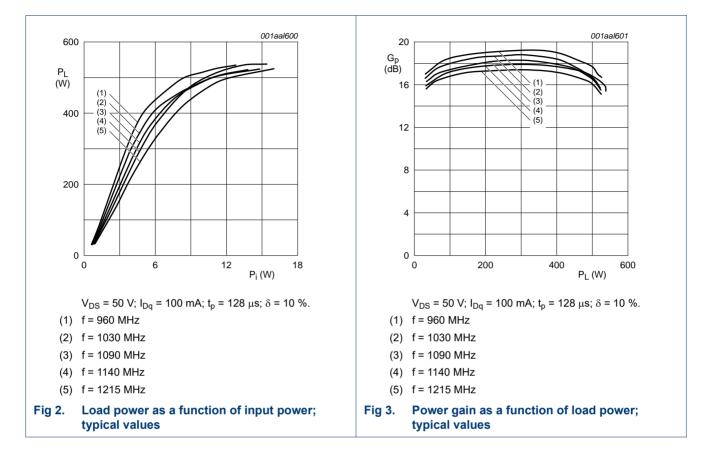
## 7.1 Impedance information

### Table 8. Typical impedance

Typical values per section unless otherwise specified.

Typical values per section	i uniess otherwise specified.	
f	Z <sub>S</sub>	ZL
MHz	Ω	Ω
960	1.36 – j1.45	1.49 – j1.48
1030	1.54 – j1.25	1.51 – j1.45
1090	1.67 – j1.22	1.36 – j1.47
1140	1.68 – j1.29	1.15 – j1.41
1215	1.43 – j1.42	0.79 – j1.17



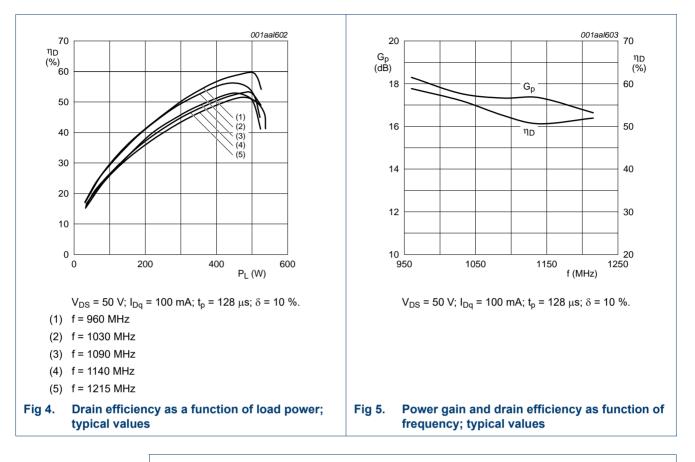


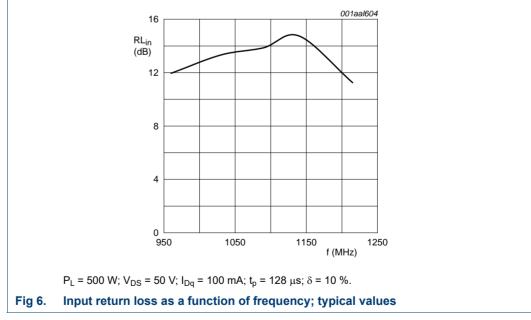
## 7.2 Performance curves

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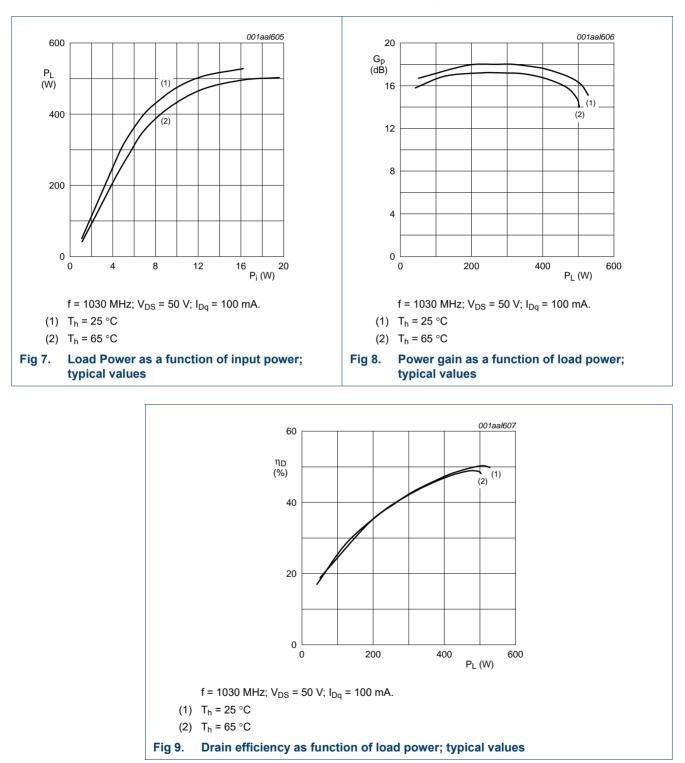




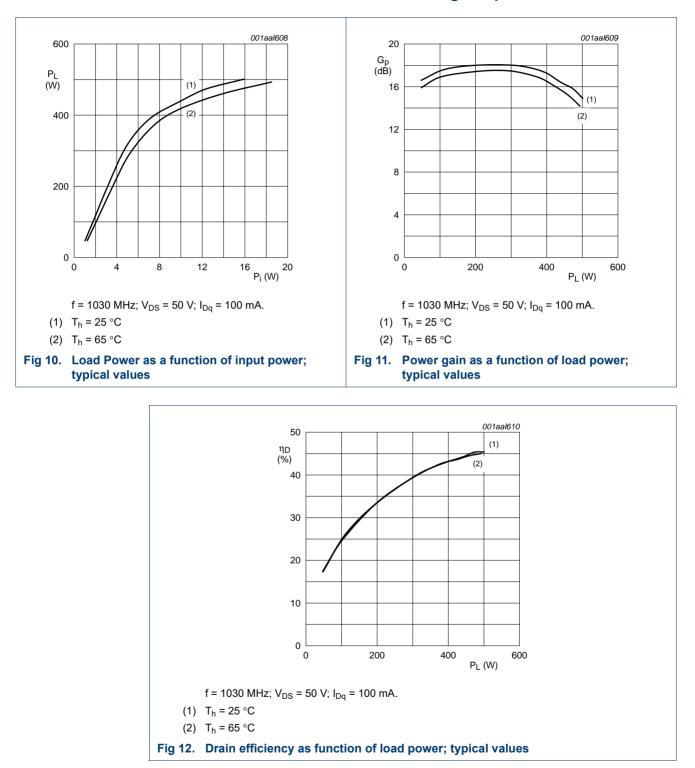
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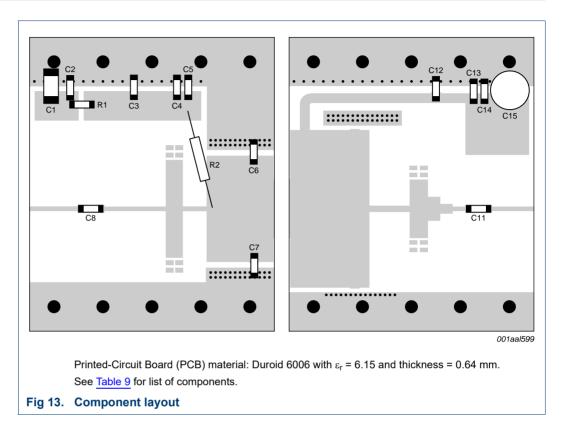
## 7.3 Curves measured under Mode-S ELM pulse-conditions



### 7.4 Curves measured under Mode-S interrogator pulse-conditions

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# 8. Test information



#### Table 9. List of components

See Figure 13 for component layout.

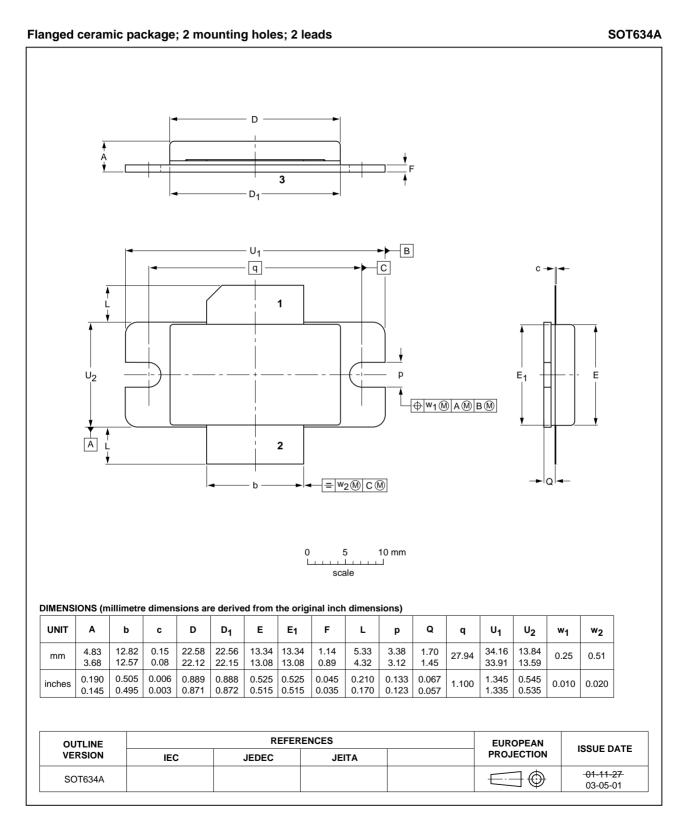
Component	Description	Value	Remarks
C1, C3	multilayer ceramic chip capacitor	10 μF; 35 V	
C2, C3, C14	multilayer ceramic chip capacitor	39 pF	[1]
C4, C13	multilayer ceramic chip capacitor	1 nF	[1]
C6, C7	multilayer ceramic chip capacitor	6.8 pF	[2]
C5, C8, C11, C12	multilayer ceramic chip capacitor	82 pF	[2]
C15	electrolytic capacitor	47 μF; 63 V	
R1	SMD resistor	56 Ω	SMD 0603
R2	metal film resistor	51 Ω	

[1] American Technical Ceramics type 100B or capacitor of same quality.

[2] American Technical Ceramics type 800B or capacitor of same quality.

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## 9. Package outline



#### Fig 14. Package outline SOT634A

# **10. Abbreviations**

Table 10. Abbreviations			
Acronym	Description		
DME	Distance Measuring Equipment		
ELM	Extended Length Message		
JTIDS	Joint Tactical Information Distribution System		
LDMOS	Laterally Diffused Metal-Oxide Semiconductor		
Mode-S	Mode Select		
RF	Radio Frequency		
SMD	Surface Mounted Device		
TACAN	TACtical Air Navigation		
TCAS	Traffic Collision Avoidance System		
VSWR	Voltage Standing-Wave Ratio		

# 11. Revision history

## Table 11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BLA6H0912-500_5	20150901	Product data sheet	-	BLA6H0912-500_4
Modifications:	of Ampleon.	his document has been rede		
	<ul> <li>Legal texts ha</li> </ul>	ve been adapted to the new o	company name where	appropriate.
BLA6H0912-500_4	20100510	Product data sheet	-	BLA6H0912-500_3
BLA6H0912-500_3	20100330	Product data sheet	-	BLA6H0912-500_2
BLA6H0912-500_2	20100302	Product data sheet	-	BLA6H0912-500_1
BLA6H0912-500_1	20090305	Objective data sheet	-	-

# **12. Legal information**

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Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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[2] The term 'short data sheet' is explained in section "Definitions".

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