

### 3<sup>rd</sup> Generation thinQ!™ SiC Schottky Diode

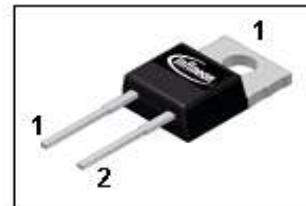
#### Features

- Revolutionary semiconductor material - Silicon Carbide
- Switching behavior benchmark
- No reverse recovery / No forward recovery
- Temperature independent switching behavior
- High surge current capability
- Pb-free lead plating; RoHS compliant
- Qualified according to JEDEC<sup>1)</sup> for target applications
- Breakdown voltage tested at 20mA<sup>2)</sup>
- Optimized for high temperature operation
- Lowest Figure of Merit  $Q_C/I_F$

#### Product Summary

$V_{DC}$	600	V
$Q_C$	16	nC
$I_F; T_C < 130^\circ C$	10	A

PG-T0220-2



#### thinQ! 3G Diode designed for fast switching applications like:

- SMPS e.g.; CCM PFC
- Motor Drives; Solar Applications; UPS

Type	Package	Marking	Pin 1	Pin 2
IDH10SG60C	PG-T0220-2	D10G60C	C	A

#### Maximum ratings

Parameter	Symbol	Conditions	Value	Unit
Continuous forward current	$I_F$	$T_C < 130^\circ C$	10	A
Surge non-repetitive forward current, sine halfwave	$I_{F,SM}$	$T_C = 25^\circ C, t_p = 10 \text{ ms}$	51	
		$T_C = 150^\circ C, t_p = 10 \text{ ms}$	44	
Non-repetitive peak forward current	$I_{F,max}$	$T_C = 25^\circ C, t_p = 10 \mu\text{s}$	410	
$i^2t$ value	$\int i^2 dt$	$T_C = 25^\circ C, t_p = 10 \text{ ms}$	13	$\text{A}^2\text{s}$
		$T_C = 150^\circ C, t_p = 10 \text{ ms}$	10	
Repetitive peak reverse voltage	$V_{RRM}$	$T_j = 25^\circ C$	600	V
Diode dv/dt ruggedness	$dv/dt$	$V_R = 0 \dots 480 \text{ V}$	50	$\text{V/ns}$
Power dissipation	$P_{tot}$	$T_C = 25^\circ C$	120	W
Operating and storage temperature	$T_j, T_{stg}$		-55 ... 175	$^\circ C$
Soldering temperature, w wavesoldering only allowed at leads	$T_{sold}$	1.6mm (0.063 in.) from case for 10s	260	
Mounting torque		M3 and M3.5 screws	60	Ncm

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	

### Thermal characteristics

Thermal resistance, junction - case	$R_{thJC}$		-	-	1.25	K/W
Thermal resistance, junction - ambient	$R_{thJA}$	Thermal resistance, junction- ambient, leaded	-	-	62	

**Electrical characteristics**, at  $T_j=25$  °C, unless otherwise specified

### Static characteristics

DC blocking voltage	$V_{DC}$	$I_R=0.05$ mA, $T_j=25$ °C	600	-	-	V
Diode forward voltage	$V_F$	$I_F=10$ A, $T_j=25$ °C	-	1.8	2.1	
		$I_F=10$ A, $T_j=150$ °C	-	2.2	-	
Reverse current	$I_R$	$V_R=600$ V, $T_j=25$ °C	-	0.8	90	$\mu$ A
		$V_R=600$ V, $T_j=150$ °C	-	3.3	860	

### AC characteristics

Total capacitive charge	$Q_c$	$V_R=400$ V, $I_F \leq I_{F,max}$ , $di_F/dt=200$ A/ $\mu$ s, $T_j=150$ °C	-	16	-	nC
Switching time <sup>3)</sup>	$t_c$		-	-	<10	ns
Total capacitance	$C$	$V_R=1$ V, $f=1$ MHz	-	290	-	pF
		$V_R=300$ V, $f=1$ MHz	-	40	-	
		$V_R=600$ V, $f=1$ MHz	-	40	-	

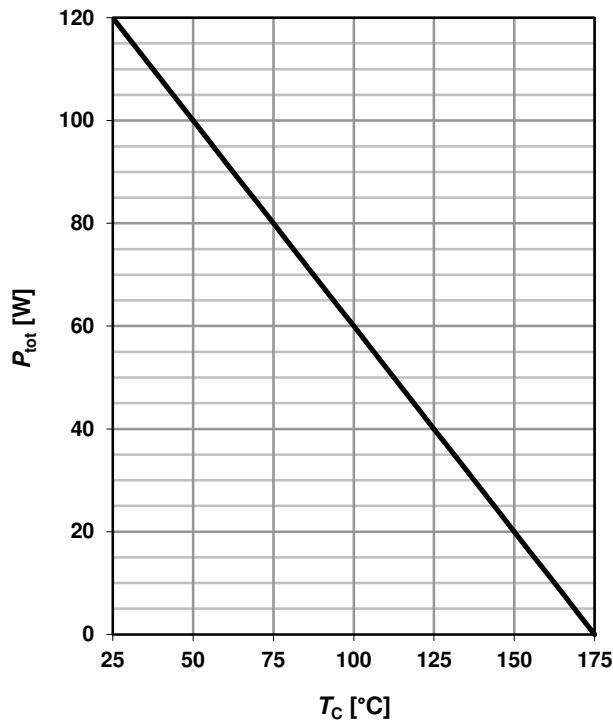
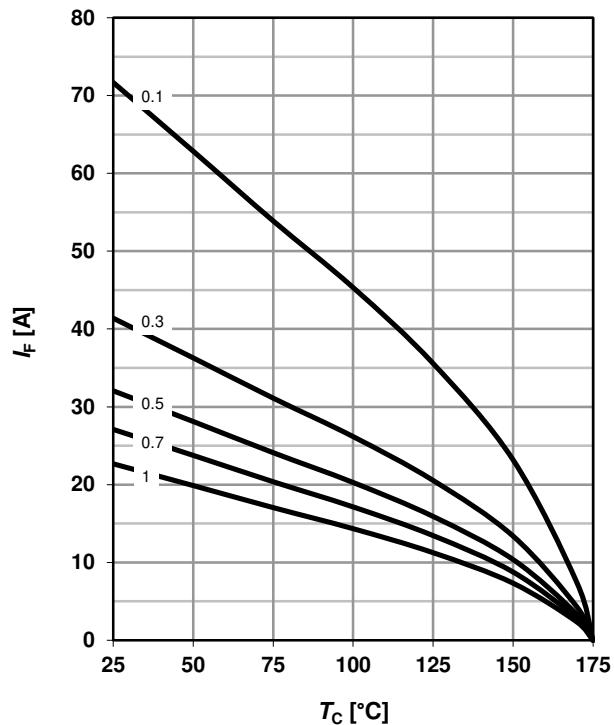
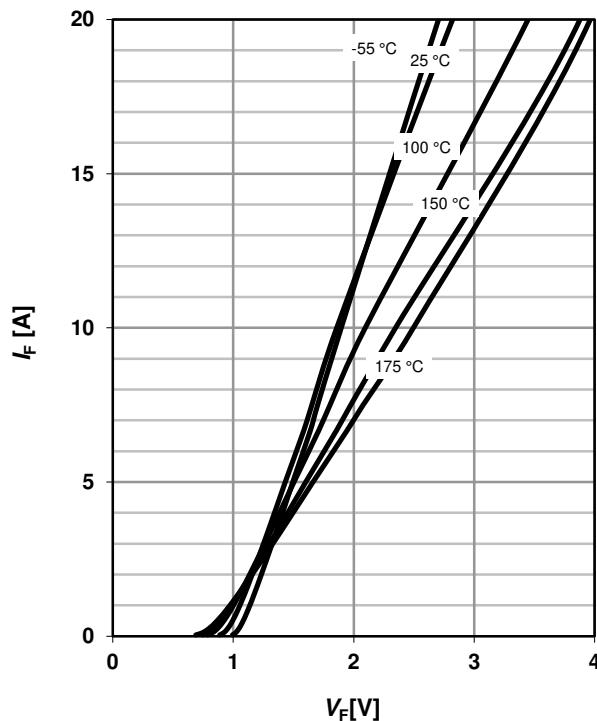
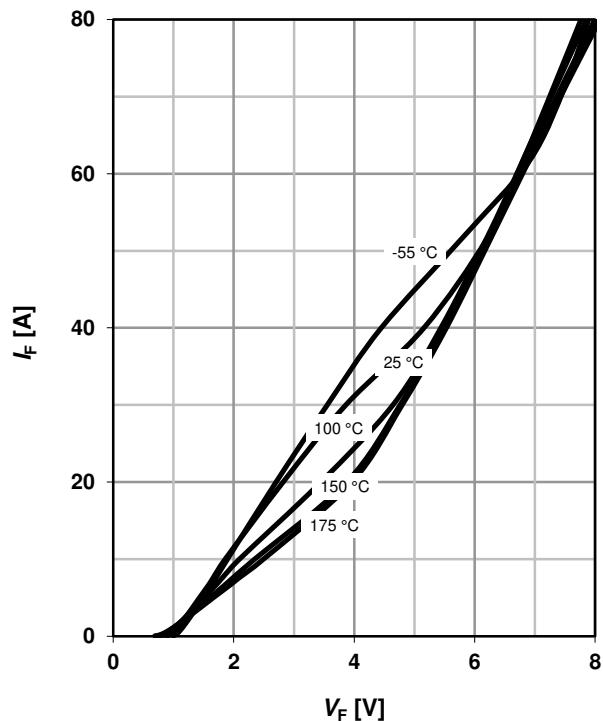
<sup>1)</sup> J-STD20 and JESD22

<sup>2)</sup> All devices tested under avalanche conditions, for a time period of 10ms, at 20mA.

<sup>3)</sup>  $t_c$  is the time constant for the capacitive displacement current waveform (independent from  $T_j$ ,  $I_{LOAD}$  and  $di/dt$ ), different from  $t_{rr}$  which is dependent on  $T_j$ ,  $I_{LOAD}$  and  $di/dt$ . No reverse recovery time constant  $t_{rr}$  due to absence of minority carrier injection.

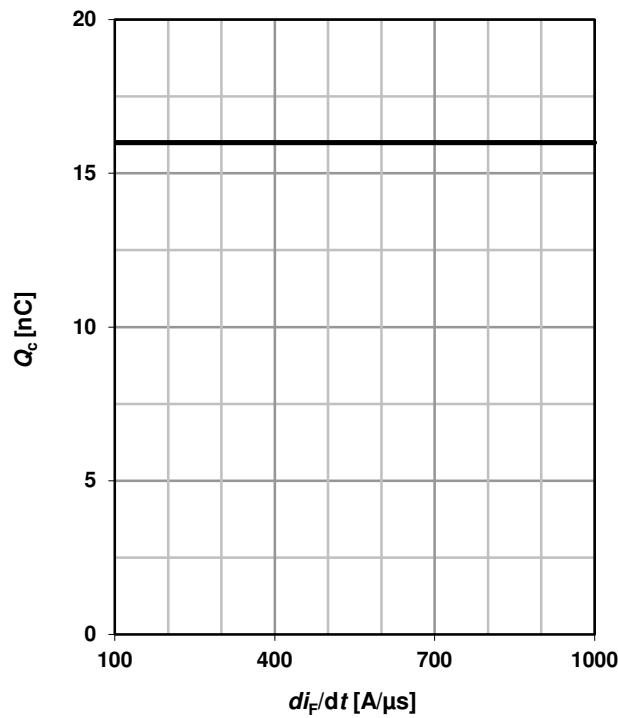
<sup>4)</sup> Under worst case  $Z_{th}$  conditions.

<sup>5)</sup> Only capacitive charge occurring, guaranteed by design.

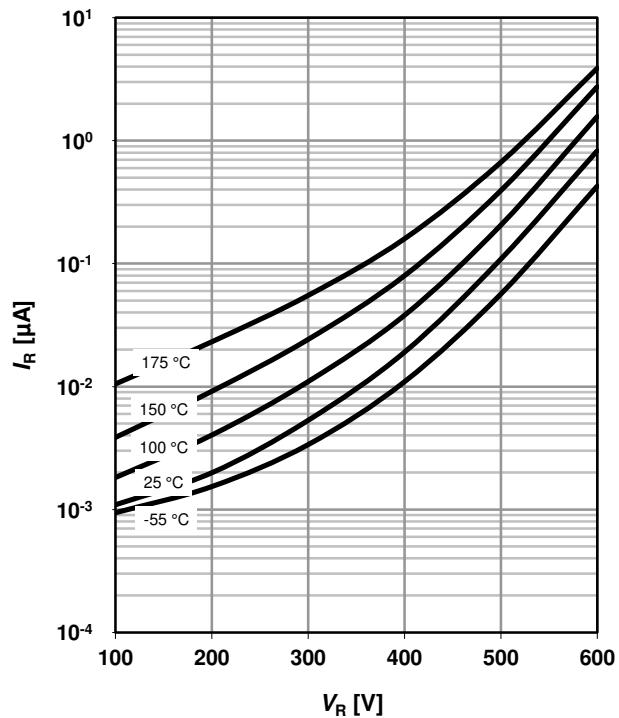
**1 Power dissipation**
 $P_{\text{tot}} = f(T_C)$ ; parameter:  $R_{\text{thJC(max)}}$ 

**2 Diode forward current**
 $I_F = f(T_C)$ ;  $T_j \leq 175$  °C; parameter:  $D = t_p/T$ 

**3 Typ. forward characteristic**
 $I_F = f(V_F)$ ;  $t_p = 400$  μs; parameter:  $T_j$ 

**4 Typ. forward characteristic in surge current mode**
 $I_F = f(V_F)$ ;  $t_p = 400$  μs; parameter:  $T_j$ 


**5 Typ. capacitance charge vs. current slope**

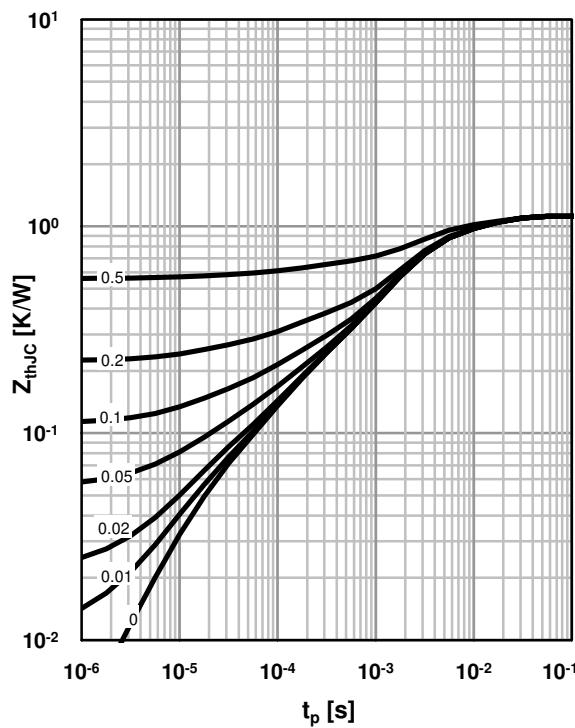
$$Q_C = f(dI_F/dt)^{5/2}; I_F \leq I_{F,\max}$$


**6 Typ. reverse current vs. reverse voltage**

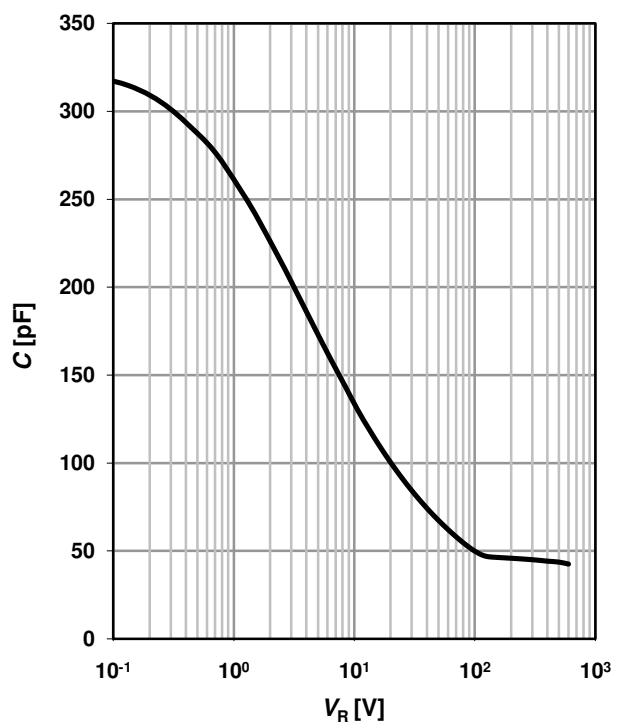
$$I_R = f(V_R); \text{ parameter: } T_j$$


**7 Typ. transient thermal impedance**

$$Z_{thJC} = f(t_p); \text{ parameter: } D = t_p/T$$

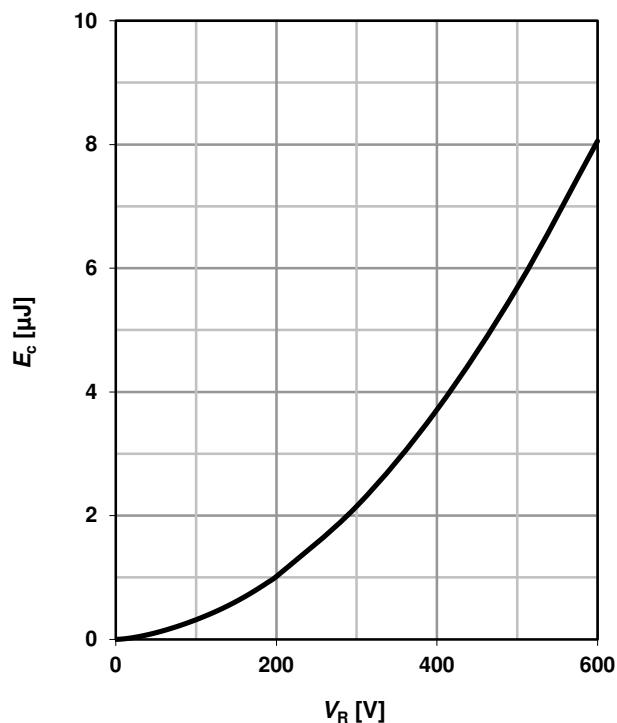

**8 Typ. capacitance vs. reverse voltage**

$$C = f(V_R); T_C = 25^\circ\text{C}, f = 1 \text{ MHz}$$

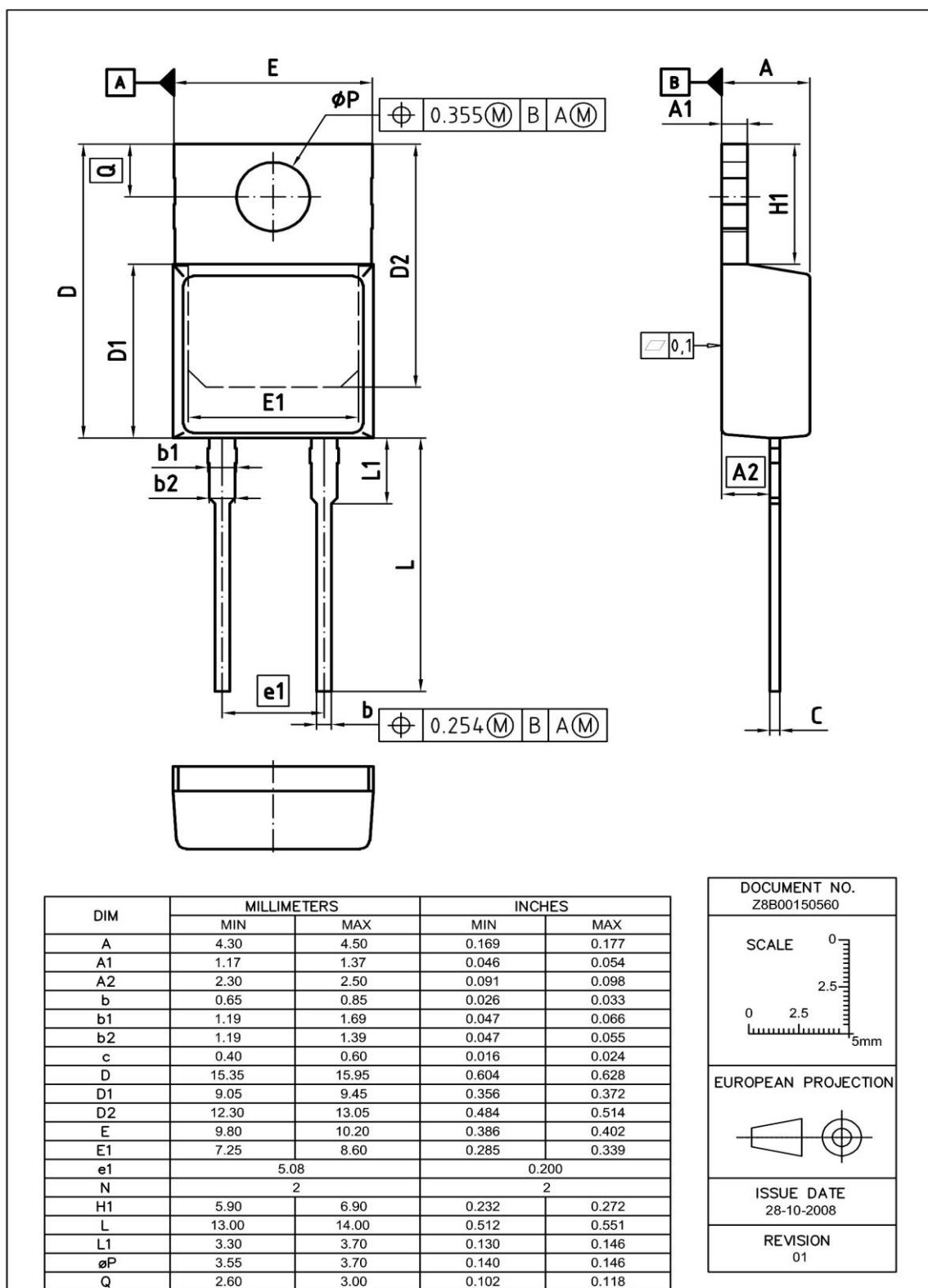


**9 Typ. C stored energy**

$$E_C = f(V_R)$$



## PG-T0220-2: Outline



Dimensions in mm/inches

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