

# HMC183QS24 / 183QS24E

v02.0805

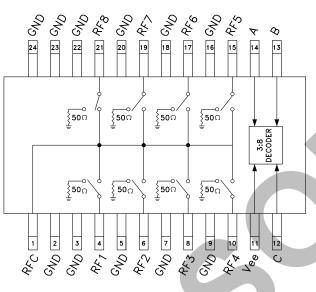


# **Typical Applications**

The HMC183QS24 / HMC183QS24E is ideal for:

Basestation Infrastructure

### **Functional Diagram**



# GaAs MMIC SP8T NON-REFLECTIVE SWITCH, DC - 2 GHz

#### Features

Low Insertion Loss (1 GHz): 0.8dB Integrated 3:8 Decoder 24 Lead QSOP Package

# **General Description**

The HMC183QS24 & HMC183QS24E are low-cost non-reflective SP8T switches in 24-lead QSOP packages for use in antenna diversity, switched filter banks, gain/attenuation selection, and general channel multiplexing applications. A 3:8 decoder is integrated on the switch, requiring only 3 control lines with a negative bias to select an RF path. The 3:8 decoder replaces 16 control lines normally required by GaAs SP8T switches. Switch outputs are terminated when "off". The QSOP24 package occupies the same area as a 14-lead SOIC. See positive bias/TTL version HMC253QS24.

# **Electrical Specifications**,

 $T_{A} = +25^{\circ}$  C, For 0/-5V Control and Vee = -5V in a 50 Ohm System

Parameter	Frequency	Min.	Тур.	Max.	Units
Insertion Loss	DC - 1.0 GHz DC - 1.5 GHz DC - 2.0 GHz		0.8 1.0 1.3	1.2 1.5 1.8	dB dB dB
Isolation	DC - 0.5 GHz DC - 1.0 GHz DC - 1.5 GHz DC - 2.0 GHz	38 32 29 26	42 36 33 30		dB dB dB dB
Return Loss RFC	DC - 1.0 GHz DC - 1.5 GHz DC - 2.0 GHz	16 10 7	20 14 11		dB dB dB
RF 1-8 'ON' and 'OFF'	DC - 1.0 GHz DC - 1.5 GHz DC - 2.0 GHz	12 10 10	15 13 13		dB dB dB
Input Power for 1 dB Compression	50 MHz 0.5 - 2.0 GHz		18 20		dBm dBm
Input Third Order Intercept (Two-Tone Input Power = +7 dBm Each Tone)	50 MHz 0.5 - 2.0 GHz	30 37	35 42		dBm dBm
Switching Characteristics	DC - 2.0 GHz				
tRISE, tFALL (10/90% RF) tON, tOFF (50% CTL to 10/90% RF)			35 50		ns ns

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SWITCHES - SMT



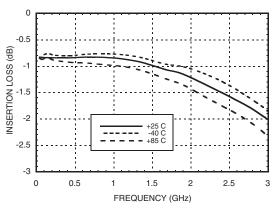
# HMC183QS24 / 183QS24E

SWITCH, DC - 2 GHz

GaAs MMIC SP8T NON-REFLECTIVE

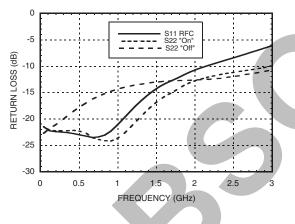
# RoHS V

### **Insertion Loss**



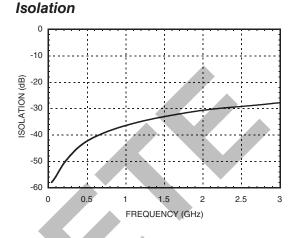
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### **Return Loss**

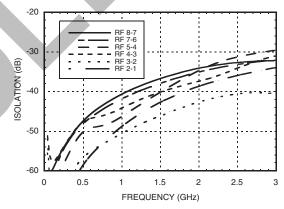


### **Bias Voltage & Current**

	Ve	e Range =	-5.0 Vdc ± 1	0%
Vee (Vdc)			(Typ.) nA)	lee (Max.) (mA)
-5.0		e	6.0	9.0



# Isolation Between Several RF I/Os



# **Control Voltages**

State	Bias Condition
Low	0 to -3 VDC 2 70 uA Typ.
High	-5 to -4.2 VDC @ 5 uA Typ.

# 10

# SWITCHES - SMT

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# GaAs MMIC SP8T NON-REFLECTIVE SWITCH, DC - 2 GHz

### Absolute Maximum Ratings

Bias Voltage Range (Port Vee)	-7.0 Vdc
Control Voltage Range (A & B)	Vee -0.5 to +1.0 Vdc
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C
Maximum Input Power	+26 dBm (<500 MHz) +29 dBm (>500 MHz)



ELECTROSTATIC SENSITIVE DEVICE
OBSERVE HANDLING PRECAUTIONS

.345 .337 8.75 8.55

19

18

20

<u>⁄4</u>

15

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#### **Control Input** Signal Path State С RFCOM to: А В High High High RF1 High High RF2 Low Low High RF3 High Low High RF4 Low Low RF5 High High Low High Low RF6

Low

Low

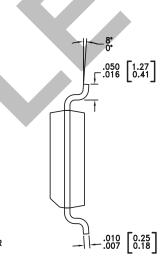
RF7

RF8

**Truth Table** 

High

Low



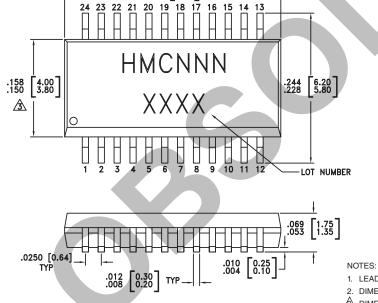
Low

Ľow

# **Outline Drawing**

23 22

24



1. LEADFRAME MATERIAL: COPPER ALLOY

2. DIMENSIONS ARE IN INCHES [MILLIMETERS].

DIMENSION DOES NOT INCLUDE MOLDFLASH OF 0.15mm PER SIDE.

A DIMENSION DOES NOT INCLUDE MOLDFLASH OF 0.25mm PER SIDE.

ALL GROUND LEADS MUST BE SOLDERED TO PCB RF GROUND. 5.

# Package Information

Part Number	Package Body Material	Leadframe Plating	MSL Rating	Package Marking [3]
HMC183QS24	Low Stress Injection Molded Plastic Silica and Silicon Impregnated	Sn/Pb Solder	MSL1 <sup>[1]</sup>	HMC183 XXXX
HMC183QS24E	RoHS-compliant Low Stress Injection Molded Plastic Silica and Silicon Impregnated	100% Matte Tin	MSL1 <sup>[2]</sup>	HMC183 XXXX

[1] Max peak reflow temperature of 235 °C

[2] Max peak reflow temperature of 260 °C

[3] 4-Digit lot number XXXX

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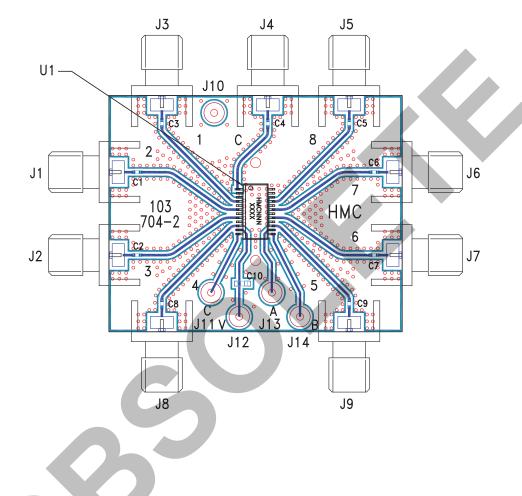
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# GaAs MMIC SP8T NON-REFLECTIVE SWITCH, DC - 2 GHz

## **Evaluation Circuit Board**



# List of Materials for Evaluation PCB 103706 [1]

	(	
Item		Description
J1 - J9		PCB Mount SMA RF Connector
J10 - J14		DC Pin
C1 - C9		100 pF capacitor, 0402 Pkg.
C10		10,000 pF capacitor, 0603 Pkg.
U1		HMC183QS24 / HMC183QS24E SP8T Switch
PCB*		103704 Evaluation PCB

Reference this number when ordering complete evaluation PCB
Circuit Board Material: Rogers 4350

The circuit board used in the final application should be generated with proper RF circuit design techniques. Signal lines at the RF port should have 50 ohm impedance and the package ground leads and package bottom should be connected directly to the ground plane similar to that shown above. The evaluation circuit board shown above is available from Hittite Microwave Corporation upon request.

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