



Description

The AS321 is a high gain and internally frequency compensated operational amplifier specifically designed to operate from a single power supply. Operation from split power supply is also possible and the low power supply current drain is independent of the magnitude of the power supply voltages. Typical applications include battery charger, active filters, general purpose controllers and most conventional operational amplifier circuits.

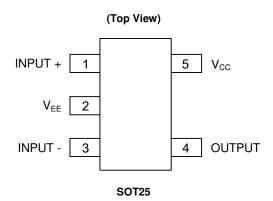
The AS321 is compatible with industry standard 321.

The AS321 is available in SOT25 package.

Features

- · Excellent Phase Margin: 60 deg.
- Large Voltage Gain: 100dB (Typical)
- Low Input Bias Current: 20nA (Typical)
- Low Input Offset Voltage: 2mV (Typical)
- Low Supply Current: 0.35mA at V_{CC} = 5V
- Wide Power Supply Voltage:
 - Single Supply: 3V to 36V
 - Dual Supplies: ±1.5V to ±18V
- Wide Input Common Mode Voltage Range: 0V to V_{CC}-1.5V
- Lead-Free Packages: SOT25
 - Totally Lead-Free; RoHS Compliant (Notes 1 & 2)
- Lead-Free Packages, Available in "Green" Molding Compound: SOT25
 - Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
 - Halogen and Antimony Free. "Green" Device (Note 3)

Pin Assignments



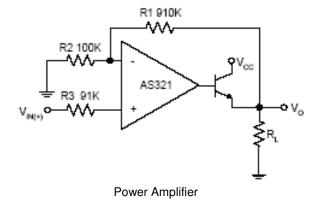
Applications

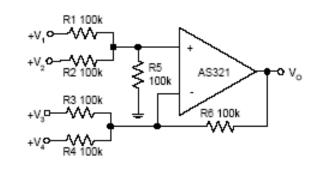
- Battery Charger
- Active Filters
- General Purpose Controllers, Instruments

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

Typical Applications Circuit

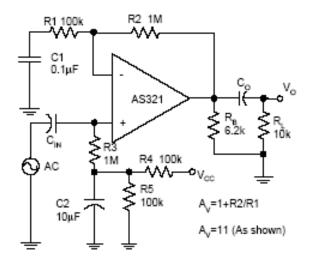




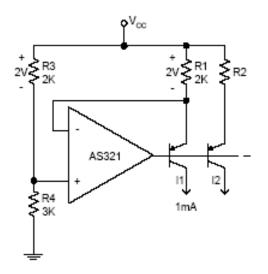
DC Summing Amplifier



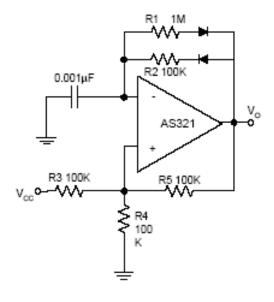
Typical Applications Circuit (Cont.)



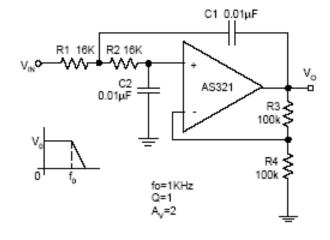
AC Coupled Non-Inverting Amplifier



Fixed Current Sources



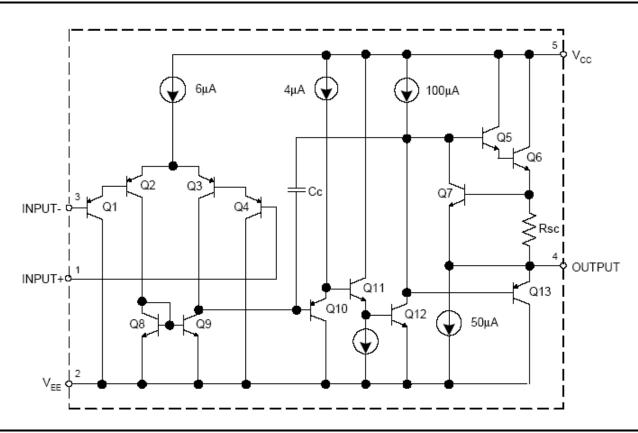
Pulse Generator



DC Coupled Low-Pass Active Filter



Functional Block Diagram



Absolute Maximum Ratings (Note 4)

Symbol	Parameter	Rating	Unit
V _S (V _{CC} -V _{EE})	Power Supply Voltage	40	٧
V _{ID}	Differential Input Voltage	40	٧
V _{IN}	Input Voltage	-0.3 to 40	٧
θJA	Thermal Resistance to Ambient	260	°C/W
TJ	Operating Junction Temperature	+150	°C
T _{STG}	Storage Temperature Range	-65 to +150	°C
T _{LEAD}	Lead Temperature (Soldering, 10 Seconds)	+260	°C

Note 4: Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

Recommended Operating Conditions

Symbol	Parameter	Min	Мах	Unit	
V _{CC}	Supply Voltage	3	36	٧	
T _A	Ambient Operating Temperature Range	-40	+85	°C	

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Electrical Characteristics (Limits in standard typeface are for $T_A = +25^{\circ}\text{C}$, **bold** typeface applies over -40°C to +85°C (Note 5), $V_{CC} = 5V$, $V_{EE} = 0V$, $V_O = 1.4V$, unless otherwise specified.)

Symbol	Parameter		Conditions	Min	Тур	Max	Unit
\/ Input Offset \/altage		Innut Offert Veltage	$V_O = 1.4V, R_S = 0\Omega, V_{CC} = 5V \text{ to } 30V$		2	5	\/
V _{IO}	Input Offset Voltage		(Note 6)	_	_	7	mV
$\Delta V_{IO}/\Delta T$	Average Temperature Coefficient of Input Offset Voltage		T _A = -40°C to +85°C	_	7	_	μV/°(
		-1		_	20	100	
IBIAS	Input Bias Currer	1[$I_{IN}+$ or $I_{IN}-$, $V_{CM}=0V$	_	_	200	nA
La	Innut Offeet Curr	ant		_	5	30	nA
I _{IO}	Input Offset Curr	ent	$I_{IN}+-I_{IN}-,\ V_{CM}=0V$	_	_	100	
V_{CM}	Input Common M Range (Note 7)	lode Voltage	V _{CC} = 30V, CMRR ≥ 50dB	0	_	V _{CC} -1.5	V
			R _L = ∞, V _{CC} = 5V	_	0.35	0.80	
l	Supply Current		nL - w, vcc = 5v	_	0.45	1.0	m 1
I _{CC}	Supply Current		R _L = ∞, V _{CC} = 30V	_	0.45	1.2	mA
			nL - ~, vcc = 30 v	_	0.65	1.5	
C	Lawra O'maal Valtana O	Large Circal Valters Cair	V 15V V- 1V to 11V D > 2k0	85	100	_	dB
G∨	Large Signal Vol	tage Gain	$V_{CC} = 15V$, $V_O = 1V$ to 11V, $R_L \ge 2k\Omega$	80	_	_	
CMDD	Common Mode I	nmon Mode Rejection Ratio V _C	$V_{CM} = 0V$ to $(V_{CC}-1.5)V$, $R_S \le 10k\Omega$	60	70	_	dB
CMRR	Common Mode F			60	_	_	
DODD	Davies Comple Da	Owner Delication Detic	$V_{CC} = 5V$ to 30V, $R_S \le 10k\Omega$	70	100	_	dB
PSRR	Power Supply Re	ejection Ratio		60	_	_	
			V _{IN+} = 1V, V _{IN} - = 0V, V _{CC} = 15V, V _O = 2V	20	40	_	mA
ISOURCE		Source		20	_	_	
	Output Current	Current Sink	$V_{IN+} = 0V$, $V_{IN-} = 1V$, $V_{CC} = 15V$, $V_{O} = 2V$	10	15	_	
Isink	Output Current			5	_	_	mA
ISHAK			$V_{IN+} = 0V$, $V_{IN-} = 1V$, $V_{CC} = 15V$, $V_{O} = 0.2V$	12	50	_	μΑ
I _{SC}	Output Short Circuit Current to Ground		V _{CC} = 15V	_	40	60	mA
			V 20V B. = 2k0	26	_	_	
V		$V_{CC} = 30V, R_L = 2k\Omega$	26	_	_	.,	
V _{OH}	Output Valtage C	`wina	$V_{CC} = 30V$, $R_L = 10k\Omega$	27	28	_	V
	Output Voltage S	owing		27	_	_	
V _{OL}				_	5	20	mV
			$V_{CC} = 5V$, $R_L = 10k\Omega$	_	_	30	
THD	Total Harmonic Distortion		$f = 1kHz, AV = 20dB, R_L = 2k\Omega,$ $V_O = 2Vp-p, C_L = 100pF, V_{CC} = 30V$		0.015		%
Фм	Phase Margin		_		60		Deg
θЈС	Thermal Resistance (Junction to Case)		SOT25	_	101	_	°C/V

Notes:

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^{5.} Limits over the full temperature are guaranteed by design, but not tested in production.

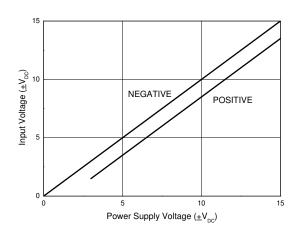
^{6.} Over the full input common-mode range 0V to $V_{\text{CC}}\text{-}1.5V$ (at +25°C).

^{7.} The input common-mode voltage of either input signal voltage should not be allowed to go negatively by more than 0.3V (at +25°C). The upper end of the common-mode voltage range is V_{CC}-1.5V (at +25°C), but either or both inputs can go to +36V without damages, independent of the magnitude of the V_{CC}.

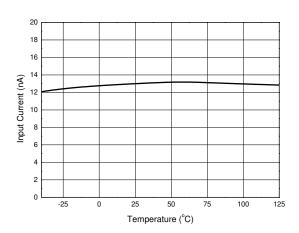


Performance Characteristics

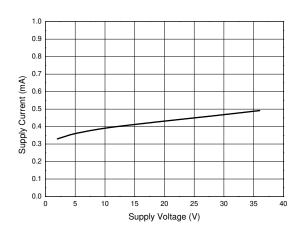
Input Voltage Range



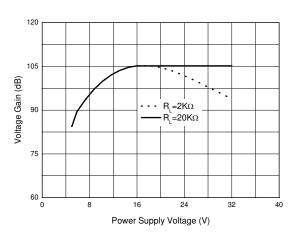
Input Current



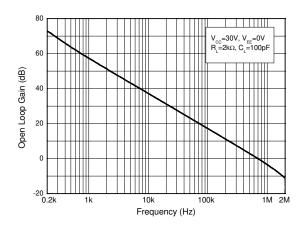
Supply Current



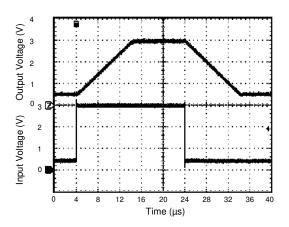
Voltage Gain



Open Loop Gain vs. Frequency



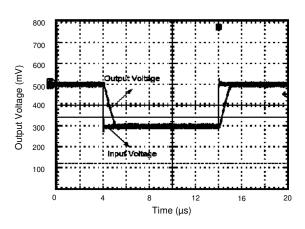
Voltage Follower Pulse Response



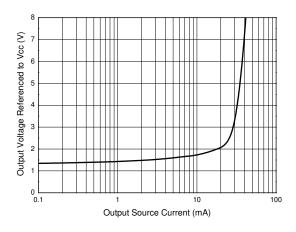


Performance Characteristics (Cont.)

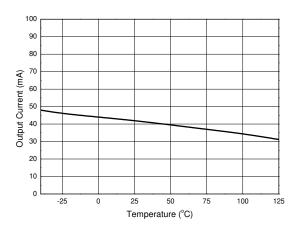
Voltage Follower Pulse Response (Small Signal)



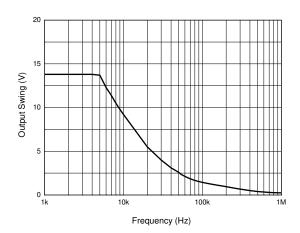
Output Characteristics: Current Sourcing



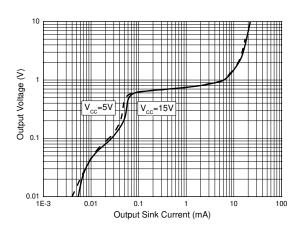
Current Limiting



Large Signal Frequency Response

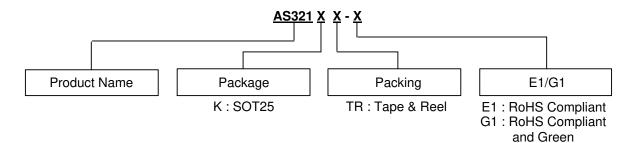


Output Characteristics: Current Sinking





Ordering Information





Part Number	Package (Note 9)	RoHS Compliant Lead Free/ Green	Temperature Range	Marking ID	Tape and Reel Quantity	Status (Note 8)	Alternative
AS321KTR-E1	SOT25	Lead Free	-40 to +85°C	E6T	3000	NRND	AS321KTR-G1
AS321KTR-G1	SOT25	Green	-40 to +85°C	G6T	3000	In Production	_

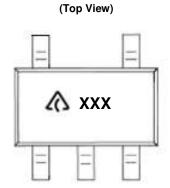
Notes:

8. AS321KTR-E1 (Lead Free package) is Not Recommended for New Design (NRND), recommended alternative is AS321KTR-G1 (Green package).

9. For packaging details, go to our website at: https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

(1) SOT25



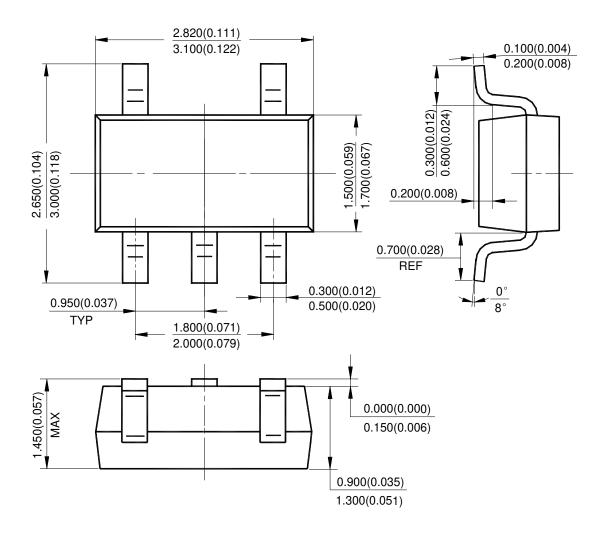
(A): Logo

XXX: Marking ID (See Ordering Information)



Package Outline Dimensions (All dimensions in mm(inch).)

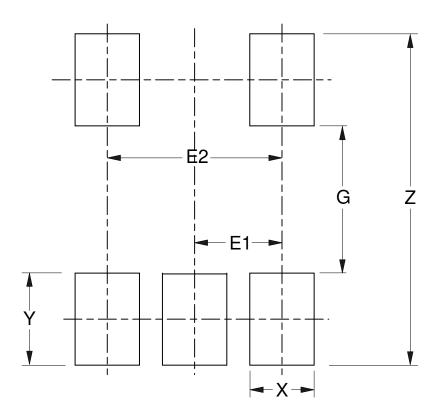
(1) Package Type: SOT25





Suggested Pad Layout

(1) Package Type: SOT25



Dimensions	Z	G	X	Υ	E1	E2
Difficitions	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)
Value	3.600/0.142	1.600/0.063	0.700/0.028	1.000/0.039	0.950/0.037	1.900/0.075



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