

The EVAL-ADV7280AEBZ evaluation kit is the platform

video decoder. The EVAL-ADV7280AEBZ evaluation kit

This user guide provides a detailed overview of the EVAL-

ADV7280AEBZ evaluation board hardware and the software

The ADV7280A data sheet and the ADV7280A/ADV7281A/

with this user guide when using the EVAL-ADV7280AEBZ

EngineerZone can be accessed to find additional information

6178-001

ADV7282A Device Manual should be consulted in conjunction

provided by Analog Devices, Inc., to evaluate the ADV7280A

contains an EVAL-ADV7280AEBZ evaluation board and all of

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Evaluation Board for the ADV7280A 10-Bit, 4× Oversampled SDTV Video Decoder with Deinterlacer

GENERAL DESCRIPTION

its necessary peripherals.

required to use it.

evaluation board.

about the ADV7280A.

FEATURES

Four video input ports capable of accepting any of the following formats: single-ended CVBS, S-video (Y/C), and component (YPbPr) Digital (ITU-R BT.656) and YPbPr outputs

EVALUATION BOARD KIT CONTENTS

EVAL-ADV7280AEBZ evaluation board 7.5 V power supply block USB cable

HARDWARE NEEDED

Source of one or more of the following video inputs: singleended CVBS, S-Video (Y/C), and/or YPbPr

PC TV or display with YPbPr input CVBS input cable(s) S-Video cable(s) Component cable(s)

SOFTWARE NEEDED

DVP Eval Software ADV7280A scripts Windows OS

PHOTOGRAPH OF THE EVAL-ADV7280AEBZ

Figure 1.

EVAL-ADV7280AEBZ User Guide

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REVISION HISTORY

8/2017—Revision 0: Initial Version

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EVALUATION BOARD HARDWARE EVALUATION BOARD OVERVIEW

The EVAL-ADV7280AEBZ evaluation board features an ADV7280A video decoder and an ADV7391 video encoder. Four analog video inputs (A_{IN}1 to A_{IN}4) are connected to the ADV7280A video decoder. The ADV7280A can receive analog video in several different format configurations; hardware configuration changes can be required to support certain formats, for example, single-ended composite video burst sync (CVBS) versus differential CVBS (see Table 1). The ADV7280A converts the analog video received into an ITU-R BT.656compatible digital stream. The digital stream is connected to the ADV7391 video encoder. The ADV7391 converts the digital stream back into analog video that is output via three analog video outputs.

EVALUATION BOARD DESCRIPTION

This section outlines how to power up, communicate with, and use the evaluation board. For an outline of the evaluation board connections, see Figure 2.

Power Supply

To power up the evaluation board, connect a mains cable to the 7.5 V power supply block included in the EVAL-ADV7280AEBZ evaluation kit. Connect the output jack plug of the 7.5 V power supply block to the input power connector (J8) on the evaluation board. LED D6 illuminates when the power supply is enabled and successfully connects to the evaluation board.

Only use the 7.5 V power supply block provided with the evaluation kit to power the evaluation board.

Communicating with the Evaluation Board

To establish communication with the evaluation board, connect the USB cable included in the EVAL-ADV7280AEBZ evaluation kit to a computer with the DVP Eval Software installed. Connect the USB cable to the USB connector (J7) on the evaluation board. LED D7 illuminates when the USB cable successfully connects between an active USB port and the evaluation board.

Connecting Input Video

Connect an analog video input(s) to the desired analog input $(A_{IN}1 \text{ to } A_{IN}4)$ of the evaluation board. Refer to Table 1 to determine how different types of input (for example, single-ended CVBS and S-Video) connect to the evaluation board. Refer to the ADV7280A data sheet and the ADV7280A/ADV7281A/ADV7282A Device Manual for more information on input muxing options.

Connecting Output Video

To observe the output of the evaluation board, connect a YPrPb cable from the analog video output connector (J1) of the evaluation board to a television or other sink device. Ensure the television or other sink device supports the output format of the evaluation board (480i/576i).

Probing the Digital Video Stream

The digital output stream of the ADV7280A can be probed with an oscilloscope or logic analyzer via a header (J5) on the evaluation board. There are also individual test points for the LLC, HS and VS/FIELD/SFL signals.

Other Considerations

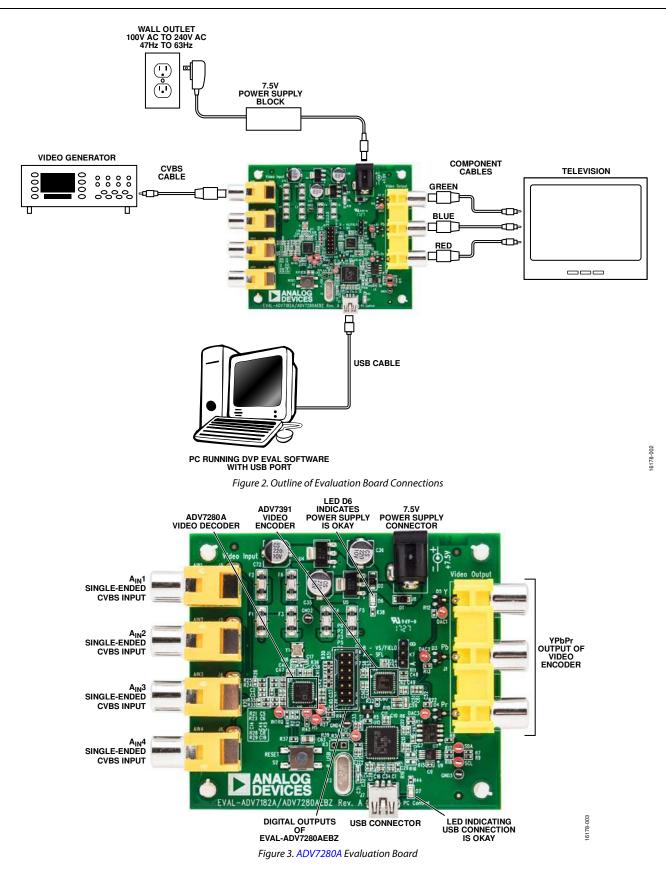
The 28.63636 MHz crystal (Y1) on the evaluation board does not oscillate until the ADV7280A is configured (see the Configuring the Evaluation Board section). The I²C master works independently of the crystal, using a ring oscillator in the ADV7280A.

Specific components on the evaluation board are outlined in Table 2 and highlighted in Figure 3. Additional details on components are outlined in Table 3.

Table 1. Analog Video Input Format Configurations for the EVAL-ADV7280AEBZ Evaluation Board

Analog Video Format	A _{IN} 1	A _{IN} 2	A _{IN} 3	A _{IN} 4
Single-ended CVBS	Single-Ended CVBS Input 1	Single-Ended CVBS Input 2	Single-Ended CVBS Input 3	Single-Ended CVBS Input 4
Differential CVBS	Not applicable	Not applicable	Not applicable	Not applicable
S-Video (Y/C)	S-Video Input 1 (Y channel)	S-Video Input 1 (C channel)	S-Video Input 2 (Y channel)	S-Video Input 2 (C channel)
YPbPr (component)	YPbPr1 Input 1 (Y channel)	YPbPr1 Input 1 (Pb channel)	YPbPr1 Input 1 (Pr channel)	Not applicable

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Designator	Function	Description
J2 to J4, J6	Analog video inputs	Analog video inputs (A_{IN} 1 to A_{IN} 4) connected to the ADV7280A video decoder.
J1	Analog video output	Analog video outputs connected to the ADV7391 encoder.
J8	Power	Connection for 7.5 V power supply. A 7.5 V power supply block is included in the EVAL-ADV7280AEBZ evaluation kit.
D6	Power enabled LED	This LED illuminates when the 7.5 V supply is connected and enabled.
J7	USB	Connecting a USB cable between this connector and a PC with DVP Eval Software and ADV7280A scripts installed allows control of the evaluation board. See the Evaluation Board Software section for more information on the DVP Eval Software and ADV7280A scripts.
D7	USB connected LED	The LED illuminates when the USB cable is connected between an active USB port on a PC and the evaluation board.

Table 2. Important Evaluation Board Components Reference

Table 3. Additional Evaluation Board Components

Reference		
Designator	Function	Description
J5	P0 to P7 digital outputs	P0 to P7 digital outputs. Digital video output from the ADV7280A.
LLC	LLC output	LLC output from the ADV7280A.
HS	HS output	HS output from the ADV7280A.
VS	VS/FIELD/SFL output	VS/FIELD/SFL output from the ADV7280A.
INTRQ	INTRQ output	Interrupt output from the ADV7280A.
DAC 1 to DAC 3	DAC 1 to DAC 3	Test points. The YPrPb outputs from the ADV7391 are accessible via the DAC1, DAC2, and DAC3 test points.
Reset and S2	Reset	The evaluation board can be reset by pressing and releasing the S2 push button. The evaluation board can also be reset by momentarily connecting the Reset test point to 0 V.
SDA and SCL	I ² C communication bus	Test points. The SDA (I ² C data) and SCL (I ² C clock) test points provide access to the I ² C communication bus on the evaluation board. This allows an external I ² C master to be connected instead of using a PC to configure the evaluation board.
K3	EEPROM programming	Never short Jumper K3 and only employ K3 during initial programming. The jumper can disable the USB interface on the evaluation board.
К7	Synchronization control	Jumper K7 controls which synchronization signal sent from the VS/FIELD/SFL pin of the ADV7280A to the ADV7391 encoder. If the VS/FIELD/SFL pin is configured for either vertical synchronization or field synchronization, place a jumper in Position A. If the VS/FIELD/SFL pin is configured for the subcarrier frequency lock (SFL) signal, place a jumper in Position B. Do not insert Jumper K7 if none of the mentioned cases are in use.

EVALUATION BOARD SOFTWARE

SOFTWARE REQUIRED

To complete the initial setup of the evaluation board, download the following:

- ADV7280A script files
- DVP Eval Software

DOWNLOADING THE ADV7280A SCRIPT FILES

To download the ADV7280A script files, complete the following steps:

- 1. Go to the ADV7280A product page.
- 2. Download the ADV7280A_Cust.zip file.
- 3. Unzip the ADV7280A_Cust.zip file.

DOWNLOADING DVP EVAL SOFTWARE

To download the DVP Eval Software, complete the following steps:

- 1. Open the Install DVP Eval Software thread on EngineerZone.
- 2. Download the Install DVP Eval Latest Source 10-14-11.exe.zip file.
- 3. Unzip the Install DVP Eval Latest Source 10-14-11.exe.zip file.

INSTALLING DVP EVAL SOFTWARE

To install the DVP Eval Software, complete the following steps:

- 1. Run the executable file Install DVP Eval Latest Source 10-14-11.exe.zip.
- 2. Read the **Software License Agreement**. If in agreement, click the **I Agree** button.
- 3. Select the desired **Desktop** or **Start Menu** shortcuts and click the **Next** button.
- Select an installation destination folder and click the Install button (see Figure 4). It is recommended to use the default destination folder. Selecting a different destination folder can cause compatibility issues with some versions of Windows* OS.
- 5. Restart the PC after installing the DVP Eval Software.

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n the following folder. To install er folder. Click Install to start the
10-14-11 Browse
re

LOADING THE ADV7280A SCRIPT FILES

This section describes how to combine the ADV7280A script files with the DVP Eval Software.

- 1. If possible, disconnect the PC from the internet, as some automatic backup agents can interfere with the script file loading process.
- Copy the unzipped ADV7280A_Cust folder to the following directory: C:\Documents and Settings\USER_NAME\My Documents\Analog Devices\DVP Eval Latest Source 10-14-11\xml\New Boards.

The location of this folder is influenced by the install location of the DVP Eval Software and USER_NAME must be defined by the user.

- Open the DVP Eval Software by selecting Start > All Programs > Analog Devices > DVP Eval Latest Source 10-14-11.
- Select File > Update Boards to combine the ADV7280A script files with the DVP Eval Software (see Figure 5).



Figure 5. Update Board Files on the DVP Eval Software

5. After the **Update Boards** process completes, click **OK** on the **Update Boards Successful** window. The PC can now reconnect to the internet if it is disconnected.

CONFIGURING THE EVALUATION BOARD

After connecting and powering up the hardware and downloading and installing the software, begin using the evaluation board.

To configure the evaluation board, complete the following steps:

- 1. Select Start > All Programs > Analog Devices > DVP Eval Latest Source 10-14-11.
- 2. Click the **Choose Board** button in the top left corner of the DVP Eval Software window to open the **Board Selector window** (see Figure 6).

		_	
Rx	MotherBoard	Tx	
lone	None III N	ine :	
ADV7280A_CUST			
ADV7280AM_OUST ADV7280LAM_OUST ADV7282AM_OUST ADV7282A_CUST			
Cano	Auto Select Boards	Loud	

Figure 6. Board Selector Window of DVP Eval Software

- Select ADV7280A_CUST in the Rx list box of the Board Selector window, select None in the MotherBoard list box, and select None in the Tx list box.
- 2. Click the **Load** button. A window similar to Figure 7 appears.
- Click Scripts > ADV7280A_CUST to select and run a script to configure the evaluation board (see Figure 8).
- 4. To monitor the registers of the ADV7280A or the ADV7391, click the associated device tab within the DVP Eval Software (see Figure 8).

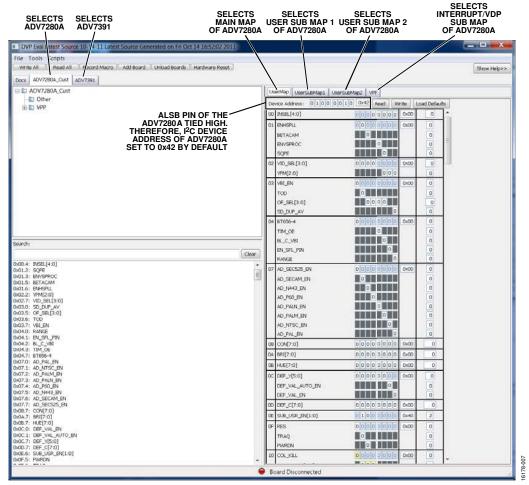


Figure 7. DVP Eval Software after Connecting the ADV7280A Evaluation Board

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File Tools So	cnpts										
Write All	Refresh Scripts		d Boards Hardware Reset								Show Help>
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E ADV7.	Edit Py Script	Ctrl+E		1 U	serMap Use	rSubMan1	UserSubMap2 VPP				
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			05_CVBS_FAST_Switch_12P			Auto	detect_CVBS_Single_Ended_Ir	Ain_4	YPbPr_O	ut	
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han an a					BL_C_VBI				0		
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0x01.6: ENHSPLL 0x02.2: YPM[2:0					AD_N443_6		0		0		
02.7: VID_SEL	[3:0]				AD_P60_EN				0		
0:03.5: OF_SEL					AD PALM			2	0		
0x03.6: TOO 0x03.7: VBLEN					AD_NTSC_			5	0		
0x04.0: RANGE 0x04.1: EN_SFL_	P/N				AD_PAL_EN				0		
0x04.2: BL_C_VB	4			08	CON[7:0]		000000000	0x00	0		
0x04.3: TIM_OE 0x04.7: BT656-4				0A	BRI[7:0]		00000000	0x00	0		
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Dx07.2: AD_PALM Dx07.3: AD_PALM	1 EN			00	DEF_Y[5:0]	i.	00000000	0x00	0	1	
0x07.4: AD_P60_	EN				DEF_VAL_A	NTO_EN			0		
0x07.5: AD_N443 0x07.6: AD_SEC/	AM_EN			L	DEF_VAL_E				0	4	
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0x00.7: DEF_C[7 0x0E.6: SUB_USA	:0]			L	PWRDN				0	-	
NOF.5: PWRDN	(Define)			10	COL_KILL			0x00	0	-	

Figure 8. Running ADV7280A Script on DVP Eval Software

I²C refers to a communications protocol originally developed by Philips Semiconductors (now NXP Semiconductors).

ESD Caution ESD (electro circuitry, dan

ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

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