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Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (_), the underscore (_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.onsemi.com. Please email any questions regarding the system integration to Fairchild_questions@onsemi.com.

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SEMICONDUCTOR®

November 2013

FQNL2N50B **N-Channel QFET® MOSFET**

500 V, 0.35 A, 5.3 Ω

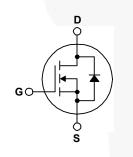
Description

This N-Channel enhancement mode power MOSFET is • 0.35 A, 500 V, $R_{DS(on)}$ = 5.3 Ω (Max.) @ V_{GS} = 10 V, produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state \cdot Low Gate Charge (Typ. 6 nC) resistance, and to provide superior switching performance • Low Crss (Typ. 4 pF) and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power factor correction (PFC), and electronic lamp ballasts.

Features

- I_D = 0.175 A





Absolute Maximum Ratings T_c = 25°C unless otherwise noted.

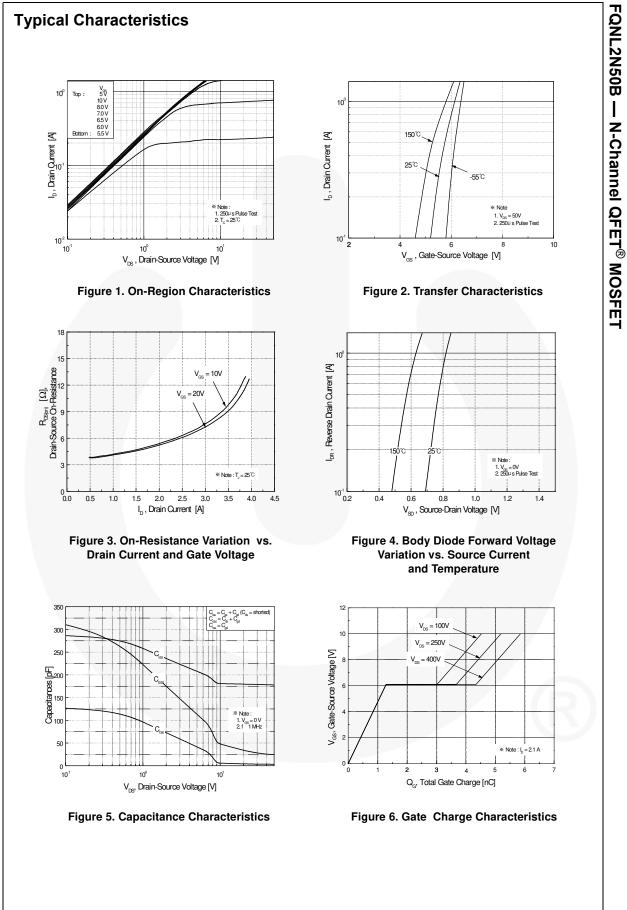
Symbol	Parameter	FQNL2N50BTA	Unit	
V _{DSS}	Drain-Source Voltage	500	V	
I _D	Drain Current - Continuous (T _C = 25°C)		0.35	А
	- Continuous (T _C = 100°C)		0.22	A
I _{DM}	Drain Current - Pulsed	Note 1)	1.4	A
V _{GSS}	Gate-Source Voltage		± 30	V
I _{AR}	Avalanche Current	Note 1)	0.35	Α
E _{AR}	Repetitive Avalanche Energy	Note 1)	0.15	mJ
dv/dt	Peak Diode Recovery dv/dt	Note 2)	4.5	V/ns
PD	Power Dissipation ($T_C = 25^{\circ}C$)		1.5	W
	- Derate above 25°C		0.012	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to +150	°C	
TL	Maximum lead temperature for soldering,		300	°C
'L	1/8" from case for 5 seconds.	300	U	

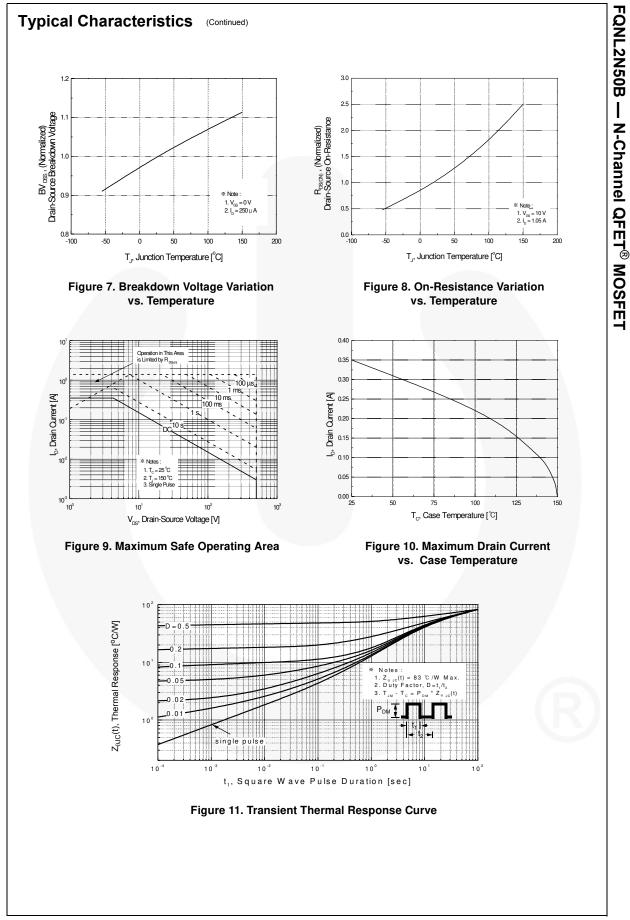
Thermal Characteristics

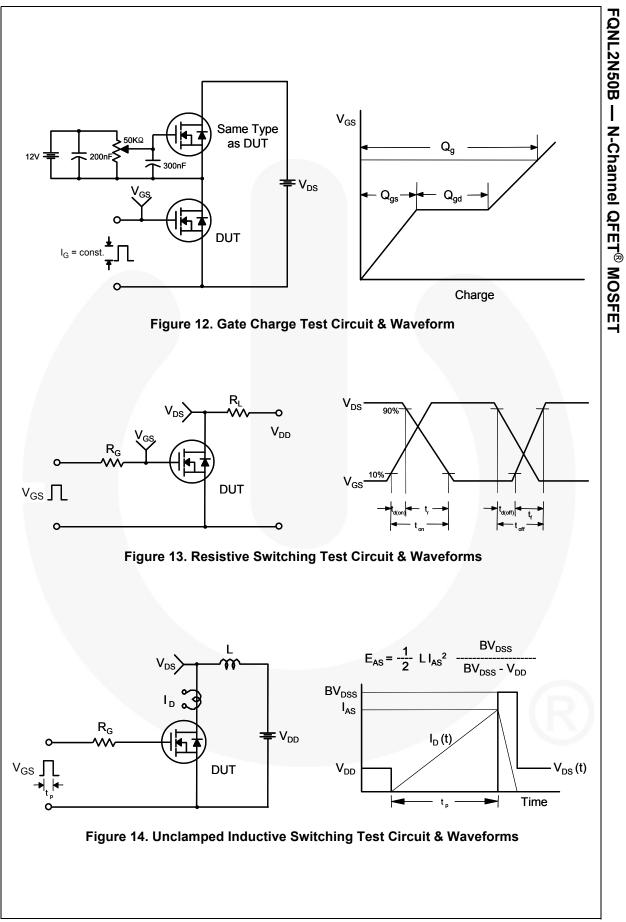
Symbol	Parameter	FQNL2N50BTA Unit			
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction-to-Ambient, Max.	83	°C/W		

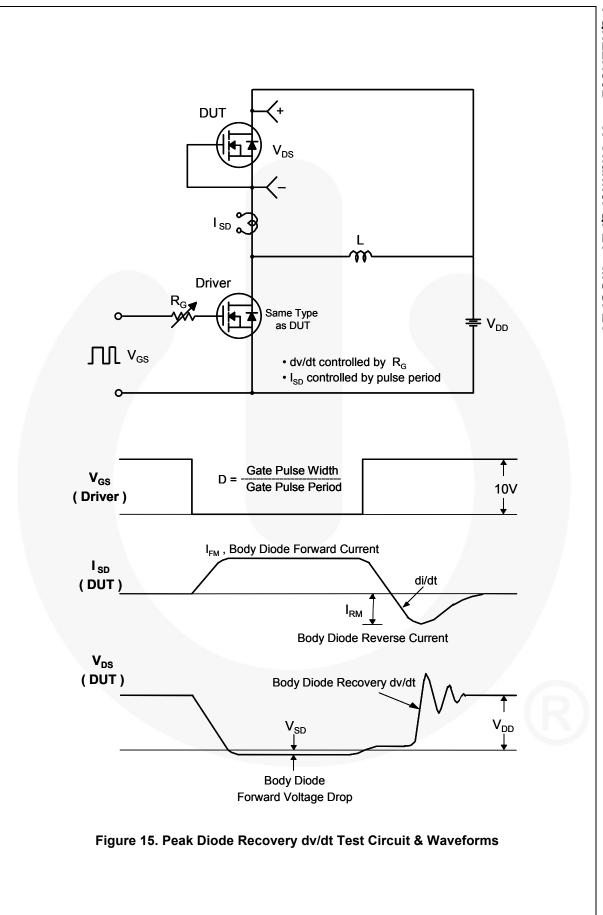
		Pack	kage Packing Method Reel		Size	Tape W	idth	Quantity			
		-92L AMMO			N/	A	N/A		2000 units		
lectri	cal Cha	racteristics	T _C = 25°C	cunless oth	nerwise noted.						
Symbol		Parameter			Test Con	ditions		Min.	Тур.	Max	. Unit
Off Cha	aracterist	ics									
BV _{DSS}	Drain-Sou	rce Breakdown Volta	age	V _{GS} = 0 V, I _D = 250 μA			500			V	
ΔBV _{DSS} / ΔΤ _J	Breakdown Voltage Temperature Coefficient			$I_D = 250 \ \mu$ A, Referenced to 25°C				0.48		V/°C	
	Zero Gate Voltage Drain Current		V _{DS} = 500 V, V _{GS} = 0 V				1	μA			
			ent	$V_{DS} = 400 \text{ V}, \text{ T}_{C} = 125^{\circ}\text{C}$					10	μA	
GSSF	Gate-Body	/ Leakage Current, F	orward	V _{GS} = 30 V, V _{DS} = 0 V					100	nA	
GSSR	Gate-Body	/ Leakage Current, F	Reverse	V _{GS} =	$V_{GS} = -30 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$					-100	nA
On Cha	aracterist	ics									
V _{GS(th)}	Gate Threshold Voltage		V _{DS} = V _{GS} , I _D = 250 μA			2.3	3.0	3.7	V		
00(11)				$V_{DS} = V_{GS}, I_{D} = 250 \text{ mA}$			3.6	4.3	5.0	V	
R _{DS(on)}	Static Dra On-Resist			V _{GS} = 10 V, I _D = 0.175 A			4.2	5.3	Ω		
9 _{FS}	Forward T	ransconductance		V _{DS} =	50 V, I _D = 0	.175 A			0.72		S
Dynam	ic Charac	teristics									
C _{iss}	Input Capa	acitance		V _{DS} = 25 V, V _{GS} = 0 V,			180	230	pF		
C _{oss}	Output Ca	pacitance		f = 1.0					30	40	pF
C _{rss}	Reverse T	ransfer Capacitance	•						4	6	pF
Switchi	ing Chara	acteristics									
t _{d(on)}	Turn-On D	elay Time		Voo =	250 V, I _D =	214			6	20	ns
t _r	Turn-On F	lise Time		$R_G = 2$	_	2.17,			25	60	ns
t _{d(off)}	Turn-Off D	elay Time		G -					10	30	ns
t _f	Turn-Off F	all Time				((Note 3)		20	50	ns
Qg	Total Gate	Charge		V _{DS} =	400 V, I _D =	2.1 A,			6.0	8.0	nC
Q _{gs}	Gate-Sour	ce Charge		V _{GS} = 10 V				1.3		nC	
Q _{gd}	Gate-Drai	n Charge				((Note 3)		3.0		nC
	Source Di	ode Characteri	stics ar	nd Max	cimum R	atinas					
Is		Continuous Drain-S								0.35	A
I _{SM}		Pulsed Drain-Sourc								1.4	A
V _{SD}		rce Diode Forward \		$V_{GS} = 0 \text{ V}, \text{ I}_{S} = 0.35 \text{ A}$				/	1.4	V	
t _{rr}		Recovery Time	5		0 V, I _S = 2.				195		ns
Q _{rr}		Recovery Charge		00	: = 100 A/με				0.69		μC

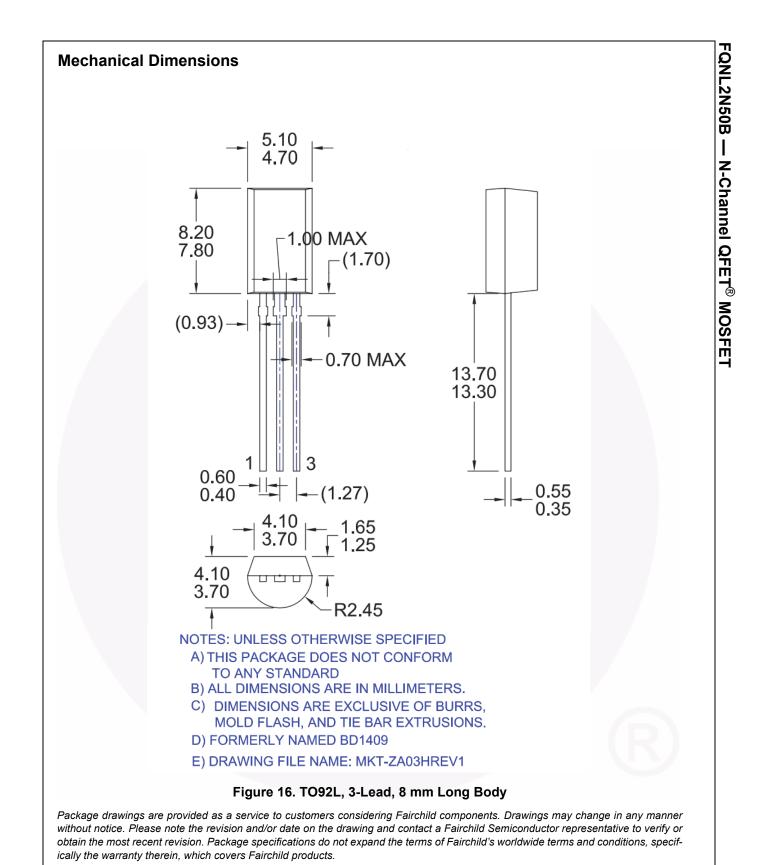
Notes: 1. Repetitive rating : pulse-width limited by maximum junction temperature. 2. $I_{SD} \leq 2.1 \text{ A}$, di/dt $\leq 200 \text{ A/}\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, starting $T_J = 25^{\circ}\text{C}$. 3. Essentially independent of operating temperature.











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http://www.fairchildsemi.com/package/packageDetails.html?id=PN_TO92-H03



DutuSheet facilitineation		Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.
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