TOSHIBA Transistor Silicon NPN Triple Diffused Type (PCT process)

## 2SC5172

Switching Regulator and High-Voltage Switching Applications

High-Speed DC-DC Converter Applications

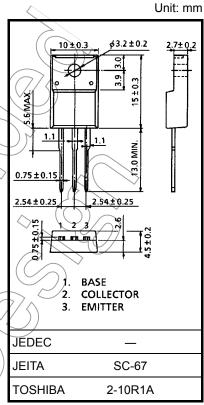
• Excellent switching times:  $t_r$  = 0.5  $\mu s$  (max),

 $t_f = 0.3 \mu s \text{ (max) at IC} = 2 \text{ A}$ 

• High collector breakdown voltage:  $V_{CEO} = 400 \text{ V}$ 

## Absolute Maximum Ratings (Tc = 25°C)

				/ >	
Characteristics		Symbol	Rating	Unit	
Collector-base voltage		V <sub>CBO</sub>	600	V	
Collector-emitter voltage		V <sub>CEO</sub>	400	V	
Emitter-base voltage		V <sub>EBO</sub>	7	V	
Collector current	DC	IC	5	A	
	Pulse	ICP	(\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\		
Base current		I <sub>B</sub>	2	/ (A	
Collector power dissipation	Ta = 25°C	Pc	2.0	W	
	Tc = 25°C	FC	25	VV	
Junction temperature		$(T_j \land)$	150	∕\°C	
Storage temperature range		Tstg	-55 to 150	-7.¢	



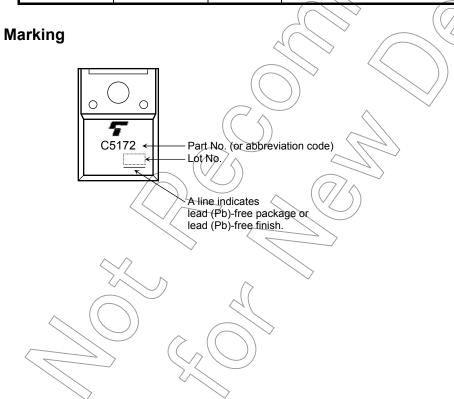
Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

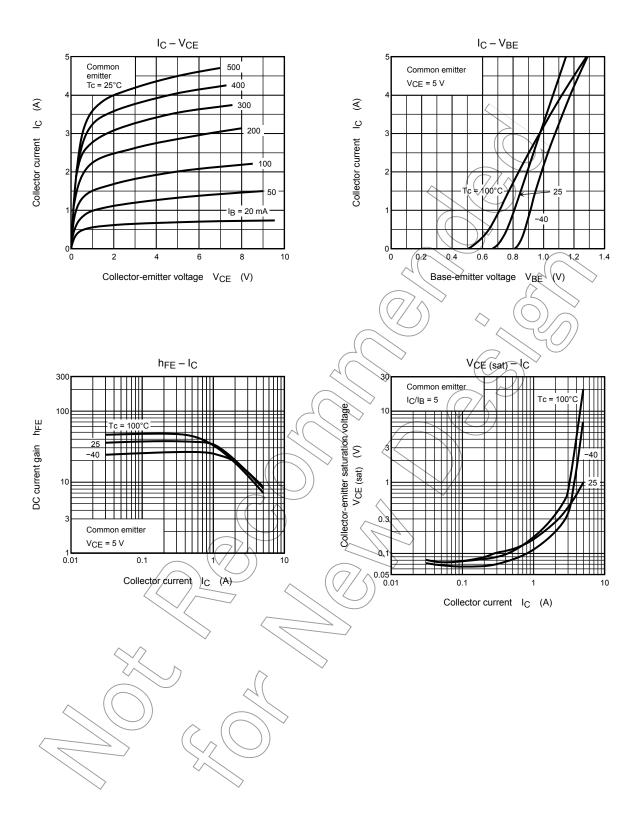
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

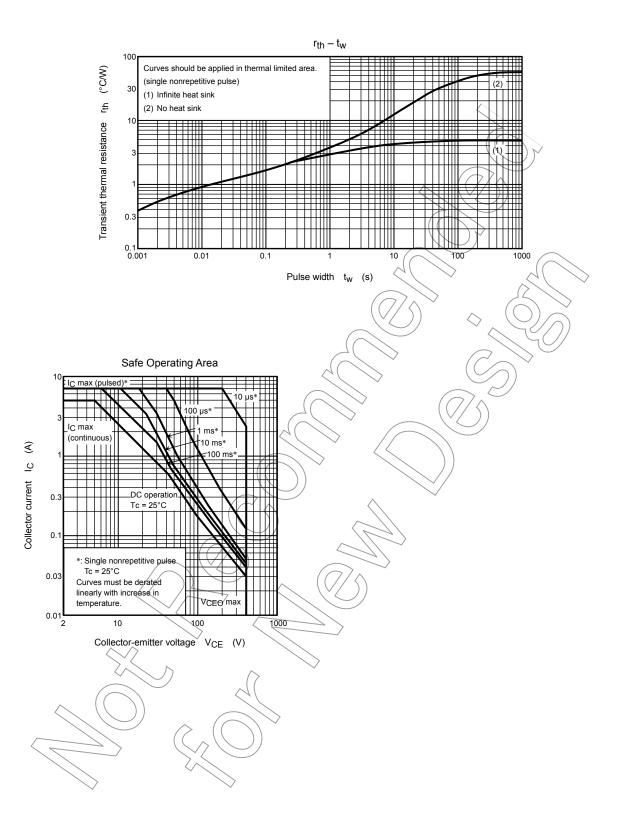
## Electrical Characteristics (Tc = 25°C)

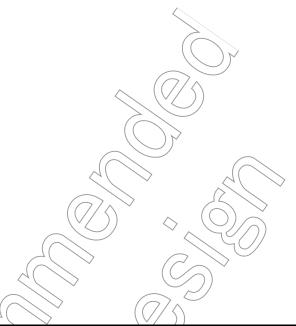
Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off of	current	I <sub>CBO</sub>	V <sub>CB</sub> = 500 V, I <sub>E</sub> = 0	_	_	20	μΑ
Emitter cut-off cu	rrent	I <sub>EBO</sub>	V <sub>EB</sub> = 7 V, I <sub>C</sub> = 0	_	_	100	nA
Collector-base br	eakdown voltage	V (BR) CBO	I <sub>C</sub> = 1 mA, I <sub>E</sub> = 0	600	_	_	V
Collector-emitter	breakdown voltage	V (BR) CEO	I <sub>C</sub> = 10 mA, I <sub>B</sub> = 0	400	_	_	V
DC current gain		h <sub>FE (1)</sub>	V <sub>CE</sub> = 5 V, I <sub>C</sub> = 1 mA		) / —	_	
		h <sub>FE</sub> (2)	V <sub>CE</sub> = 5 V, I <sub>C</sub> = 0.5 A	20	_	65	
Collector-emitter	saturation voltage	V <sub>CE</sub> (sat)	I <sub>C</sub> = 2 A, I <sub>B</sub> = 0.25 A	$\bigcirc )$	_	1.0	V
Base-emitter saturation voltage V <sub>BE</sub> (sat)		V <sub>BE</sub> (sat)	I <sub>C</sub> = 2 A, I <sub>B</sub> = 0.25 A	_	_	1.3	V
	Rise time	t <sub>r</sub>	20 µs Input IB2 Qutput  20 µs Input IB2 Qutput  20 µs Input IB2 Qutput  C  C  C  B2  VCC ≈ 200 V  IB1 = 0.25 A, IB2 = -0.5 A, duty cycle < 1%	_		0.5	
	Storage time	t <sub>stg</sub>				2.0	
	Fall time	t <sub>f</sub>			>_	0.3	



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