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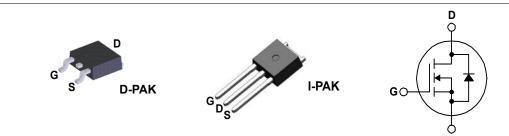
FQD1N60C / FQU1N60C N-Channel QFET[®] MOSFET 600 V, 1.0 A, 11.5 Ω

Features

- 1 A, 600 V, $R_{DS(on)}$ = 11.5 Ω (Max.) @ V_{GS} = 10 V, I_{D} = 0.5 A
- Low Gate Charge (Typ. 4.8 nC)
- Low Crss (Typ. 3.5 pF)
- 100% Avalanche Tested
- RoHS Compliant

Description

This N-Channel enhancement mode power MOSFET is produced using ON Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power factor correction (PFC), and electronic lamp ballasts.



Absolute Maximum Ratings T_C = 25°C unless otherwise noted.

| Symbol | Parameter | | FQD1N60CTM / FQU1N60CTU | Unit |
|-----------------------------------|---|----------|-------------------------|------|
| V _{DSS} | Drain-Source Voltage | | 600 | V |
| | Drain Current - Continuous ($T_C = 25^{\circ}C$) | | 1 | А |
| I _D | - Continuous (T _C = 100°C) | | 0.6 | А |
| I _{DM} | Drain Current - Pulsed | (Note 1) | 4 | А |
| V _{GSS} | Gate-Source Voltage | | ± 30 | V |
| E _{AS} | Single Pulsed Avalanche Energy | (Note 2) | 33 | mJ |
| I _{AR} | Avalanche Current | (Note 1) | 1 | А |
| E _{AR} | Repetitive Avalanche Energy | (Note 1) | 2.8 | mJ |
| dv/dt | Peak Diode Recovery dv/dt (N | | 4.5 | V/ns |
| | Power Dissipation $(T_A = 25^{\circ}C)^*$ | | 2.5 | W |
| P _D | Power Dissipation ($T_C = 25^{\circ}C$) | | 28 | W |
| | - Derate Above 25°C | | 0.22 | W/°C |
| T _J , T _{STG} | Operating and Storage Temperature Range | | -55 to +150 | °C |
| TL | Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds | | 300 | °C |

Thermal Characteristics

| Symbol | Parameter | FQD1N60CTM / FQU1N60CTU | Unit |
|---------------------|---|----------------------------|------|
| $R_{	ext{	heta}JC}$ | Thermal Resistance, Junction-to-Case, Max. | 4.53 | |
| P | Thermal Resistance, Junction-to-Ambient (Minimum Pad of 2-oz Copper), Max. | 110 | °C/W |
| $R_{	hetaJA}$ | Thermal Resistance, Junction-to-Ambient (*1 in ² Pad of 2-oz Copper), Max. | 50 | |

Quantity

2500 units

70 units

Package Marking and Ordering Information Part Number Top Mark Package **Packing Method Reel Size** Tape Width FQD1N60C FQD1N60CTM D-PAK Tape and Reel 330 mm 16mm FQU1N60CTU FQU1N60C I-PAK Tube N/A N/A

Electrical Characteristics $T_C = 25^{\circ}C$ unless otherwise noted.

| Symbol | Parameter | Test Conditions | Min. | Тур. | Max. | Unit |
|---|--|--|------|------|------|------|
| Off Cha | aracteristics | | | | | |
| BV _{DSS} | Drain-Source Breakdown Voltage | V _{GS} = 0 V, I _D = 250 μA | 600 | | | V |
| ΔBV _{DSS} / ΔT _J | Breakdown Voltage Temperature Coefficient | I_D = 250 μ A, Referenced to 25°C | | 0.6 | | V/°C |
| | Zero Gate Voltage Drain Current | V _{DS} = 600 V, V _{GS} = 0 V | | | 1 | μA |
| IDSS | | V _{DS} = 480 V, T _C = 125°C | | | 10 | μA |
| I _{GSSF} | Gate-Body Leakage Current, Forward | V_{GS} = 30 V, V_{DS} = 0 V | | | 100 | nA |
| I _{GSSR} | Gate-Body Leakage Current, Reverse | V_{GS} = -30 V, V_{DS} = 0 V | | | -100 | nA |
| On Cha | racteristics | | | | | |
| V _{GS(th)} | Gate Threshold Voltage | V _{DS} = V _{GS} , I _D = 250 μA | 2.0 | | 4.0 | V |
| R _{DS(on)} | Static Drain-Source On-Resistance | $V_{GS} = 10 \text{ V}, \text{ I}_{D} = 0.5 \text{ A}$ | | 9.3 | 11.5 | Ω |
| 9 _{FS} | Forward Transconductance | V _{DS} = 40 V, I _D = 0.5 A | | 0.75 | | S |
| C _{iss} | ic Characteristics Input Capacitance | V _{DS} = 25 V, V _{GS} = 0 V, | | 130 | 170 | pF |
| C _{oss} | Output Capacitance | V _{DS} = 25 V, V _{GS} = 0 V, f = 1.0 MHz | | 19 | 25 | pF |
| C _{rss} | Reverse Transfer Capacitance | | | 3.5 | 4.5 | pF |
| Switchi | ing Characteristics | 1 | | 1 | 1 | 1 |
| t _{d(on)} | Turn-On Delay Time | V _{DD} = 300 V, I _D = 1.1 A, | | 7 | 24 | ns |
| t _r | Turn-On Rise Time | $R_G = 25 \Omega$ | | 21 | 52 | ns |
| t _{d(off)} | Turn-Off Delay Time | | | 13 | 36 | ns |
| t _f | Turn-Off Fall Time | (Note 4) | | 27 | 64 | ns |
| Qg | Total Gate Charge | V _{DS} = 480 V, I _D = 1.1 A, V _{GS} = 10 V | | 4.8 | 6.2 | nC |
| Q _{gs} | Gate-Source Charge | | | 0.7 | | nC |
| Q _{gd} | Gate-Drain Charge | (Note 4) | | 2.7 | | nC |
| Drain-S | ource Diode Characteristics a | nd Maximum Ratings | | | | |
| I _S | Maximum Continuous Drain-Source Dic | Q | | | 1 | Α |
| <u> </u> | Maximum Duland Drain Course Diado F | | | | 4 | ٨ |

| IS | Maximum Continuous Drain-Source Diode Forward Current | | | 1 | A |
|-----------------|---|--|----------|-----|----|
| I _{SM} | Maximum Pulsed Drain-Source Diode Forward Current | | | 4 | А |
| V _{SD} | Drain-Source Diode Forward Voltage | $V_{GS} = 0 V, I_{S} = 0.5 A$ | | 1.4 | V |
| t _{rr} | Reverse Recovery Time | V _{GS} = 0 V, I _S = 1.1 A, | 190 | | ns |
| Q _{rr} | Reverse Recovery Charge | dI _F / dt = 100 A/µs | 0.53 | | μC |

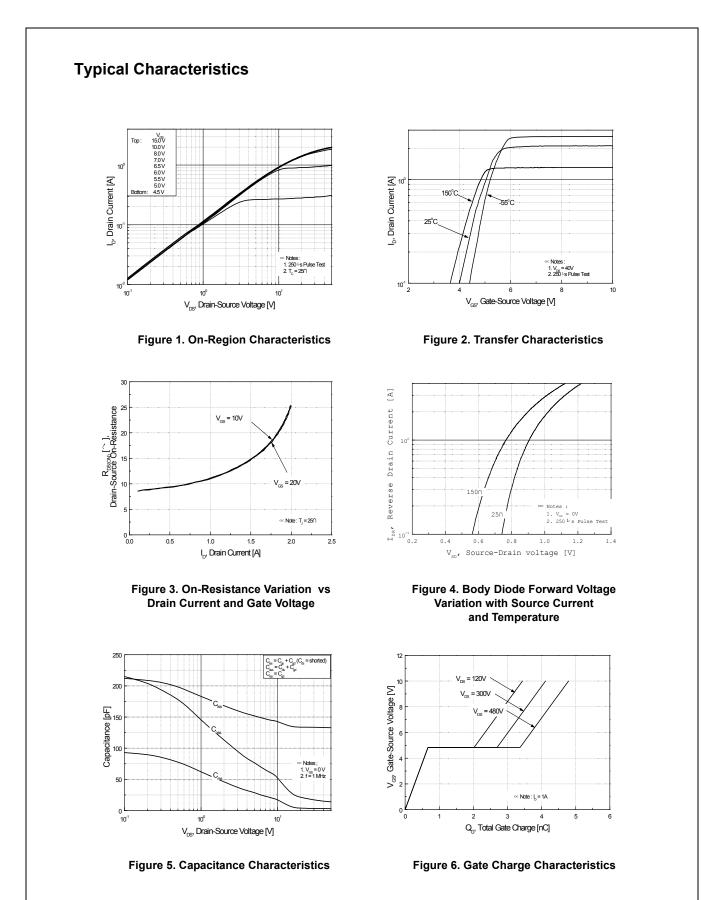
Notes:

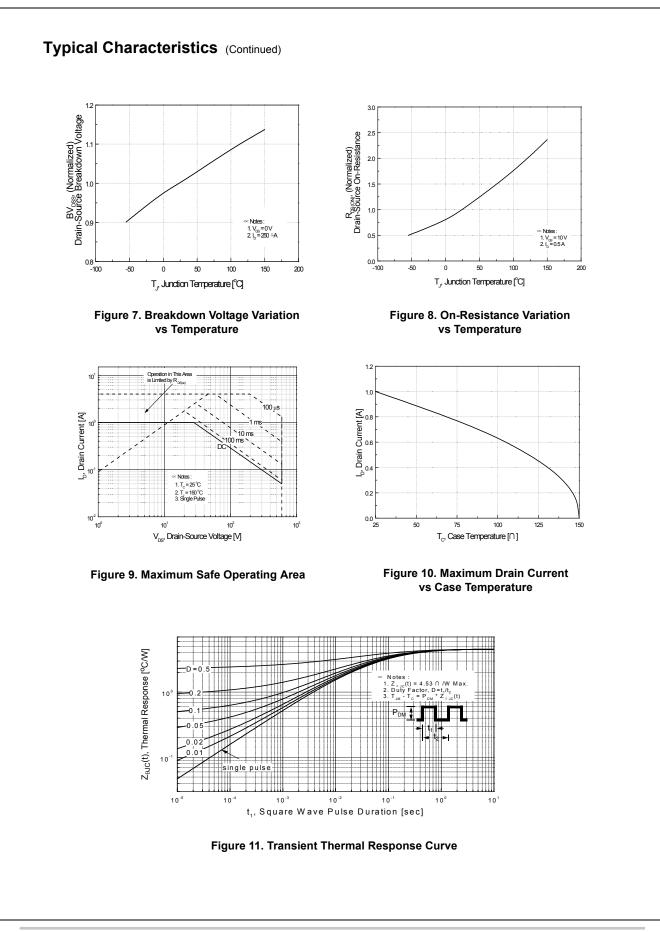
1. Repetitive Rating : pulse-width limited by maximum junction temperature.

2. L = 59 mH, I_{AS} = ~ 1.1 A, V_{DD} = 50 V, R_G = 25 $\Omega,$ starting ~ T_J = 25°C.

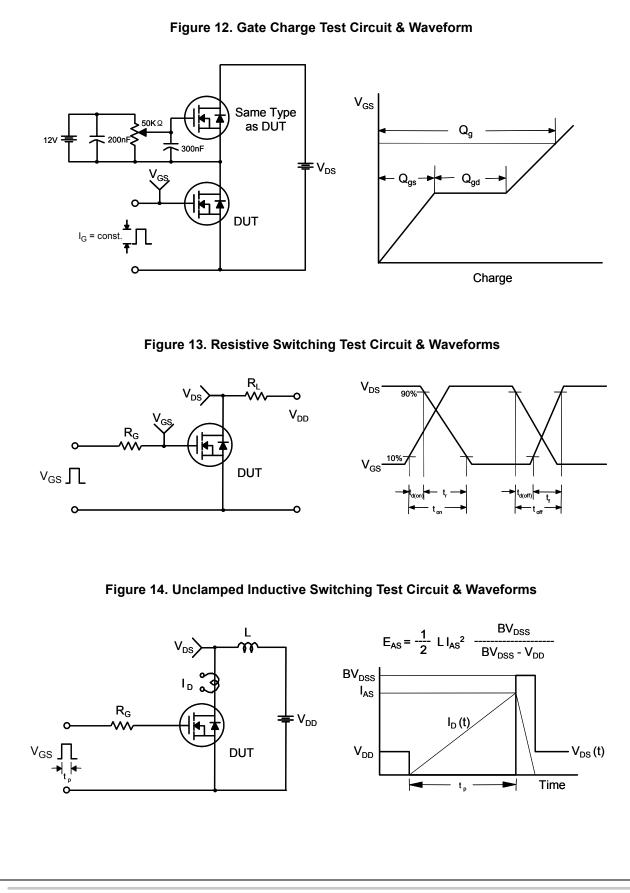
3. I_{SD} \leq 1.1 A, di/dt \leq 200 A/µs, V_{DD} \leq BV_{DSS,} starting ~ T_{J} = 25°C.

4. Essentially independent of operating temperature.





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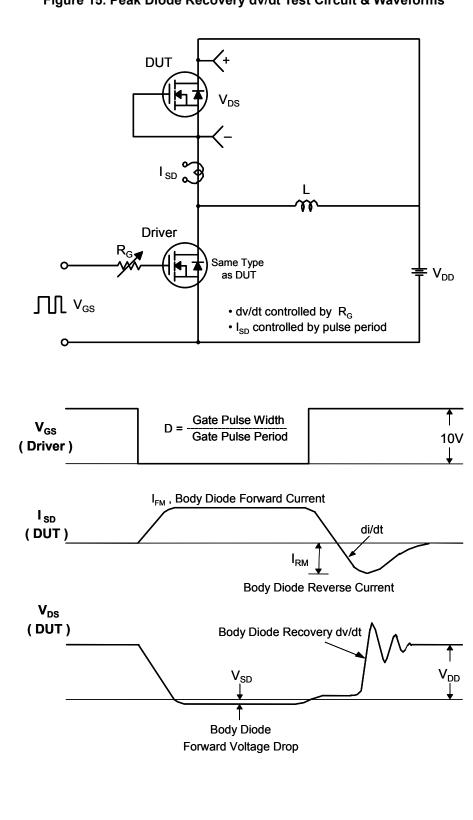


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

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