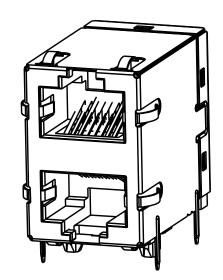
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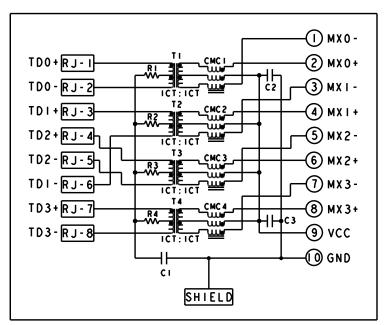
PRODUCT MAY BE PROTECTED BY ONE OR MORE OF THE FOLLOWING US PATENTS:

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S8G56 GIGABIT CIRCUIT



CI = 1000pF, 2kV CAPACITOR C2 - C3 = 10nF, 50V CAPACITORS RI-R4 = 75 Ohms, 1/16 W, RESISTORS I. MATERIALS:
PLASTIC HOUSING: BLACK, THERMOPLASTIC FLAMMABILITY RATING UL 94V-0
SHIELD: BRASS, PREPLATED WITH 0.76 um MIN SEMI-BRIGHT NICKEL,
POST DIPPED WITH 2.54 um MIN SAC SOLDER ON SOLDER TAILS,
CONTACTS: PHOSPHOR BRONZE, 1.27 um MIN OVERALL NICKEL
UNDERPLATE WITH SELECT 1.27 um MIN GOLD AT MATING INTERFACE
AND 2.54 um MIN MATTE TIN ON SOLDER TAILS.
ALL PC BOARDS: HIGH TEMPERATURE PCB, TG>170 °C

MAGNETICS
APPLICATION: 10/100/1000 BASE-T
IMPEDANCE: 100 OHMS
TURNS RATIO (CHIP:CABLE): 1:1 ALL FOUR PAIRS
OPEN CIRCUIT INDUCTANCE (OCL): 350 uh MIN @100 kHz, 0.1 VRMS,
8 mADC BIAS FROM 0 °C TO 70 °C, ALL FOUR PAIRS
ALL FOUR PAIRS BI-DIRECTIONAL
PERFORMANCE @ 25 °C:
INSERTION LOSS (IL): 1.1 dB MAX FROM 0.5 MHz TO 100 MHz
RETURN LOSS (RL): 18 dB MIN FROM 0.5 MHz TO 40 MHz
12-20 LOG(f/80) dB MIN FROM 40.1 MHz TO 100 MHz
CROSSTALK ATTENUATION: 35 dB MIN FROM 0.5 MHz TO 40 MHz
33-20 LOG(f/50) dB MIN FROM 40.1 MHz TO 100 MHz
COMMON MODE REJECTION RATIO (CMRR): 30 dB MIN FROM 0.5 MHz TO 100 MHz
ISOLATION VOLTAGE: 2250 VDC (MAX) FOR 60 SECONDS WITH A RISE TIME OF 500 V/SEC AND

PART NUMBER, DATE CODE AND COUNTRY OF ORIGIN ARE LOCATED IN APPROXIMATE AREA SHOWN.DATE CODE: "YY" IS YEAR, "WW" IS WORK WEEK, "D" IS DAY OF WEEK, WITH SUNDAY=I

TRP LOGO AND AGENCY APPROVAL LOGO ARE LOCATED IN APPROXIMATE AREA SHOWN.

5. OPERATING TEMP: FROM 0°C TO +70°C.

6. RJ45 CAVITY CONFORMS TO FCC RULES AND REGULATION PART 68 SUBPART F.

WITH ALL PORTS CONNECTED

M INDICATED MAGNETIC CONNECTIONS ARE SYMMETRICAL AND SUPPORT AUTO-MDI/MDIX.

⚠ DATUM AND BASIC DIMENSION ESTABLISHED BY CUSTOMER.

BASIC DIMENSION ESTABLISHED BY CUSTOMER, BUT MAY NOT BE GREATER THAN 5.08mm.

10. THE PARTS ARE RECOMMENDED FOR WAVE SOLDERING PROCESS, PEAK TEMPERATURE 260°C MAX, 10 SECONDS MAX.

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