

Intel® 82801DB I/O Controller

Specifications

 Essentials

| | |
|----------------------------|------------------------------|
| Status | Launched |
| Launch Date | Q4'03 |
| Embedded Options Available | <input type="checkbox"/> Yes |
| Max TDP | 2.2 W |
| Recommended Customer Price | N/A |
| Datasheet | Link |

 Expansion Options

| | |
|-------------|-----------|
| PCI Support | 6 (32/33) |
|-------------|-----------|

 I/O Specifications

| | |
|-----------------------|------------|
| USB Revision | USB 2.0 |
| # of USB Ports | 6 |
| Total # of SATA Ports | 0 |
| Integrated LAN | 10/100 |
| Integrated IDE | 2 Channels |

 Package Specifications

| | |
|-------------------------------|-------------|
| T _{CASE} | 110°C |
| Package Size | 31mm x 31mm |
| Low Halogen Options Available | See MDDS |

 Advanced Technologies

| | |
|----------------------------------|------------------------------|
| Intel® ME Firmware Version | No |
| Intel® Quick Resume Technology | No |
| Intel® Quiet System Technology | No |
| Intel® HD Audio Technology | No |
| Intel® AC97 Technology | <input type="checkbox"/> Yes |
| Intel® Matrix Storage Technology | No |

Ordering and Spec Information

Trade Compliance Information

| ECCN | CCATS | US HTS |
|-------|-------|------------------|
| 4A994 | NA | 8542310000-CHPST |

Retired and Discontinued

| Spec Code | Ordering Code | Step | RCP |
|---|---------------|------|-----|
| Intel® 82801DB I/O Controller Hub (ICH4) | | | |
| SL6DM | FW82801DB | B1 | N/A |
| Intel® 82801DB I/O Controller Hub (ICH4), Pb-Free SLI | | | |
| SL8DE | NH82801DB | B1 | N/A |

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"Announced" SKUs are not yet available. Please refer to the Launch Date for market availability.

‡ This feature may not be available on all computing systems. Please check with the system vendor to determine if your system delivers this feature, or reference the system specifications (motherboard, processor, chipset, power supply, HDD, graphics controller, memory, BIOS, drivers, virtual machine monitor-VMM, platform software, and/or operating system) for feature compatibility. Functionality, performance, and other benefits of this feature may vary depending on system configuration.

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System and Maximum TDP is based on worst case scenarios. Actual TDP may be lower if not all I/Os for chipsets are used.

Low Halogen: Applies only to brominated and chlorinated flame retardants (BFRs/CFRs) and PVC in the final product. Intel components as well as purchased components on the finished assembly meet JS-709 requirements, and the PCB / substrate meet IEC 61249-2-21 requirements. The replacement of halogenated flame retardants and/or PVC may not be better for the environment.

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