## 54ACT11027, 74ACT11027 TRIPLE 3-INPUT POSITIVE-NOR GATES

SCAS020A - D2957, JULY 1987 - REVISED APRIL 1993

- Inputs Are TTL-Voltage Compatible
- Flow-Through Architecture Optimizes PCB Layout
- Center-Pin V<sub>CC</sub> and GND Configurations Minimize High-Speed Switching Noise
- *EPIC* <sup>™</sup> (Enhanced-Performance Implanted CMOS) 1-μm Process
- 500-mA Typical Latch-Up Immunity at 125°C
- Package Options Include Plastic Small-Outline Packages, Ceramic Chip Carriers, and Standard Plastic and Ceramic 300-mil DIPs

### description

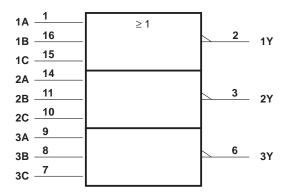
These devices contain three independent 3-input NOR gates. They perform the Boolean functions  $Y = \overline{A} + \overline{B} + \overline{C}$  or  $Y = \overline{A} \cdot \overline{B} \cdot \overline{C}$  in positive logic.

The 54ACT11027 is characterized for operation over the full military temperature range of  $-55^{\circ}$ C to 125°C. The 74ACT11027 is characterized for operation from  $-40^{\circ}$ C to 85°C.

FUNCTION TABLE (each gate)

	INPUTS	;	OUTPUT
Α	В	С	Y
Н	Х	Х	L
Х	Н	Х	L
Х	Х	Н	L
L	L	L	Н

### logic symbol<sup>†</sup>



<sup>†</sup> This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

Pin numbers shown are for the D, J, and N packages.

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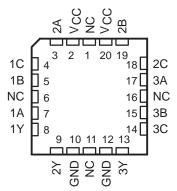
PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

4ACT11027	J PACKAGE . D OR N PACKAGE P VIEW)
1A [ 1	U 16] 1B
1Y [ 2	15] 1C

7

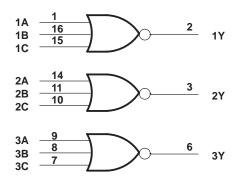
11		
	3	14 2A
GND [	4	13 V <sub>CC</sub> 12 V <sub>CC</sub> 11 28
L	5	12 V <sub>CC</sub>
3Y [	6	11 🛛 2B
3C [ 3B [	7	10 🛛 2C
3B [	8	9 🛛 3A

54ACT11027 . . . FK PACKAGE (TOP VIEW)



NC - No internal connection

### logic diagram (positive logic)



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## 54ACT11027, 74ACT11027 TRIPLE 3-INPUT POSITIVE-NOR GATES

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### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)<sup>†</sup>

Supply voltage range, V <sub>CC</sub>	0.5 V to 6 V
Input voltage range, V <sub>I</sub> (see Note 1)	$\dots \dots -0.5$ V to V <sub>CC</sub> + 0.5 V
Output voltage range, VO (see Note 1)	$\dots \dots -0.5 \text{ V to V}_{CC} + 0.5 \text{ V}$
Input clamp current, $I_{IK}$ ( $V_I < 0$ or $V_I > V_{CC}$ )	± 20 mA
Output clamp current, $I_{OK}$ ( $V_O < 0$ or $V_O > V_{CC}$ )	$\dots \dots \pm 50 \text{ mA}$
Continuous output current, $I_O (V_O = 0 \text{ to } V_{CC})$	
Continuous current through V <sub>CC</sub> or GND	
Storage temperature range	

<sup>†</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

### recommended operating conditions

		54ACT11027		74ACT11027		UNIT
		MIN	MAX	MIN	MAX	UNIT
VCC	Supply voltage	4.5	5.5	4.5	5.5	V
VIH	High-level input voltage	2		2		V
VIL	Low-level input voltage		0.8		0.8	V
VI	Input voltage	0	VCC	0	VCC	V
Vo	Output voltage	0	VCC	0	VCC	V
ЮН	High-level output current		-24		-24	mA
IOL	Low-level output current		24		24	mA
$\Delta t/\Delta v$	Input transition rise or fall rate	0	10	0	10	ns/V
Т <sub>А</sub>	Operating free-air temperature	-55	125	- 40	85	°C

# electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

DADAMETED		Vee		λ = 25°C	;	54AC	Г11027	74AC1	Г11027	LINUT
PARAMETER	TEST CONDITIONS	Vcc	MIN	TYP	MAX	MIN	MAX	MIN	MAX	UNIT
	1	4.5 V	4.4			4.4		4.4		
	I <sub>OH</sub> = – 50 μA	5.5 V	5.4			5.4		5.4		
Maria	I <sub>OH</sub> = – 24 mA	4.5 V	3.94			3.7		3.8		
VOH	OH = -24 MA	5.5 V	4.94			4.7		4.8		V
	$I_{OH} = -50 \text{ mA}^{\ddagger}$	5.5 V				3.85				
	I <sub>OH</sub> = – 75 mA <sup>‡</sup>	5.5 V						3.85		
	I <sub>OL</sub> = 50 μA	4.5 V			0.1		0.1		0.1	
		5.5 V			0.1		0.1		0.1	
	I <sub>OL</sub> = 24 mA	4.5 V			0.36		0.5		0.44	v
VOL		5.5 V			0.36		0.5		0.44	
	$I_{OL} = 50 \text{ mA}^{\ddagger}$	5.5 V					1.65			
	$I_{OL} = 75 \text{ mA}^{\ddagger}$	5.5 V							1.65	
Ц	$V_{I} = V_{CC}$ or GND	5.5 V			± 0.1		± 1		± 1	μA
Icc	$V_{I} = V_{CC} \text{ or GND},  I_{O} = 0$	5.5 V			4		80		40	μA
ΔICC§	One input at 3.4 V, Other inputs at GND or $V_{CC}$	5.5 V			0.9		1		1	mA
Ci	$V_{I} = V_{CC}$ or GND	5 V		3.5						pF

<sup>‡</sup> Not more than one output should be tested at a time, and the duration of the test should not exceed 10 ms.

§ This is the increase in supply current for each input that is at one of the specified TTL voltage levels rather than 0 V or V<sub>CC</sub>.



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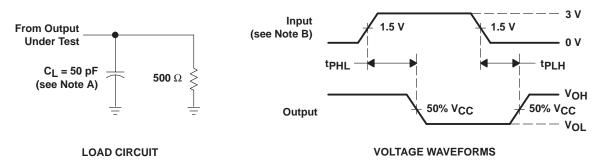
# switching characteristics over recommended operating free-air temperature range (unless otherwise noted) (see Figure 1)

	FROM		FROM TO		T <sub>A</sub> = 25°C		54ACT11027		74ACT11027		
PARAMETER	(INPUT)	(OUTPUT)	MIN	TYP	MAX	MIN	MAX	MIN	MAX	UNIT	
<sup>t</sup> PLH	A		1.5	5	9.2	1.5	10.6	1.5	10.1		
<sup>t</sup> PHL	Any	Ý	1.5	6	8.6	1.5	10	1.5	9.4	ns	

## operating characteristics, $V_{CC} = 5 V$ , $T_A = 25^{\circ}C$

PARAMETER		TEST CONDITIONS	TYP	UNIT
Cpd	Power dissipation capacitance per gate	$C_L = 50 \text{ pF}, \qquad f = 1 \text{ MHz}$	27	pF

### PARAMETER MEASUREMENT INFORMATION



NOTES: A. CL includes probe and jig capacitance.

- B. Input pulses are supplied by generators having the following characteristics: PRR  $\leq$  10 MHz, Z<sub>O</sub> = 50  $\Omega$ , t<sub>f</sub> = 3 ns, t<sub>f</sub> = 3 ns.
- C. The outputs are measured one at a time with one input transition per measurement.

#### Figure 1. Load Circuit and Voltage Waveforms



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