

Approved	Checked	Designed	DEVELOPMENT SPECIFICATION				
<i>T. Shoda</i>	<i>H. Kojima</i>	<i>K. Sakashita</i>		P/N:LNJ312G83RA			

T Y P E	Green Light Emitting Diode					
APPLICATION	Indicators					
MATERIAL	InGaAlP					
OUTLINE	Attached					
ABSOLUTE MAXIMUM RATINGS	P	*1 I _{FP}	I _{FIX}	V _R	Topr	Tstg
	55	60	20	4	-30~+85	-40~+100
	mW	mA	mA	V	°C	°C
CONDITION	T _a = 25 ± 3°C					

Test Specification

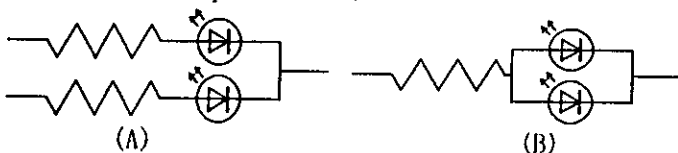
Item	Symbol	Condition	Typ.	Limit		Unit
				Min	Max	
Forward Voltage	V _F	I _F = 10 mA	2.03		2.5	V
Reverse Leakage Current	I _R	V _R = 4 V			100	μA
Luminous Intensity *2	I _O	I _F = 10 mA DC	12	6.4		mcd
Peak Emission Wavelength	λ _p	I _F = 10 mA DC	575			nm
Spectral Line Half Width	Δλ	I _F = 10 mA DC	15			nm

- *1 · The Condition of I_{FP} is duty 10 % , Pulse width 1 ms
- Please contact the Panasonic local office if you design at low current (below 1 mA DC) or pulse current operation and have any questions.
- *2 Measurement Tolerance is ±20%.
- Rank classification of luminous intensity. (Measurement condition ; I_F=10mA)

Rank	Luminous intensity(mcd)
1	6.4 ~ 9.6
2	9.6 ~ 14.4
3	14.4 ~ 21.6
4	21.6 ~ 32.4

NOTE

- ★1. Terminal:Plated with gold on copper base.
- ★2. Beware of destruction by static electricity in handling the LED.
- ★3. Soldering conditions.
Refer to handling note.
- ★4. Care should be taken that soldering is done within 3-days after opening the dry package and reel.
- ★5. Circuit to operate LED.



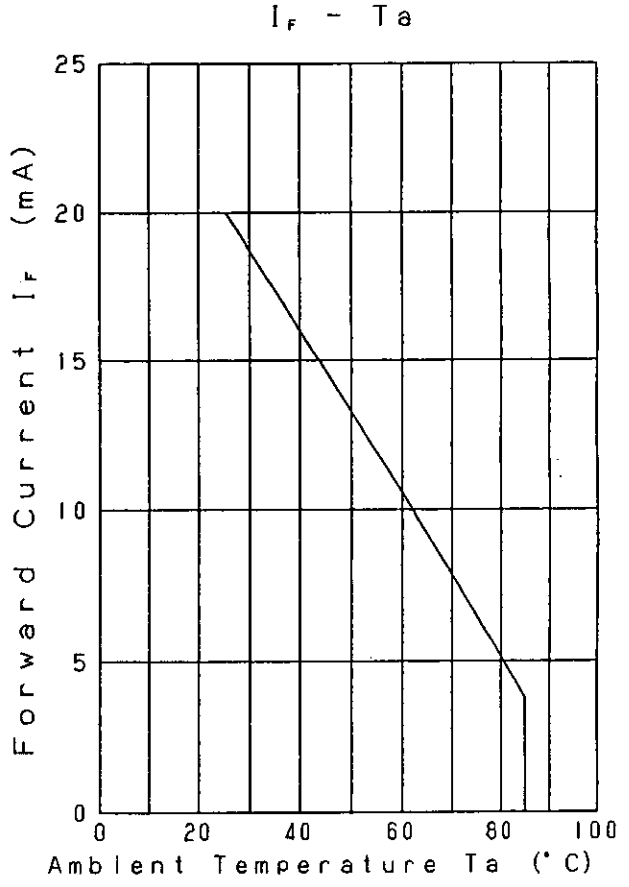
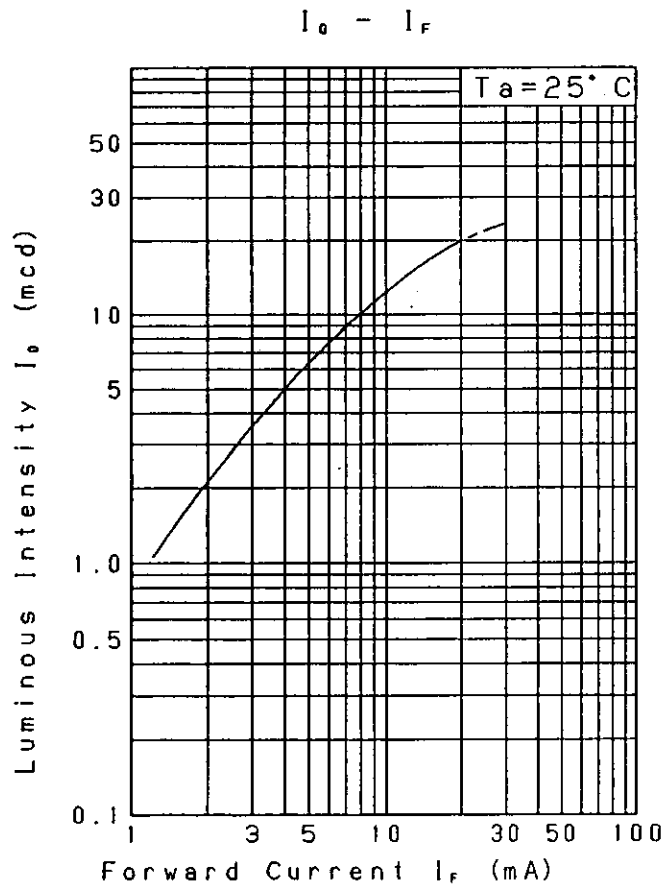
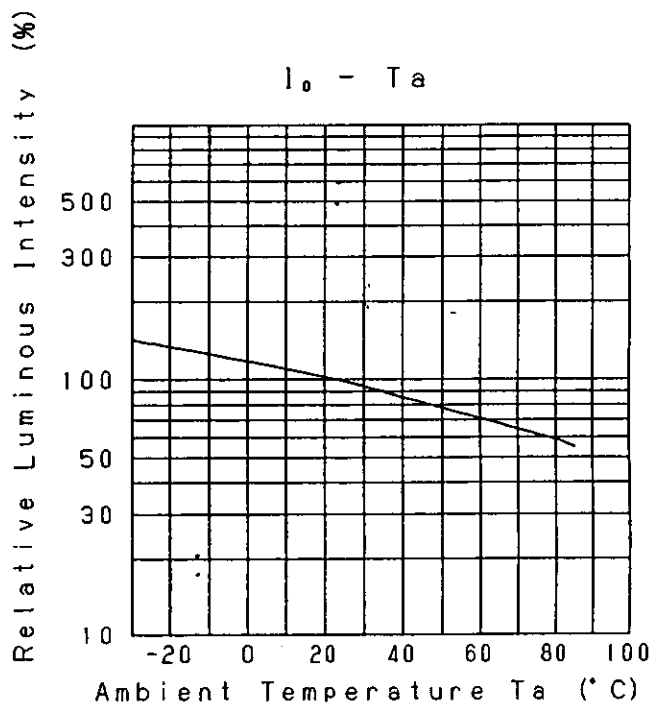
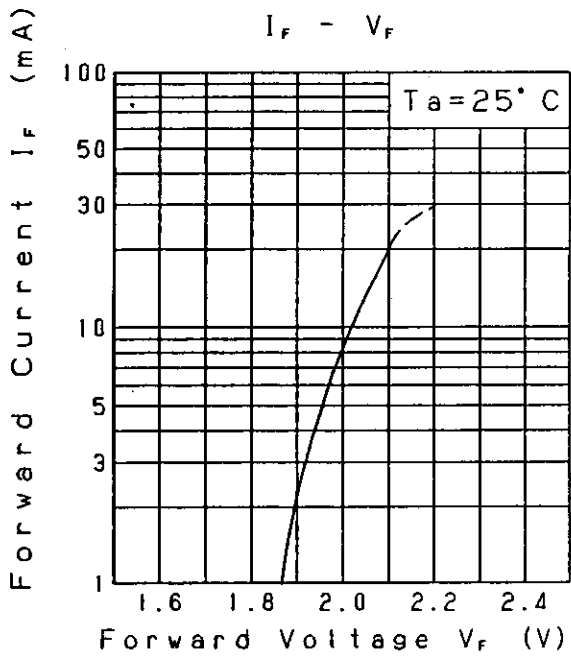
- (A) Recommended circuit.
- (B) The difference of brightness between the LED could be found due to the V_F characteristics of each LED.

Dec. 16. 2000			

Approved	Checked	Designed
T. Okada	H. I.	K. Sakurai

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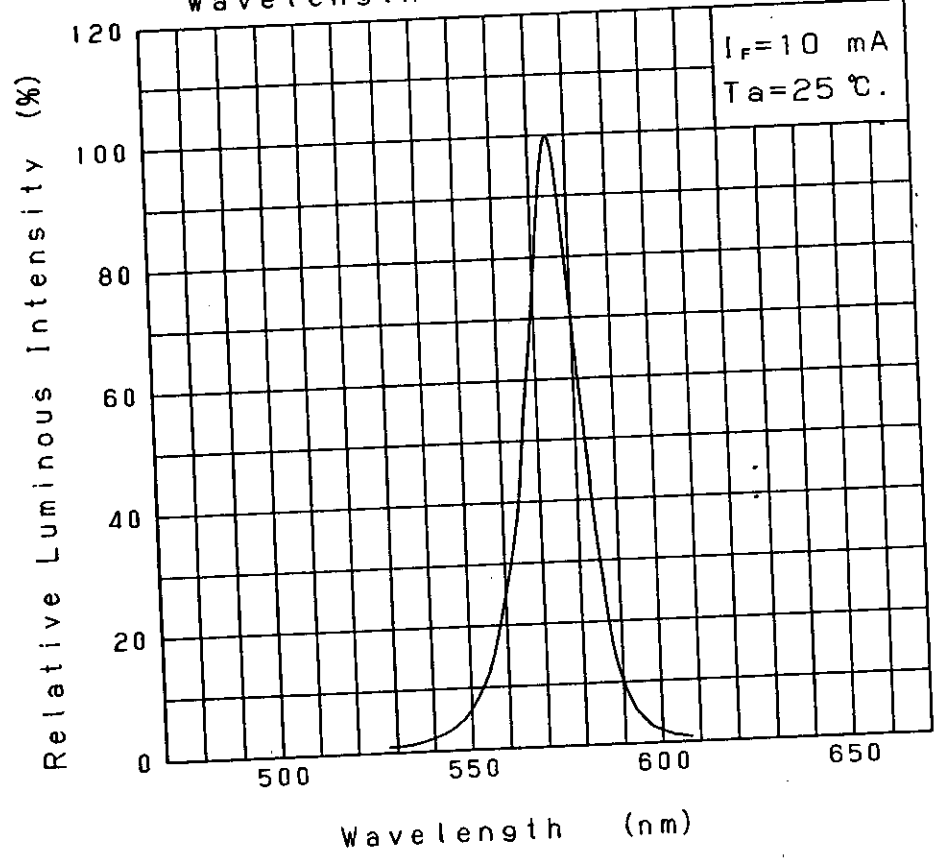
Approved | Checked | Designed

T. Shoda | M. H. | K. Shimizu

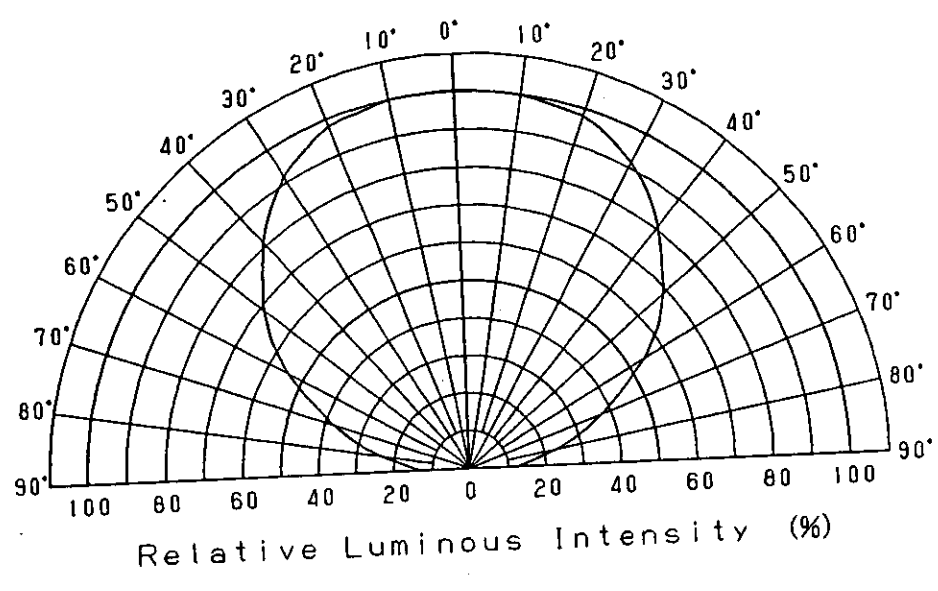
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Relative Luminous Intensity Wavelength Characteristics



Directive Characteristics



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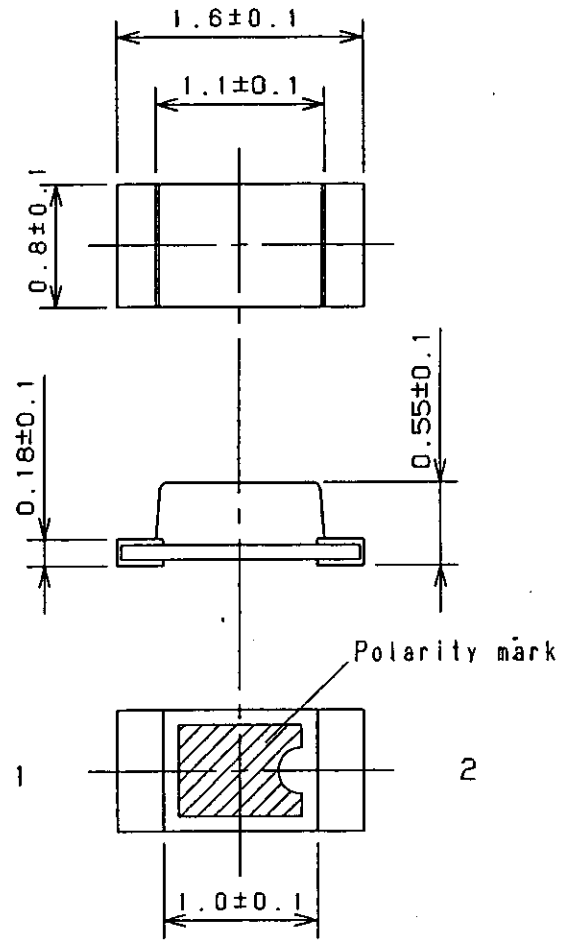
Panasonic

KAGOSHIMA MATSUSHITA ELECTRONICS CO., LTD

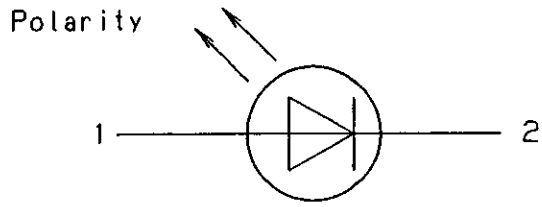
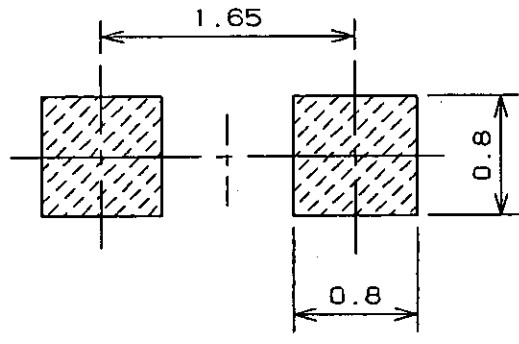
KB11-022-01EE

Approved <i>T. Shida</i>	Checked <i>M. Ito</i>	Designed <i>K. Sakuma</i>
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Recommended Land Layout



1: Anode
2: Cathode

(NOTE)

- 1. Measurement of the package doesn't include electrode projection.
- 2. Unit: mm

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