COOL POWER TECHNOLOGIES

Sixteenth-Brick Isolated DC/DC Converter

Features

- Industry-standard pinout
- Ultra-wide input voltage range: 18 72Vin
- Output: 5 V at 10 A, 50W max.
- High Efficiency 90% typical @FL
- No minimum load/capacitance required
- ROHS II Directive 2011/65/EU Compliant
- Low height 0.374" (9.5mm) max.
- Basic Insulation
- Withstands 100 V input transients
- Fixed-frequency operation
- Industry standard 1/16th brick footprint
- Full protection (OTP, OCP, OVP, UVLO auto-restart)
- Remote ON/OFF positive or negative enable logic options
- Remote sense
- Output voltage trim range: ±10% (industry-standard trim equations)
- Weight: 0.44 oz (12.5 g) open frame, 0.72 oz (20.5 g) baseplate model
- On-board input differential LC-filter
- Meets UL94, V-0 flammability rating
- Compliant to REACH (EC) No 1907/2006
- Complies with UL/CSA60950-1, TUV per IEC/EN60950-1, 2nd edition
- Designed to meet Class B conducted emissions per FCC and EN55032 when used with external filter (see EMC Compliance page.)

Description

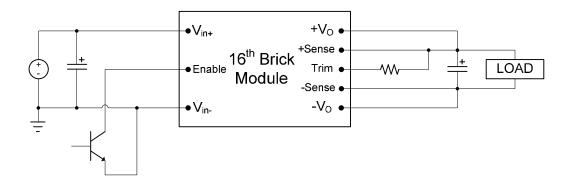
The CPT10A36 "Cool Power Technologies" DC-DC converter is an open frame sixteenth-brick DC-DC converter that conforms to industry standard specifications. The converter operates over an input voltage range of 18 to 72 VDC, and provides a tightly regulated output voltage with an output current rating of 10 Amps. The output is fully isolated from the input and the converter meets Basic Insulation requirements. The standard feature set includes remote On/Off (positive or negative enable), input undervoltage lockout, output overvoltage protection, overcurrent/short circuit protection, output voltage trim, remote sense and overtemperature shutdown with hysteresis. The high efficiency of the CPT10A36 allows operation over a wide ambient temperature range with minimal derating



TABLE OF CONTENTS

SECTION	PAGE
FEATURES & DESCRIPTION	1
APPLICATION DIAGRAM	2
ELECTRICAL SPECIFICATIONS	3
CHARACTERISTIC PERFORMANCE CURVES	6
CHARACTERISTIC WAVEFORMS	7
APPLICATION NOTES	8
RIPPLE MEASUREMENTS TEST SET-UP	8
OUTPUT VOLTAGE TRIM EQUATIONS	9
THERMAL DERATING	10
EMC COMPLIANCE	12
MECHANICAL OUTLINE & PCB FOOTPRINT	13
ORDERING INFORMATION	15

APPLICATION DIAGRAM



ELECTRICAL SPECIFICATIONS

18-72Vin, 5V/10Aout

Conditions: T_A = 25 °C, Airflow = 300 LFM, Vin = 48 VDC, Cin = 33 μ F, unless otherwise specified.

Input Characteristics						
Parameter	Conditions	Min	Тур	Max	Unit	
Operating Input Voltage Range		18	48	72	VDC	
Input Under-Voltage Lock-out Turn-on Threshold Turn-off Threshold		17.2 15.8	17.6 16.2	18.0 16.6	VDC	
Input Voltage Transient	100ms			100	VDC	
Maximum Input Current	V _{IN} = 18VDC; I _{out} = 10A			3.3	Α	
Input Standby Current	Converter Disabled		2	5	mA	
Input No-Load Current	Converter Enabled		50	100	mA	
Short Circuit Input Current	RMS		20		mA	
Input Reflected Ripple Current	5Hz to 50MHz See Fig 11 for setup		10	30	mA _{PK-PK}	
Input Voltage Ripple Rejection	120Hz	50		dB		
Inrush Current	All	-	-	0.01	A²/s	
Output Characteristics						
Parameter	Conditions	Min	Тур	Max	Unit	
Output Voltage Set point	Sense pins connected to output pins 4.9		5	5.075	VDC	
Output Current		0		10	Α	
Output Current Limit Inception		12	14	18	Α	
Peak Short-Circuit Current	10mΩ Short	0mΩ Short		28	Α	
RMS Short-Circuit Current	10mΩ Short		2.4	3.0	A _{RMS}	
External Load Capacitance				4700	uF	
Output Ripple and Noise	20MHz Bandwidth 1 uF Ceramic + 10uF Tantalum See Fig 12 for setup		80	150	mV _{PK-PK}	
Output Regulation Line: Load: Overall Output Regulation:	Over line, load & temp.	4.85	±1 ±1	±5 ±5 5.15	mV mV V	



CPT10A36

ELECTRICAL SPECIFICATIONS (continued)

18-72Vin, 5V/10Aout

Conditions: T_A = 25 °C, Airflow = 300 LFM, Vin = 48 VDC, Cin = 33 μ F, unless otherwise specified.

Absolute Maximum Ratings						
Parameter	Conditions	Min	Тур	Max	Unit	
Input Voltage	Continuous Operation	0		72	VDC	
Operating Ambient Temperature	w/derating	-40		+85	°C	
On a mating at Tamana matauma	Open Frame -40			+123	°C	
Operating Temperature	Baseplate Option	-40		+115	°C	
Storage Temperature		-55		+125	°C	
Feature Characteristics						
Parameter	Conditions	Min	Тур	Max	Unit	
Switching Frequency			440		kHz	
Output Voltage Trim Range ¹		-10		+10	%	
Remote Sense Compensation ¹				+10	%	
Output Over-voltage Protection	Non-latching	115	125	140	%	
Over-temperature Protection	Avg. PCB temp, non-latching	135			°C	
Peak Backdrive Output Current during startup into prebiased output	Sinking current from external voltage source equal to Vout – 0.6V and connected to the output via 1Ω resistor. Cout=220μF, Aluminum		-	500	mA	
Backdrive Output Current in OFF state	Converter disabled		0	5	mA	
Enable to Output Turn-ON Time	$V_{OUT} = 0.9*V_{OUT_NOM}$		20		ms	
Output Enable ON/OFF Negative Enable Converter ON Converter OFF Positive Enable Converter ON Converter OFF Enable Pin Current Source/Sink	All voltages are WRT –Vin. Converter has internal pull-up of approx. 5V	-0.5 2.4 2.4 -0.5	0.25	0.8 20 20 0.8 1	VDC VDC VDC VDC mA	
Output Voltage Overshoot @ Startup			0	2	%Vo	
Auto-Restart Period	(all protection features)		100		ms	



CPT10A36

ELECTRICAL SPECIFICATIONS (continued)

18-72Vin, 5V/10Aout

Conditions: Ta = 25 °C, Airflow = 300 LFM, Vin = 48 VDC, Cin=33 μ F, unless otherwise specified.

Efficiency						
Parameter	Conditions	Min	Тур	Max	Unit	
Full Load	Vin = 24V	90	91		%	
ruii Load	Vin = 48V	89	90.5		%	
60% Load	Vin = 24V	89	91		%	
60% Load	Vin = 48V	Vin = 48V 87			%	
Dynamic Response						
Parameter	Conditions	Min	Тур	Max	Unit	
Load Change 50%-75% or 25% to 50% of lout Max, di/dt = 0.1 A/µs	Co = 1 μF ceramic		100	150	mV	
Settling Time to 1% of Vout	+ 10 μF tantalum		50		μs	
Load Change 50%-75% or 25% to 50% of lout Max, di/dt = 1.0 A/µs	Co = 1 μF ceramic		100	150	mV	
Settling Time to 1% of Vout	+ 330 µF Tantalum		50		μs	
Isolation Specifications						
Isolation Capacitance			1000		pF	
Isolation Resistance		10			MΩ	
	Input to Output	2250			V _{DC}	
Isolation Voltage	Input to Baseplate	1500			V _{DC}	
	Output to Baseplate	1000			V _{DC}	
Reliability						
Per Telcordia SR-332, Issue 2: Method I, Case 3	MTBF	3,235,056			Hours	
(I _o =80% of I _o max, T _A =40°C, airflow = 200 lfm, 90% confidence)	FITs (failures in 10 ⁹ hours)	309		/10 ⁹ Hours		

Notes:

1) Combination of trim + remote sense cannot exceed 10% of $V_{\rm o\ nom}$



CHARACTERISTIC CURVES:

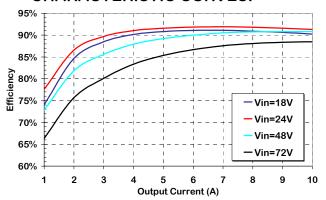


Figure 1. Efficiency vs Output Current, 300lfm airflow, 25 $^{\rm C}$ ambient.

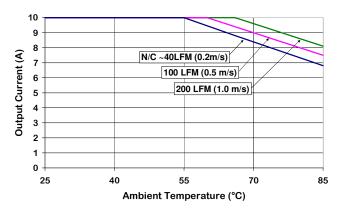


Figure 3. Output Current Derating vs Ambient Temperature & Airflow (converter mounted vertically with air flowing from pin 3 to pin 1, Vin = 48 Volts

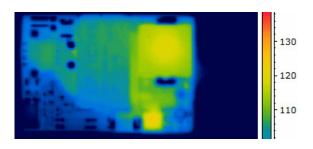


Figure 5. Thermal Image of CPT10A36 10A output, 55C Ambient, 100 LFM airflow Vin = 48V, airflow from pin 3 to pin 1, T_{max} = 120 ℃)

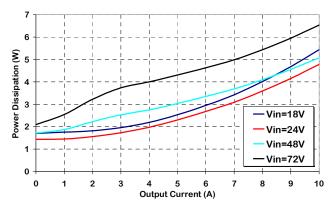


Figure 2. Power Dissipation vs. Load Current, 300lfm airflow, 25 ℃ ambient.

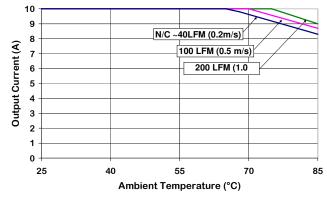


Figure 4. Output Current Derating vs Ambient Temperature & Airflow (converter mounted vertically with air flowing from pin 3 to pin 1, Vin = 24 Volts

CHARACTERISTIC WAVEFORMS:

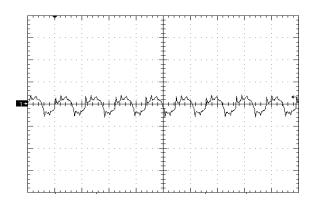


Figure 6. Output Voltage Ripple (50mV/div), time scale – 2uS/div. Vin=Vin_nom, full resistive Cout=1uF ceramic + 10uF Tantalum (see Fig 12)

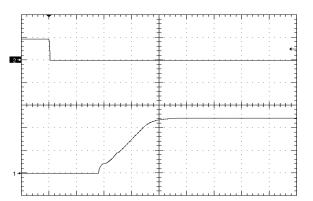


Figure 8. Startup Waveform (2V/div) via Enable Pin, time scale 10mS/div. Vin=Vin_nom, full resistive load. Negative enable, Ch2=5V/div

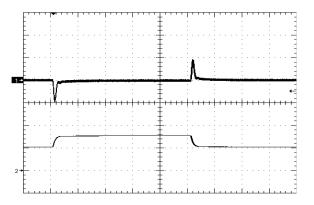


Figure 10. Load Transient Response (100mV/div), di/dt=0.1A/uS, 50% - 75% - 50% of full load, time scale: 200uS/div. Ch2=5A/div

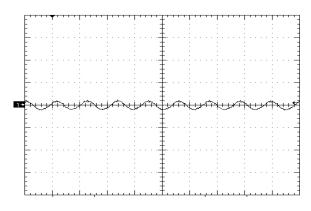


Figure 7. Input Reflected Ripple Current (10 mA/div) time scale - 2uS/div. Vin=Vin_nom, full resistive

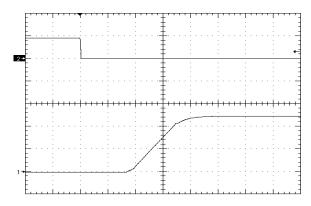
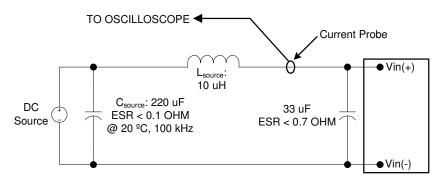


Figure 9. Startup Waveform (2V/div) via Enable Pin, time scale 4mS/div. Vin=Vin_nom, full resistive load + 4700uF, Negative enable, Ch2=5V/div

Application Notes

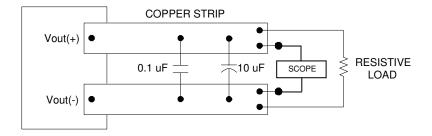
INPUT REFLECTED RIPPLE TEST SETUP:



Note: Measure input reflected-ripple current with a simulated source inductance (Ltest) of 10 uH. Capacitor C_S offsets possible source impedance.

Figure 11. Input Reflected-ripple Current Test Setup.

OUTPUT RIPPLE TEST SETUP:



Note: Use a $0.1\mu F$ X7R ceramic capacitor and a $10\mu F$ @ 25V tantalum capacitor. Scope measurement should be made using a BNC socket. Position the load 3 in. [76mm] from module.

Figure 12. Peak-to-Peak Output Noise Measurement Test Setup.

Output Voltage Trim

Output voltage adjustment is accomplished by connecting an external resistor between the Trim Pin and either the +Sense or –Sense pins.

• TRIM UP EQUATION:

$$R_{\text{trim_up}} = \left[\frac{5.1 \times \text{Vo_nom} \times (100 + \Delta\%)}{1.225 \times \Delta\%} - \frac{510}{\Delta\%} - 10.2 \right] \times \text{k}\Omega$$

Where R_{trim_up} is the resistance value in k-ohms and $\Delta\%$ is the percent change in the output voltage. E.g. to $R_{trim_up} = \left[\frac{5.1 \times 5 \times (100 + 10)}{5.1 \times 100} - \frac{510}{5.10} - \frac{5$

trim the output up 10%, $R_{trim_up} = \left[\frac{5.1 \times 5 \times (100 + 10)}{1.225 \times 10} - \frac{510}{10} - 10.2 \right] \times k\Omega$ or $R_{trim_up} = 168$ kOhm.

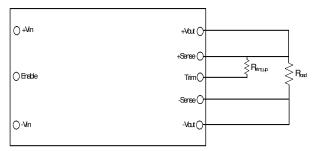


Figure 13. Trim UP circuit configuration

• TRIM-DOWN EQUATION:

$$R_{\text{trim_down}} = \left(\frac{510}{\Delta\%} - 10.2\right) \times k\Omega$$

Where R_{trim_down} is the resistance value in k ohms and $\Delta\%$ is the percent change in the output voltage.

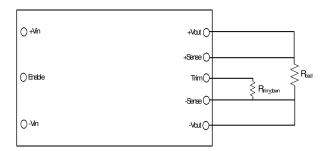
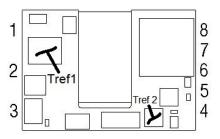
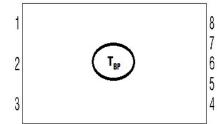


Figure 14. Trim DOWN circuit configuration

Thermal Derating

- It is preferable that the DC-DC module have an unobstructed flow of air across it for best thermal performance. Components taller than ~ 2mm in front of the module can deflect airflow and possibly create hotspots.
- Significant cooling is achieved through conductive flow from the modules I/O pins to the host PCB. Sufficiently large traces connecting the dc-dc converter to the source and load will help ensure thermal derating performance will meet or exceed the derating curves published in this datasheet. Solder flow-through that contacts standoff of output pins is essential for proper derating performance – especially on models with greater than 10A output current.
- If the module is expected to be operated near the load limits defined in the derating curves, insystem verification of module derating performance should be performed to ensure long-term system reliability. Peak temperatures are to be measured using infrared thermography or by gluing a fine gauge (AWG #40) thermocouple at the T_{ref} location(s) shown below. Temperatures at the specified location(s) are not to exceed 123°C in order to maintain converter reliability. For baseplate models, T_{BP} should not exceed 115°C.





Input Undervoltage Lockout

The converter is disabled until the input voltage has exceeded the UVLO turn-on threshold.
Once the input voltage exceeds this level (see Input Under-Voltage Lock-out in Electrical
Specifications table) the module will commence soft-start. Hysteresis of 2-3 volts minimizes the
likelihood of pulling the input voltage below the turn-off threshold during startup which could
create an undesirable on/off cycling condition. Once started, the converter will continue to
operate until the input voltage subsequently falls below the UVLO turn-off threshold.

Enable Pin Function

- The module has a remote enable function that allows it to be turned on or off remotely. The Enable pin is referenced to the negative input pin (-Vin) of the converter. Modules can be ordered with either negative or positive enable.
- With the negative enable option, the converter will not turn on unless the enable pin is connected to –Vin. The positive enable option allows the converter to turn on as soon as voltage sufficient to exceed the UVLO threshold of the converter has been applied to the input terminals. In this case the module is turned off by connecting the Enable pin to –Vin. On/off thresholds are located in the Electrical Specifications table.



Output Overvoltage Protection

The module has an independent feedback loop that will disable the output of the converter if a
voltage greater than about 125% of the nominal set point is detected. When this threshold is
reached, the converter will shut down and remain off for the amount of time specified by the
Auto-Restart Period. The converter will attempt a restart once this period of time has elapsed.

Output Overtemperature Protection

To provide protection under certain fault conditions, the unit is equipped with a thermal shutdown circuit. The unit will shutdown if the average PCB temperature exceeds approx.
 135°C, but the thermal shutdown is not intended as a guarantee that the unit will survive temperatures beyond its rating. The module will automatically restart once it has cooled below the shutdown temperature minus hysteresis (typically 20 deg C.)

SMT Version Layout Considerations (if applicable)

- Copper traces with sufficient cross-section must be provided for all output & input pins. SMT pads tied to internal power/ground planes must have multiple vias around each SMT pad to couple expected current loads from module pins into internal traces/planes. One 0.024" (0.6mm) diameter via for each 4A of expected source or load current must be provided as close to the termination as possible, preferably in the direction of current flow from SMT pad to load. Vias must be at least 0.024" (0.6 mm) away from the SMT pad to prevent solder from flowing into the vias.
- SMT pads on the host card are to be 0.080" (2.03 mm) diameter. Solder paste screen opening should be 0.075" (1.9 mm) diameter and the screen should be 0.006" (0.15 mm) thick (other thicknesses are possible; 0.006" provides a good compromise between solder volume and coplanarity compensation.)

Paralleling Converters

Modules may be paralleled but it is recommended that the total power draw not exceed the
output power rating of a single module. External sharing controllers are recommended for
reliability and to ensure equal distribution of the load to the converters. . In lower current
applications, ORing diodes can be used to prevent converter interactions and improve current
sharing.



EMC COMPLIANCE:

To meet Class B compliance for EN55032 (CISPR 32) or FCC part 15 sub part j, the following input filter is required:

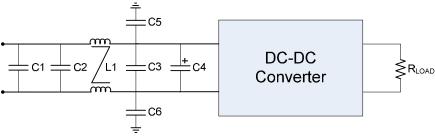


Figure 15. EMI Filter

L1 =	1.32 mH Common Mode Inductor (Pulse P0420)
C1,C2,C3 =	2.2uF ceramic
C4 =	100uF electrolytic
C5,C6 =	10nF (@2kV if output is ref. to earth gnd.)

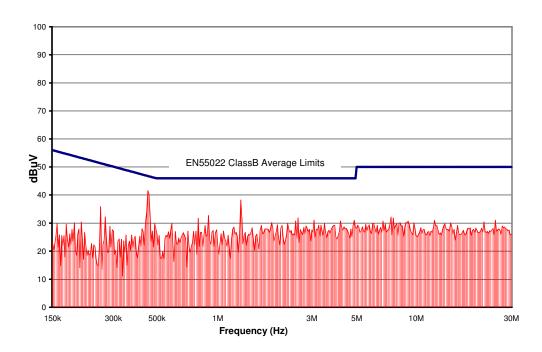


Figure 16. CPT10A36 Conducted Emissions using above specified input filter.

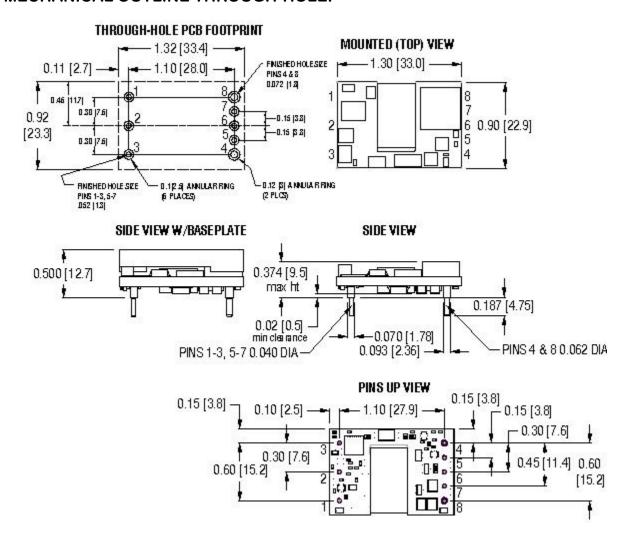
Vin = 48V, Full Resistive Load



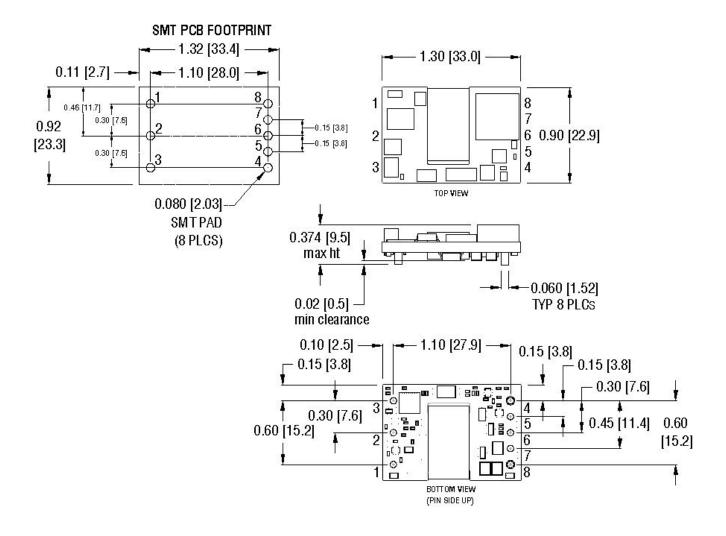
MODULE PIN ASSIGNMENT:

PIN#	DESIGNATION	NOTES
1	V _{IN} (+)	1) All dimensions in inches [mm]
2	On/Off	Tolerances: .xx ± 0.02 [.x ± .5]
3	V _{IN} (-)	.xxx ± 0.010 [.xx ± .25] 2) Input, on/off control and sense/trim pins are Ø 0.040" [1.02]
4	V _{OUT} (-)	with Ø 0.070" [1.77] standoff shoulders.
5	Sense (-)	3) Output pins are Ø 1.57 mm (0.062") with Ø 0.093" [2.36]
6	Trim	shoulders (note, shoulder sits .008" above mounting surface) 4) All pins are gold plated with nickel under plating.
7	Sense (+)	5) Weight: 12.5 g (0.44 oz) open frame, 20.5g (0.72 oz) baseplated
8	V _{OUT} (+)	6) Workmanship: Meets or exceeds IPC-A-610 Class II

MECHANICAL OUTLINE THROUGH-HOLE:



MECHANICAL OUTLINE SMT:



ORDERING INFORMATION:							
Product Identifier	Output Current	Output Voltage	Input Voltage	Enable logic option	Additional features		
CPT	10	Α	36	N or P	S or B		
"Cool Power Technologies"	10A	5V	18 – 72V	N = Negative P = Positive	S = Surface Mount B = Baseplate Option		

Rev 1.2, 20-June-18

