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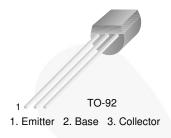
May 2016



2N5401 Amplifier Transistor

Features

- Collector-Emitter Voltage: V_{CEO} = 150V
- Collector Dissipation: P_C (max) = 625mW
- Suffix "-C" means Conter Collector (1. Emitter 2. Collector 3. Base)



Ordering Information

Part Number	Top Mark	Package	Packing Method	Pack Quantity
2N5401YBU	2N5401	TO-92 3L	Bulk	10000
2N5401YTA	2N5401	TO-92 3L	Ammo	2000

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Unit
V _{CBO}	Collector-Base Voltage	-160	V
V _{CEO}	Collector-Emitter Voltage	-150	V
V _{EBO}	Emitter-Base Voltage	-5	V
Ι _C	Collector Current	-600	mA
T _J , T _{STG}	Operating and Storage Junction Temperature Range	-55 to 150	°C

Thermal Characteristics⁽¹⁾

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Max.	Unit
Б	Total Device Dissipation	625	mW
PD	Derate above 25°C	5	mW/°C
R _{0JA}	Thermal Resistance, Junction to Ambient	200	°C/W

Note:

1. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

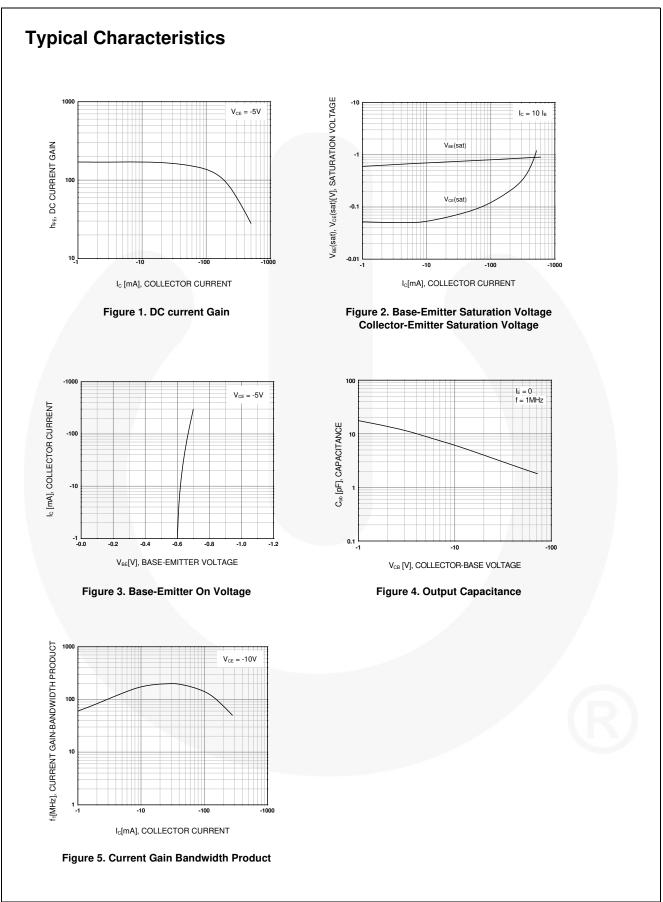
Electrical Characteristics

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

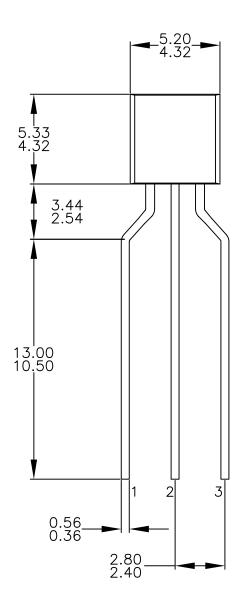
Symbol	Parameter	Con	litione	Min	Typ	Max.	Unit
Symbol	Parameter	Conditions		Min.	Тур.	wax.	Unit
BV_{CBO}	Collector-Base Breakdown Voltage	$I_{C} = -100 \ \mu A, \ I_{E} =$	$I_{C} = -100 \ \mu A, \ I_{E} = 0$				V
BV _{CEO}	Collector-Emitter Breakdown Voltage ⁽²⁾	$I_{\rm C} = -1 {\rm mA}, I_{\rm B} = 0$	$I_{\rm C} = -1 {\rm mA}, I_{\rm B} = 0$				V
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_E = -10 \ \mu A, I_C =$	$I_{E} = -10 \ \mu A, I_{C} = 0$				V
I _{CBO}	Collector Cut-Off Current	$V_{CB} = -120 \text{ V}, \text{ I}_{E} = 0$				-50	μΑ
I _{EBO}	Emitter Cut-Off Current	$V_{EB} = -3 V, I_C =$	$V_{EB} = -3 V, I_{C} = 0$			-50	μΑ
h _{FE1}		I _C = -1 mA, V _{CE} = -5 V		30			
	DC Current Gain ⁽²⁾	I _C = -10 mA, V _{CE} = -5 V	Standard Class	60		240	
			Y Class	120		240	
		$I_{\rm C} = -50 \text{ mA}, \text{ V}_{\rm CE}$	= -5 V	50			
V _{CE(sat)}	Collector-Emitter Saturation Voltage ⁽²⁾	I _C = -10 mA, I _B = -1 mA				-0.2	V
	Collector-Emitter Saturation Voltage	$I_{\rm C} = -50$ mA, $I_{\rm B} = -5$ mA				-0.5	V
V _{BE(sat)}	Base-Emitter Saturation Voltage ⁽²⁾	I _C = -10 mA, I _B =	-1 mA			-1.0	V
	Base-Emilier Saturation Voltage	$I_{\rm C} = -50$ mA, $I_{\rm B} = -5$ mA				-1.0	V
f _T	Current Gain Bandwidth Product	I _C = -10 mA, V _{CE} = -10 V, f = 100 MHz		100		400	MHz
C _{ob}	Output Capacitance	$V_{CB} = -10 \text{ V}, \text{ I}_{E} = 0, \text{ f} = 1 \text{ MHz}$				6	pF
N _F	Noise Figure	I_C = -250 μA, V _{CE} = -5 V,R _S = 1 kΩ, f = 10 Hz to 15.7 kHz				8	dB

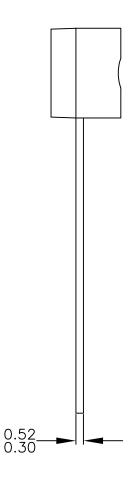
Note:

2. Pulse Test: Pulse Width≤300µs, Duty Cycle≤2%.



2N5401 — Amplifier Transistor

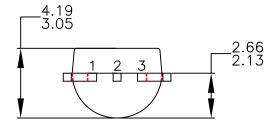


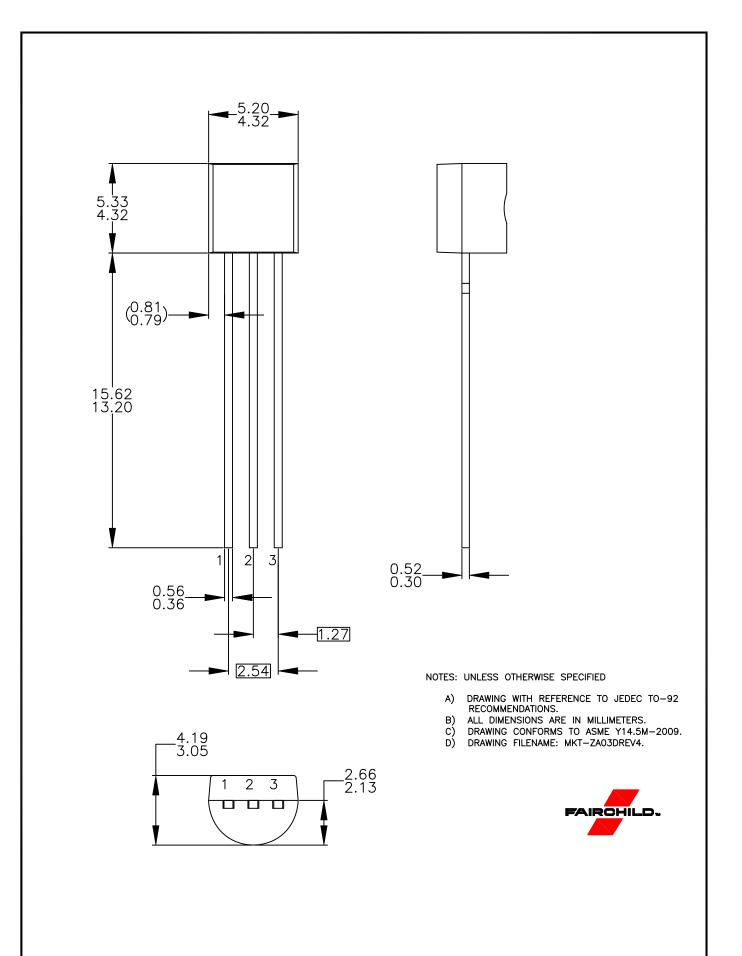


NOTES: UNLESS OTHERWISE SPECIFIED

- DRAWING CONFORMS TO JEDEC MS-013, VARIATION AC. ALL DIMENSIONS ARE IN MILLIMETERS. DRAWING CONFORMS TO ASME Y14.5M-2009. DRAWING FILENAME: MKT-ZA03FREV3. FAIRCHILD SEMICONDUCTOR. Α.

- В. С. D. Е.





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