



N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

| BV _{DSS} | Rds(on) | I D T _A = +25°C |
|-------------------|-----------------------------|--------------------------------------|
| | 3Ω @ V _{GS} = 4.5V | 0.3A |
| 20V | 4Ω @ V _{GS} = 2.5V | 0.26A |
| | 6Ω @ V _{GS} = 1.8V | 0.21A |

Description and Applications

This new generation MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- DC-DC Converters
- Load Switch

Notes:

Power Management Functions

Features and Benefits

- Low On-Resistance
- Very Low Gate Threshold Voltage, 1.0V Max
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DMN2991UTQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/guality/product-definitions/

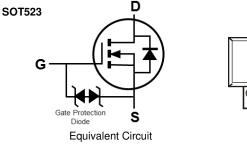
Mechanical Data

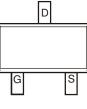
- Case: SOT523
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Alloy 42
 Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram
- Weight: 0.002 grams (Approximate)





Top View





Top View

Ordering Information (Note 4)

| Part Number | Case | Packaging |
|---------------|--------|-------------------|
| DMN2991UTQ-7 | SOT523 | 3000/Tape & Reel |
| DMN2991UTQ-13 | SOT523 | 10000/Tape & Reel |

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

| Date Code Key | | | ве5 | <u>▼</u> м | SOT523 | $\frac{YM}{Y} = Da$ | Product Typ ate Code M r (ex: H = 2 nth (ex: 9 = | larking 2020) | | | | |
|---------------|------|------|------|----------------|--------|---------------------|---|------------------|------|------|------|------|
| Year | 2020 | 2021 | 2022 | 2023 | 2024 | 2025 | 2026 | 2027 | 2028 | 2029 | 2030 | 2031 |
| Code | Н | I | J | K | L | М | N | 0 | Р | R | S | Т |
| Manah | lan | Fak | Мак | A | Max | l | l. d | Διια | Sep | Oct | Nov | Dec |
| Month | Jan | Feb | Mar | Apr | May | Jun | Jul | Aug | Sep | 001 | NOV | Dec |
| Code | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 0 | Ν | D |



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

| Cha | racteristic | | Symbol | Value | Unit |
|---|--|--|------------------|-------------|------|
| Drain-Source Voltage | | | VDSS | 20 | V |
| Gate-Source Voltage | | | V _{GSS} | ±10 | V |
| Continuous Drain Current (Note 6) V _{GS} = 4.5V | Steady $T_A = +25^{\circ}C$ State $T_A = +75^{\circ}C$ | | D | 0.3 0.24 | A |
| Maximum Continuous Body Diode Forward Current (Note 6) | | | ls | 0.3 | А |
| Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%) | | | ldм | 1.4 | A |

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

| Characteristic | | Symbol | Value | Unit |
|--|--------------|----------|-------------|------|
| Total Power Dissipation (Note 5) | | PD | 0.28 | W |
| Thermal Resistance, Junction to Ambient (Note 5) | Steady State | Reja | 439 | °C/W |
| Total Power Dissipation (Note 6) | | PD | 0.43 | W |
| Thermal Resistance, Junction to Ambient (Note 6) | Steady State | Reja | 291 | °C/W |
| Operating and Storage Temperature Range | | TJ, TSTG | -55 to +150 | °C |

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

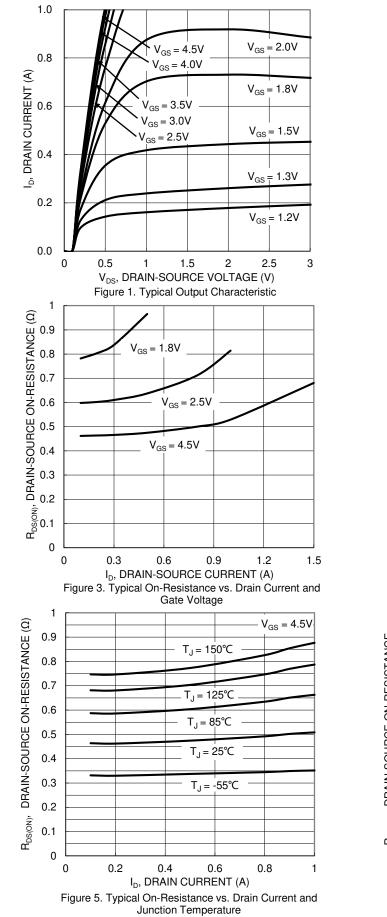
| | | | _ | | | |
|-----------------------------------|---------------------|-----|------|------|------|--|
| Characteristic | Symbol | Min | Тур | Max | Unit | Test Condition |
| OFF CHARACTERISTICS (Note 7) | | | | | | - |
| Drain-Source Breakdown Voltage | BVDSS | 20 | — | — | V | $V_{GS} = 0V, I_D = 250 \mu A$ |
| Zero Gate Voltage Drain Current | IDSS | | — | 1 | μA | $V_{DS} = 16V, V_{GS} = 0V$ |
| Gate-Source Leakage | IGSS | | — | ±10 | μA | $V_{GS} = \pm 5V, \ V_{DS} = 0V$ |
| ON CHARACTERISTICS (Note 7) | | | | | | |
| Gate Threshold Voltage | VGS(TH) | 0.5 | — | 1.0 | V | $V_{DS} = V_{GS}$, $I_D = 250 \mu A$ |
| | | | 0.4 | 3.0 | | $V_{GS} = 4.5V, I_D = 100mA$ |
| Static Drain-Source On-Resistance | Bracow | | 0.6 | 4.0 | Ω | $V_{GS} = 2.5V, I_D = 50mA$ |
| Static Drain-Source On nesistance | RDS(ON) | | 0.8 | 6.0 | | $V_{GS} = 1.8V, I_{D} = 20mA$ |
| | | _ | 1.0 | 10.0 | | $V_{GS} = 1.5V, I_{D} = 10mA$ |
| Diode Forward Voltage | V _{SD} | _ | 0.8 | 1.0 | V | $V_{GS} = 0V, I_{S} = 150mA$ |
| DYNAMIC CHARACTERISTICS (Note 8) | - | | | | | • |
| Input Capacitance | Ciss | _ | 21.5 | _ | pF | |
| Output Capacitance | Coss | _ | 4.9 | _ | pF | V _{DS} = 15V, V _{GS} = 0V, f = 1.0MHz |
| Reverse Transfer Capacitance | Crss | _ | 3.7 | _ | pF | |
| Gate Resistance | Rg | | 0.94 | _ | Ω | $V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$ |
| Total Gate Charge | Qg | | 0.35 | _ | nC | |
| Gate-Source Charge | Qgs | _ | 0.07 | _ | nC | VGS = 4.5V, VDS = 10V, ID = 250mA |
| Gate-Drain Charge | Q _{gd} | _ | 0.08 | _ | nC | |
| Turn-On Delay Time | td(on) | _ | 5.6 | — | ns | |
| Turn-On Rise Time | tR | _ | 4.9 | | ns | $V_{DD} = 10V, V_{GS} = 4.5V,$ |
| Turn-Off Delay Time | t _{D(OFF)} | _ | 60.6 | | ns | $R_{L} = 47\Omega, R_{g} = 10\Omega,$ |
| Turn-Off Fall Time | tF | _ | 27.6 | _ | ns | I _D = 200mA |

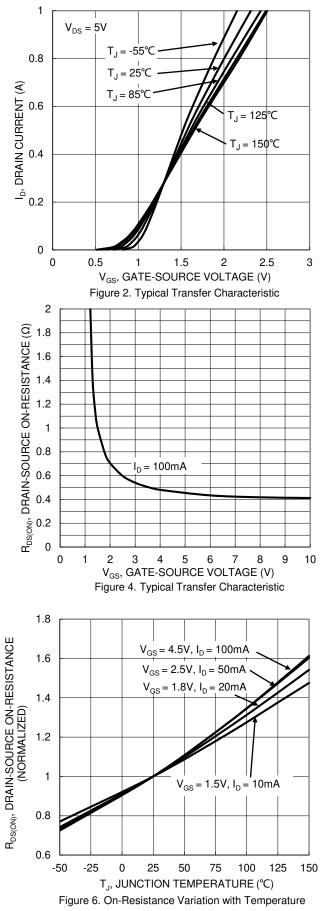
Notes: 5. Device mounted on FR-4 substrate PC board, with minimum recommended pad layout.

Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.



DMN2991UTQ

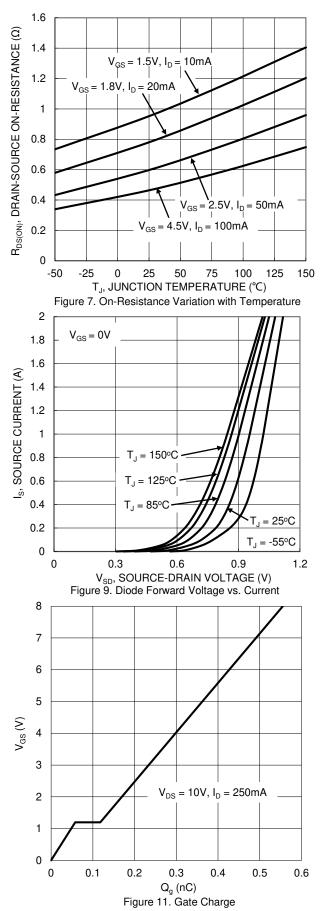


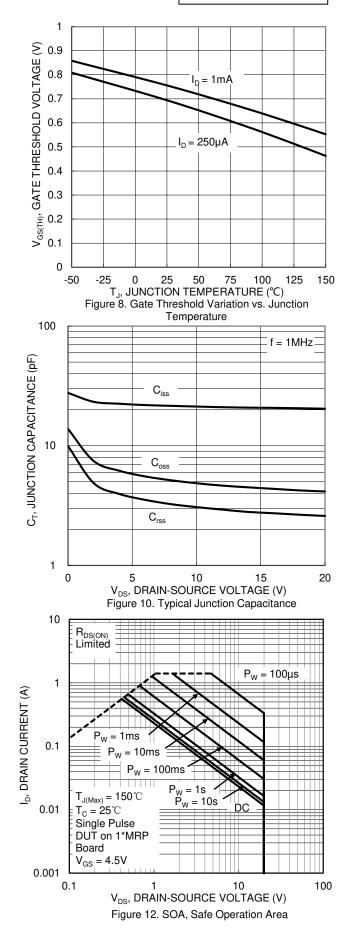


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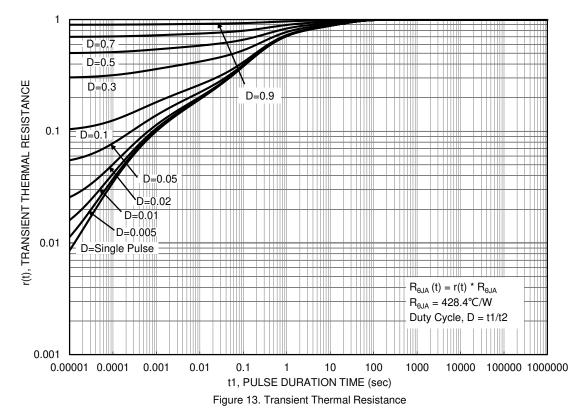






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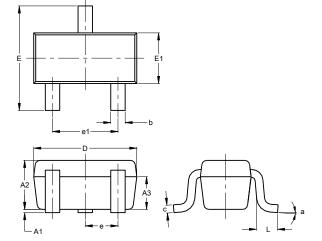




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

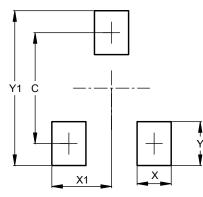




| SOT523 | | | | | | | |
|--------|----------------------|---------|------|--|--|--|--|
| Dim | Min | Max | Тур | | | | |
| A1 | 0.00 | 0.10 | 0.05 | | | | |
| A2 | 0.60 | 0.80 | 0.75 | | | | |
| A3 | 0.45 | 0.65 | 0.50 | | | | |
| b | 0.15 | 0.30 | 0.22 | | | | |
| С | 0.10 | 0.20 | 0.12 | | | | |
| D | 1.50 | 1.70 | 1.60 | | | | |
| Е | 1.45 | 1.75 | 1.60 | | | | |
| E1 | 0.75 | 0.85 | 0.80 | | | | |
| е | | 0.50 BS | С | | | | |
| e1 | 0.90 | 1.10 | 1.00 | | | | |
| L | 0.20 | 0.40 | 0.33 | | | | |
| а | 0° | | 8° | | | | |
| Α | All Dimensions in mm | | | | | | |

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT523

| Dimensions | Value (in mm) |
|------------|------------------|
| С | 1.29 |
| Х | 0.40 |
| X1 | 0.70 |
| Y | 0.51 |
| Y1 | 1.80 |

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