# RENESAS

# DATA SHORT

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## ISL69122

Digital Dual Output, 4-Phase Configurable, VR13 PWM Controller with SVID and 2-Bit AVID Interfaces

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The <u>ISL69122</u> is a digital dual output, flexible multiphase  $(X+Y \le 4)$  PWM controller that is compliant with Intel VR13 specifications. The digital multiphase controller can be configured to support any desired phase assignments up to a maximum of four phases across the two outputs (X and Y). For example, 3+1, 2+2, or even single output operation as a 4+0 configuration are supported. With a flexible X+Y  $\le$  4 phase assignment along with PMBus and AVID interfaces, the ISL69122 is ideal for controlling the microprocessor core, memory, and system rails of Intel VR13 platforms.

The ISL69122 uses the proprietary Renesas digital linear synthetic current modulation scheme to achieve the industry's best combination of transient response and ease of tuning while addressing the challenges of powering the latest generation of Intel microprocessors. Device configuration and telemetry monitoring are accomplished using the intuitive Renesas PowerNavigator<sup>™</sup> software. Diode emulation and automatic phase add/drop features allow the user to extract maximum efficiency from the converter regardless of load conditions.

The ISL69122 supports a comprehensive fault management system to enable the design of highly reliable systems. From an overcurrent protection scheme including peak and average detection, to the configurable power-good and catastrophic fault protection flags, any need is accommodated.

With minimal external components, the ability to store eight configurations, robust fault management, and highly accurate regulation capability, implementing a high-performance, multiphase regulator has never been easier.

## **Related Literature**

- For a full list of related documents, visit our website
  - <u>ISL69122</u> product page

## Applications

- Core and memory for Intel VR13 based designs
  - High performance server core and memory rails
  - High performance graphics rails
- Networking, data center, storage, and general purpose

### Features

- 2-bit AVID interface
- Intel SVID interface
- · Advanced linear digital modulation scheme
  - Zero latency synthetic current control for excellent high frequency current balance
  - Auto phase add/drop for excellent load vs efficiency profile
  - Excellent DVID performance
  - Dual edge modulation for faster transient response
- Flexible phase assignment from 0 to 4 phases per output
- Up to 1MHz operation for high density designs
- · Diode braking for overshoot reduction
- Diode emulation for enhanced light-load efficiency
- Differential remote voltage sensing supports  $\pm 0.5\%$  closed-loop system accuracy over load, line, and temperature
- Highly accurate current sensing for excellent load line regulation and accurate OCP
  - Supports ISL99227 60A smart power stages
  - Supports DCR sense with integrated temperature compensation
- Comprehensive fault management enables high reliability systems
  - Pulse-by-pulse phase current limiting
  - Total output current protection
  - Output and input OV/UV protection
  - Open voltage sense detect
  - Black box recording capability for faults
  - Configurable Catastrophic Failure Protection (CFP) flag output
- Intuitive configuration using PowerNavigator
- SMBus/PMBus V1.3 compatible
  - Up to 2MHz bus interface
  - NVM to store up to 8 configurations
- Pb-free (RoHS compliant)



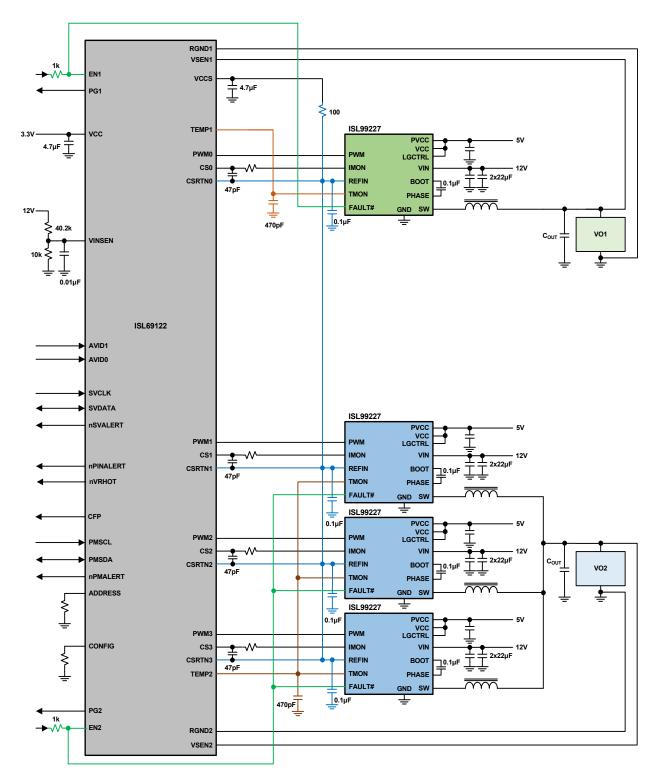


Figure 1. VR with ISL99227 Smart Power Stages Typical Application



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