

Features

- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)
- Advanced Trench MOSFET Process Technology
- Epoxy Meets UL 94 V-0 Flammability Rating
- Moisture Sensitivity Level 1
- Halogen Free. "Green" Device ^(Note 1)
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Maximum Ratings

- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 73.5°C/W Junction to Ambient^(Note 2)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	±20	V
Drain Current	I_D	3.0	A
Pulsed Drain Current ^(Note3)	I_{DM}	10	A
Total Power Dissipation	P_D	1.7	W

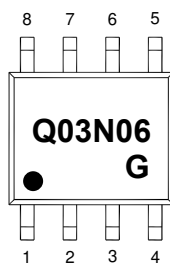
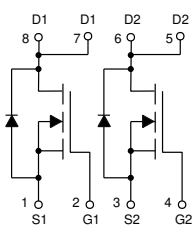
Note:

1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

2. The value of $R_{\theta JA}$ is measured with the device mounted on 1 in2 FR-4 board with 2oz.

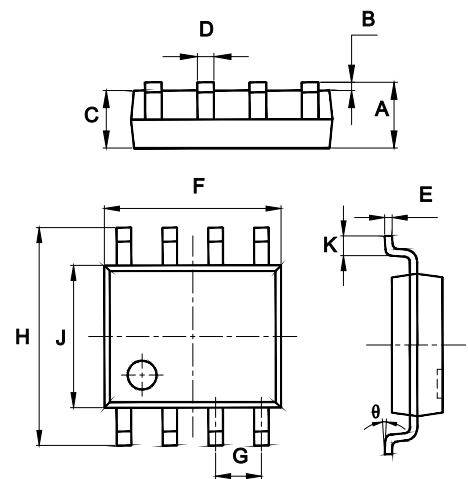
3. Pulse Test : Pulse Width ≤ 300µs, Duty Cycle ≤ 0.5%.

Internal Structure and Marking Code



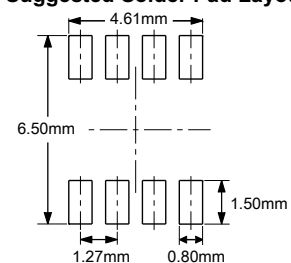
Dual N-Channel Power MOSFET

SOP-8



DIM	DIMENSIONS				NOTE
	INCHES		MM		
	MIN	MAX	MIN	MAX	
A	0.053	0.069	1.35	1.75	
B	0.004	0.010	0.10	0.25	
C	0.053	0.061	1.35	1.55	
D	0.013	0.020	0.33	0.51	
E	0.007	0.010	0.17	0.25	
F	0.185	0.200	4.70	5.10	
G	0.050		1.270		TYP.
H	0.228	0.244	5.80	6.20	
J	0.150	0.157	3.80	4.00	
K	0.016	0.050	0.40	1.27	
θ	0°	8°	0°	8°	

Suggested Solder Pad Layout



Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	60	65		V
Gate-Source Leakage Current	I_{GSS}	$V_{DS}=0V, V_{GS}=\pm 20V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=60V, V_{GS}=0V$			1	μA
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	0.8	1.35	1.4	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=3A$		75	105	m Ω
		$V_{GS}=4.5V, I_D=3A$		85	125	
Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_S=3A$			1.2	v
Forward Transconductance	g_{FS}	$V_{DS}=15V, I_D=2A$	1.4			S
Diode Forward Current	I_S				3.0	A
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS}=30V, V_{GS}=0V, f=1MHz$		247		pF
Output Capacitance	C_{oss}			34		
Reverse Transfer Capacitance	C_{rss}			19.5		
Switching Characteristics						
Total Gate Charge	Q_g	$V_{DS}=30V, V_{GS}=4.5V, I_D=3A$		6		nC
Gate-Source Charge	Q_{gs}			1		
Gate-Drain Charge	Q_{gd}			1.3		
Turn-On Delay Time	$t_{d(on)}$	$V_{GS}=10V, V_{DD}=30V, I_D=1.5A, R_{GEN}=1\Omega$		6		ns
Turn-On Rise Time	t_r			15		
Turn-Off Delay Time	$t_{d(off)}$			15		
Turn-Off Fall Time	t_f			10		

Curve Characteristics

Fig. 1 - Output Characteristics

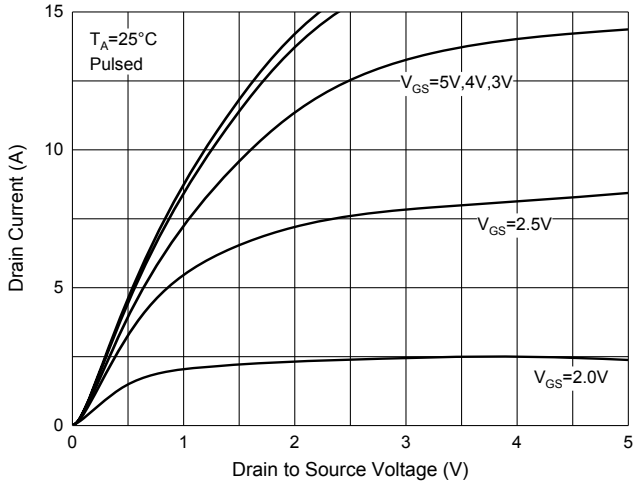


Fig. 2 - Transfer Characteristics

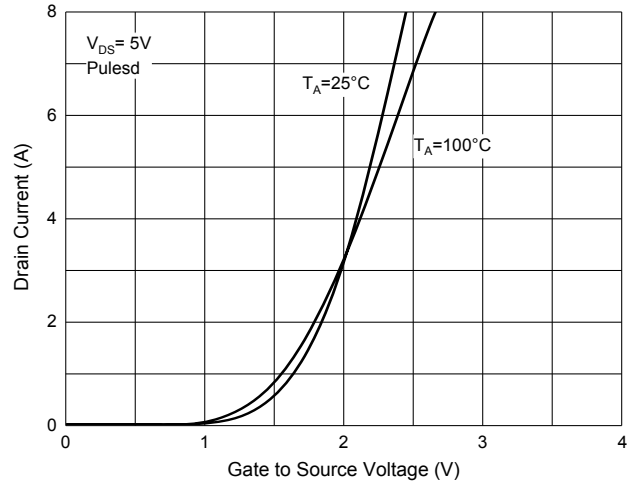


Fig. 3 - $R_{DS(ON)} - I_D$

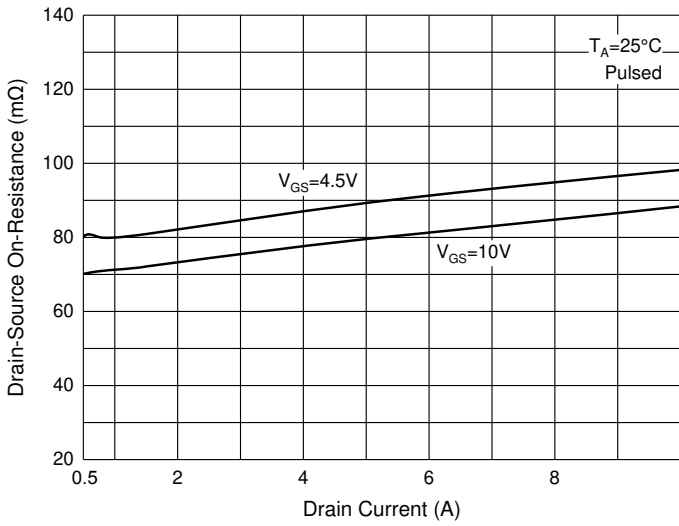


Fig. 4 - $R_{DS(ON)} - V_{GS}$

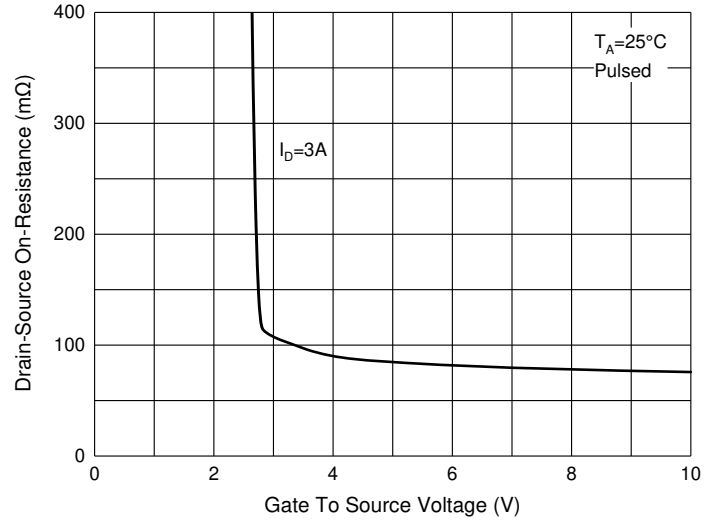


Fig. 5 - $I_S - V_{SD}$

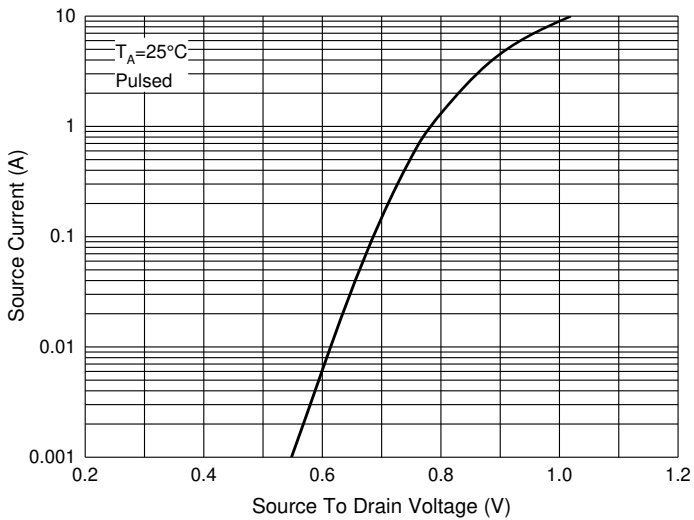
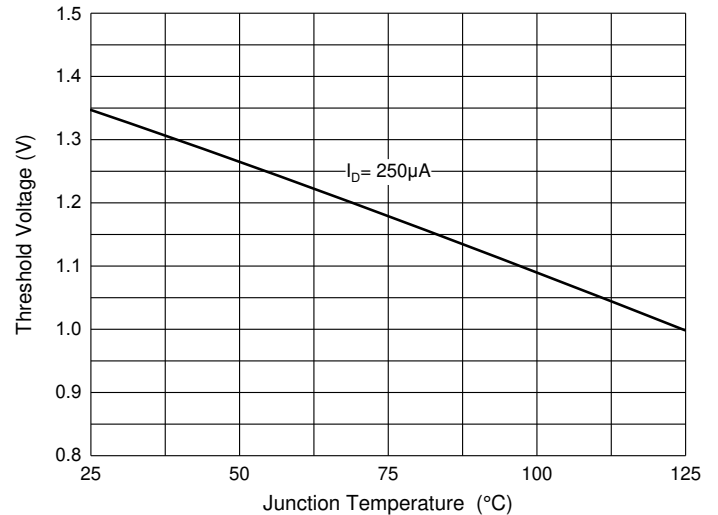


Fig. 6 - Threshold Voltage



Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 4Kpcs/Reel

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