# RX3R05BBH

### Nch 150V 50A Power MOSFET

Datasheet

V <sub>DSS</sub>	150V
R <sub>DS(on)</sub> (Max.)	29mΩ
I <sub>D</sub>	±50A
P <sub>D</sub>	89W

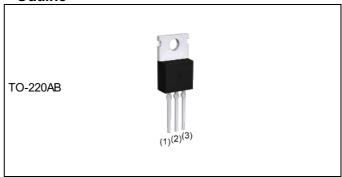
### Features

- 1) Low on resistance
- 2) High power small mold package (TO220AB)
- 3) Pb-free plating; RoHS compliant
- 4) 100% Rg and UIS tested
- 5) Halogen free

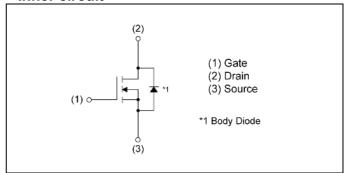
# Application

Switching

### Outline



# •Inner circuit



Packaging specifications

• i ackaç	Jing specifications	
	Packing	Tube
T. 100 0	Quantity (pcs)	1000
Type	Taping code	C16
	Marking	RX3R05BBH

# ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit	
Drain - Source voltage		V <sub>DSS</sub>	150	V
Continuous drain current V <sub>GS</sub> = 10V		I <sub>D</sub> *1	±50	Α
Pulsed drain current	I <sub>DP</sub> *2	±200	Α	
Gate - Source voltage	$V_{GSS}$	±20	V	
Avalanche current, single pulse	I <sub>AS</sub> *3	13	Α	
Avalanche energy, single pulse	E <sub>AS</sub> *3	7.2	mJ	
Power dissipation	P <sub>D</sub> *1	89	W	
Junction temperature	T <sub>j</sub>	150	°C	
Operating junction and storage ter	T <sub>stg</sub>	-55 to +150	°C	

### ●Thermal resistance

Parameter	Symbol	Values			Lleit
		Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R <sub>thJC</sub> *1	-	ı	1.40	°C/W

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Davamatav	Cymahal	Conditions	Values			l limit	
Parameter	Symbol Conditions —		Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V$ , $I_D = 1mA$	150	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I <sub>D</sub> = 1mA referenced to 25°C	-	98	-	mV/°C	
Zero gate voltage drain current	$I_{DSS}$ $V_{DS} = 150V, V_{GS} = 0V$		-	-	5	μA	
Gate - Source leakage current	I <sub>GSS</sub>	$I_{GSS}$ $V_{GS} = \pm 20V, V_{DS} = 0V$		-	±500	nA	
Gate threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 1mA$	2.0	-	4.0	V	
Gate threshold voltage temperature coefficient	$\frac{\DeltaV_{GS(th)}}{\DeltaT_j}$	I <sub>D</sub> = 1mA referenced to 25°C	-	-5.7	-	mV/°C	
Static drain - source	R <sub>DS(on)</sub> *4	V <sub>GS</sub> = 10V, I <sub>D</sub> = 25A	-	22	29	mO	
on - state resistance		V <sub>GS</sub> = 6V, I <sub>D</sub> = 25A	-	24	35	mΩ	
Gate resistance	R <sub>G</sub> -		-	0.9	-	Ω	
Forward Transfer Admittance	Y <sub>fs</sub>  *4	V <sub>DS</sub> = 5V, I <sub>D</sub> = 25A	17	-	-	S	

<sup>\*1</sup> T<sub>c</sub>=25°C, Limited only by maximum temperature allowed.

<sup>\*2</sup> Pw≤ 10µs , Duty cycle≤ 1%

<sup>\*3</sup> L  $\simeq$  0.05mH, V<sub>DD</sub> = 75V, R<sub>G</sub> = 25 $\Omega$ , Starting T<sub>j</sub> = 25 $^{\circ}$ C Fig.3-1,3-2

<sup>\*4</sup> Pulsed

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Daramatar	Cumbal	Conditions		Lloit			
Parameter	Symbol	I Conditions		Тур.	Max.	Unit	
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	2150	-		
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 75V	-	180	-	pF	
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz		13	-		
Turn - on delay time	t <sub>d(on)</sub> *4	$V_{DD} \simeq 75V, V_{GS} = 10V$	-	24	-		
Rise time	t <sub>r</sub> *4	I <sub>D</sub> = 25A	-	14	-		
Turn - off delay time	t <sub>d(off)</sub> *4	$R_L \simeq 3\Omega$	-	56	-	ns	
Fall time	t <sub>f</sub> *4	$R_G = 10\Omega$	-	22	-		

# • Gate charge characteristics $(T_a = 25^{\circ}C)$

Doromotor	Cymahal	Symbol Conditions		Values			l limit	
Parameter	Symbol			Min.	Тур.	Max.	Unit	
T. I. I. I. C. *1	O *4		V <sub>GS</sub> = 10V	-	37.0	-		
Total gate charge	Q <sub>g</sub> *4	$\mathbf{Q}_{g}$	V <sub>DD</sub> ≃ 75V		-	25.0	-	<b>"</b> C
Gate - Source charge	Q <sub>gs</sub> *4	I <sub>D</sub> = 50A	V <sub>GS</sub> = 6V	-	8.0	-	nC	
Gate - Drain charge	Q <sub>gd</sub> *4			-	9.6	-		

# ● Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Darameter	Cumbal	Conditions	Values			Unit
Parameter	Symbol	I Conditions		Тур.	Max.	Offic
Continuous forward current	I <sub>S</sub>	T = 25°C	1	-	50	Α
Pulse forward current	I <sub>SP</sub> *2	T <sub>a</sub> = 25°C	1	-	200	Α
Forward voltage	V <sub>SD</sub> *4	$V_{GS} = 0V, I_{S} = 50A$	-	-	1.2	V
Reverse recovery time	t <sub>rr</sub> *4	I <sub>S</sub> = 50A, V <sub>GS</sub> =0V	-	115	-	ns
Reverse recovery charge	Q <sub>rr</sub> *4	di/dt = 100A/μs	-	350	-	nC

Fig.1 Power Dissipation Derating Curve

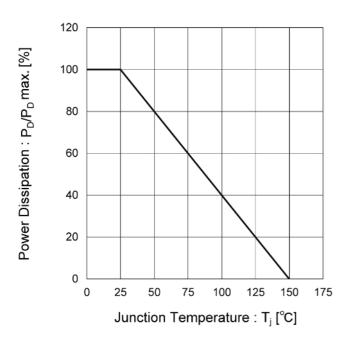
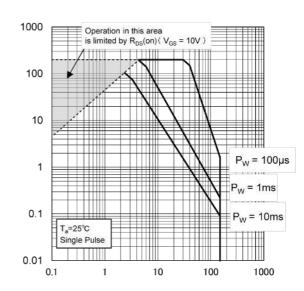


Fig.2 Maximum Safe Operating Area



Drain Current : I<sub>D</sub> [A]

Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

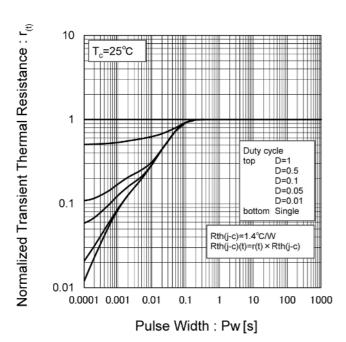


Fig.4 Single Pulse Maximum Power Dissipation

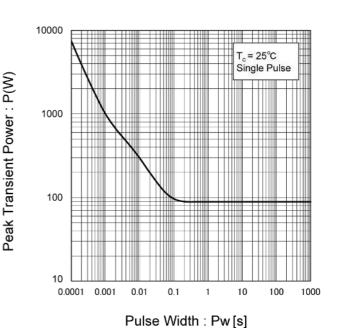


Fig.5 Typical Output Characteristics(I)

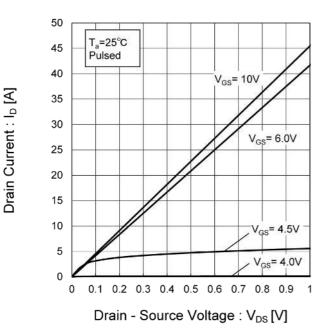


Fig.6 Typical Output Characteristics(II)

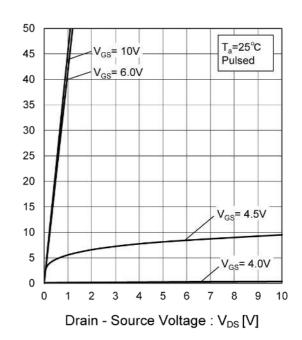


Fig.7 Breakdown Voltage vs.
Junction Temperature

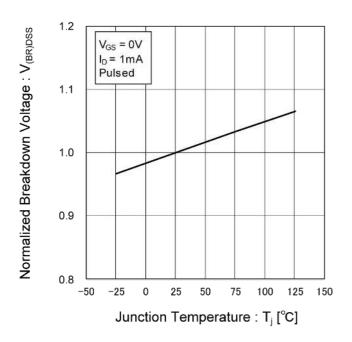
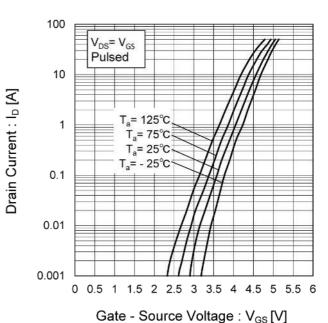


Fig.8 Typical Transfer Characteristics



Drain Current: Ip [A]

Fig.9 Gate Threshold Voltage vs.
Junction Temperature

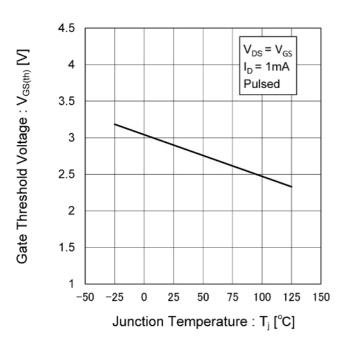


Fig.10 Forward Transfer Admittance vs.
Drain Current

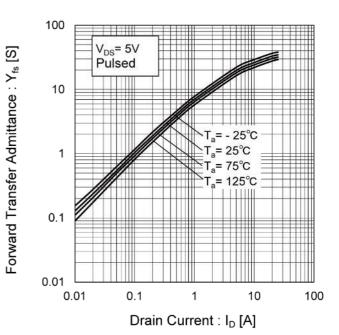


Fig.11 Drain Current Derating Curve

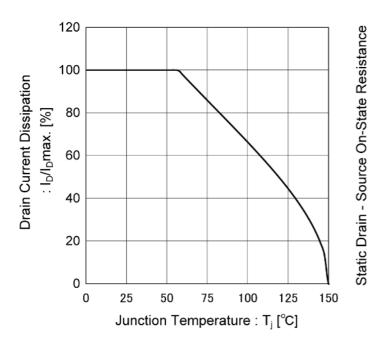


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

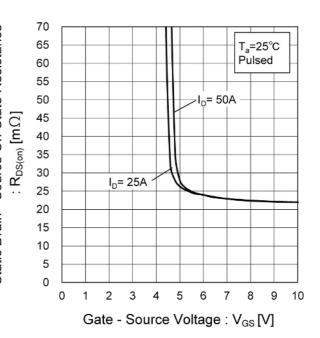


Fig.13 Static Drain - Source On - State
Resistance vs. Junction Temperature

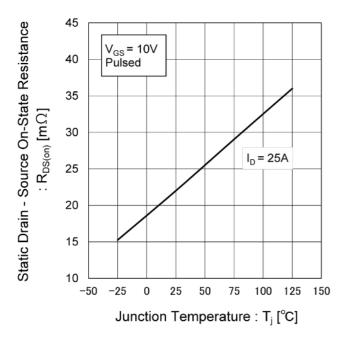


Fig.14 Static Drain - Source On - State
Resistance vs. Drain Current (I)

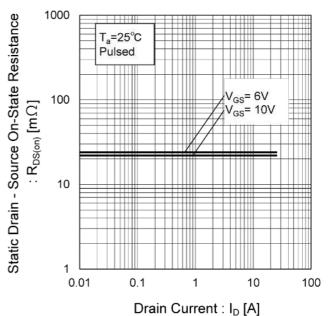


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

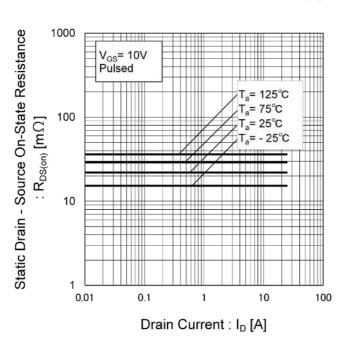


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

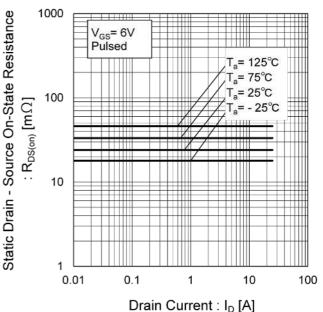


Fig.17 Typical Capacitances vs.

Drain - Source Voltage

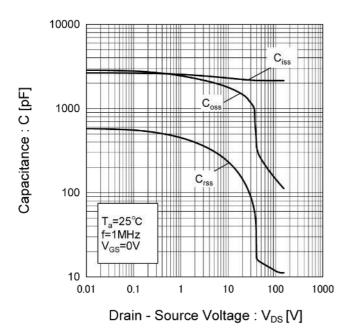


Fig.18 Switching Characteristics

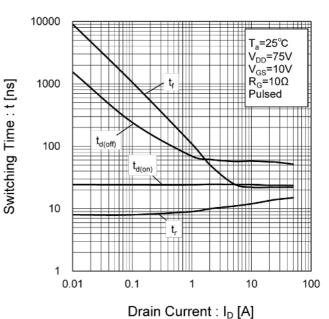


Fig.19 Typical Gate Charge

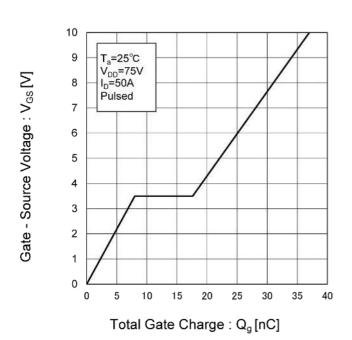
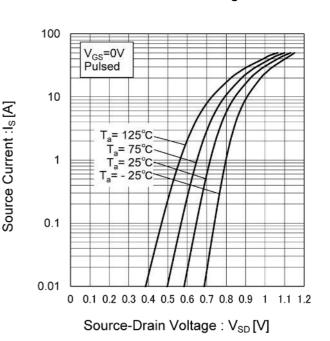


Fig.20 Source Current vs.

Source Drain Voltage



#### Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

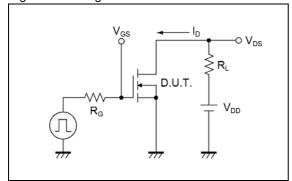


Fig.1-2 Switching Waveforms

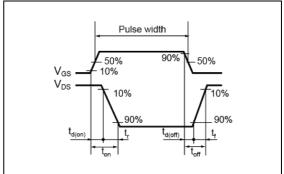


Fig.2-1 Gate Charge Measurement Circuit

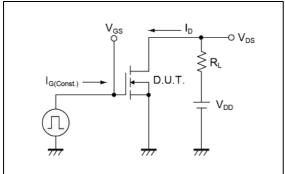


Fig.2-2 Gate Charge Waveform

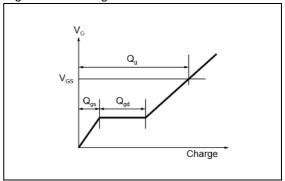


Fig.3-1 Avalanche Measurement Circuit

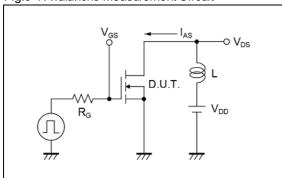
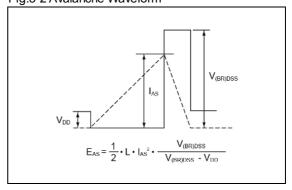
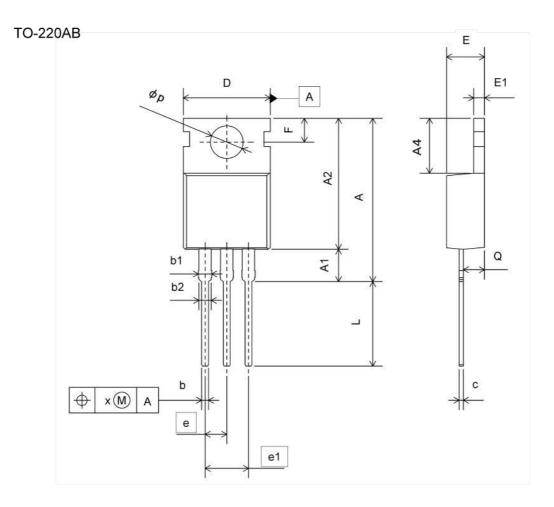


Fig.3-2 Avalanche Waveform



### Dimensions



DIM	MILIME	TERS	INCI	HES
DIM	MIN	MAX	MIN	MAX
Α	18.30	20.00	0.720	0.787
A1	3.60	4.00	0.142	0.157
A2	14.70	16.00	0.579	0.630
A4	6.30	6.60	0.248	0.260
b	0.65	0.95	0.026	0.037
b1	1.20	1.75	0.047	0.069
b2	1.20	1.70	0.047	0.067
С	0.35	0.65	0.014	0.026
D	9.96	10.36	0.392	0.408
E	4.24	4.64	0.167	0.183
E1	1.14	1.40	0.045	0.055
е	2.	54	0.1	00
e1	5.	08	0.2	00
F	2.60	3.00	0.102	0.118
L	9.47	10.37	0.373	0.408
φр	3.69	3.99	0.145	0.157
Q	2.30	2.70	0.091	0.106
х	2	0.38		0.015

Dimension in mm/inches



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CLASSIV	CLASSII	CLASSⅢ	CLASSⅢ

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  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
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- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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