

FDP6676S / FDB6676S

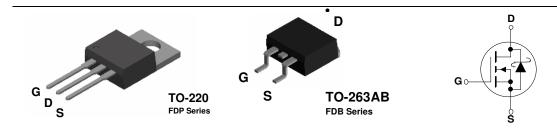
30V N-Channel PowerTrench® SyncFET™

General Description

This MOSFET is designed to replace a single MOSFET and parallel Schottky diode in synchronous DC:DC power supplies. This 30V MOSFET is designed to maximize power conversion efficiency, providing a low $R_{\rm DS(ON)}$ and low gate charge. The FDP/B6676S includes an integrated Schottky diode using Fairchild's monolithic SyncFET technology. The performance of the FDP/B6676S as the low-side switch in a synchronous rectifier is indistinguishable from the performance of the FDP/B6676 in parallel with a Schottky diode.

Features

- 38 A, 30 V. $R_{DS(ON)} = 6.5 \ m\Omega \ @V_{GS} = 10 \ V$ $R_{DS(ON)} = 8.0 \ m\Omega \ @V_{GS} = 4.5 \ V$
- Includes SyncFET Schottky body diode
- Low gate charge (40nC typical)
- High performance trench technology for extremely low R_{DS(ON)} and fast switching
- High power and current handling capability



Absolute Maximum Ratings T_A=25°C unless otherwise noted

Symbol	Parameter		Ratings	Units
V _{DSS}	Drain-Source Voltage		30	V
V _{GSS}	Gate-Source Voltage		±16	V
I _D	Drain Current - Continuous	(Note 1)	76	A
	- Pulsed	(Note 1)	150	
P _D	Total Power Dissipation @ T _C = 25°C Derate above 25°C		70	W
			0.56	W/°C
T _J , T _{STG}	Operating and Storage Junction Temper	ature Range	-55 to +150	°C
TL	Maximum lead temperature for soldering 1/8" from case for 5 seconds	purposes,	275	°C

Thermal Characteristics

$R_{ heta JC}$	Thermal Resistance, Junction-to-Case	1.8	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	55	°C/W

Package Marking and Ordering Information

Device Marking						
		Device	Reel Size	Tape width	Quantity	
	FDB6676S	FDB6676S	13"	24mm	800	
	FDP6676S	FDP6676S	Tube	n/a	45	

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Drain-Sc	ource Avalanche Ratings (Note	2)			I	l
W _{DSS}	Drain-Source Avalanche Energy	Single Pulse, $V_{DD} = 25 \text{ V}$, $I_D=12\text{A}$			310	mJ
I _{AR}	Drain-Source Avalanche Current				12	Α
Off Char	acteristics		II.		ı	ı
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_{D} = 1 \text{ mA}$	30			V
ΔBV _{DSS} ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 1 mA, Referenced to 25°C		25		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 24 \text{ V}, \qquad V_{GS} = 0 \text{ V}$			500	μΑ
I _{GSSF}	Gate-Body Leakage, Forward	$V_{GS} = 16 \text{ V}, \qquad V_{DS} = 0 \text{ V}$			100	nA
I _{GSSR}	Gate-Body Leakage, Reverse	$V_{GS} = -16 \text{ V}$ $V_{DS} = 0 \text{ V}$			-100	nA
On Char	acteristics (Note 2)				•	•
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}$, $I_D = 1$ mA	1	1.3	3	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate Threshold Voltage Temperature Coefficient	I _D = 1 mA, Referenced to 25°C		-8.4		mV/°C
$R_{DS(on)}$	Static Drain–Source On–Resistance	$\label{eq:VGS} \begin{array}{llllllllllllllllllllllllllllllllllll$		4.7 5.2 7.3	6.5 8.0 11	mΩ
I _{D(on)}	On-State Drain Current	$V_{GS} = 10 \text{ V}, \qquad V_{DS} = 10 \text{ V}$	60			Α
g FS	Forward Transconductance	$V_{DS} = 10 \text{ V}, \qquad I_{D} = 38 \text{ A}$		145		S
Dvnamio	Characteristics					
C _{iss}	Input Capacitance	$V_{DS} = 15 \text{ V}, \qquad V_{GS} = 0 \text{ V},$		4853		pF
Coss	Output Capacitance	f = 1.0 MHz		850		pF
C _{rss}	Reverse Transfer Capacitance	7		316		pF
Switchir	ng Characteristics (Note 2)	•				
t _{d(on)}	Turn-On Delay Time	$V_{DS} = 15 \text{ V}, \qquad I_{D} = 1 \text{ A}, \ V_{GS} = 10 \text{ V}, \qquad R_{GEN} = 6 \Omega$		14	25	ns
t _r	Turn-On Rise Time	$V_{GS} = 10 \text{ V}, \qquad R_{GEN} = 6 \Omega$		11	20	ns
t _{d(off)}	Turn-Off Delay Time			89	142	ns
t _f	Turn-Off Fall Time			31	50	ns
Qg	Total Gate Charge	$V_{DS} = 15 \text{ V}, \qquad I_{D} = 38 \text{ A},$		40	56	nC
Q _{gs}	Gate-Source Charge	$V_{GS} = 5 V$		10		nC
Q_{gd}	Gate-Drain Charge			11		nC
Drain-S	ource Diode Characteristics	and Maximum Ratings				
V_{SD}	Drain-Source Diode Forward Voltage	$V_{GS} = 0 \text{ V}, I_S = 3.5 \text{ A}$ (Note 1) $V_{GS} = 0 \text{ V}, I_S = 7 \text{ A}$ (Note 1)		0.4 0.5	0.7	V
t _{rr}	Diode Reverse Recovery Time	$I_F = 3.5 \text{ A},$		28.5		nS
Q _{rr}	Diode Reverse Recovery Charge	$d_{iF}/d_t = 300 \text{ A/}\mu\text{s} \qquad \text{(Note 2)}$		57		nC

Notes:

^{1.} Pulse Test: Pulse Width < 300 μ s, Duty Cycle < 2.0%

^{2.} See "SyncFET Schottky body diode characteristics" below.

Typical Characteristics

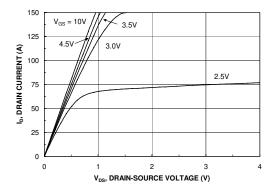


Figure 1. On-Region Characteristics.

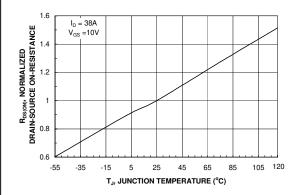


Figure 3. On-Resistance Variation with Temperature.

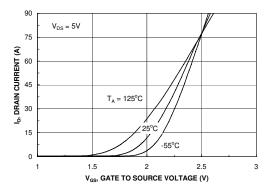


Figure 5. Transfer Characteristics.

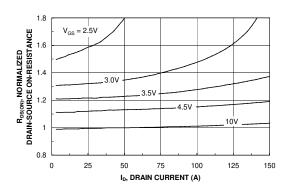


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.

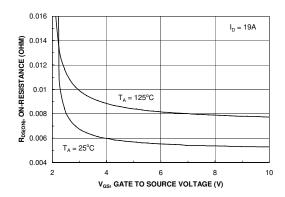


Figure 4. On-Resistance Variation with Gate-to-Source Voltage.

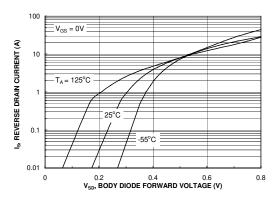
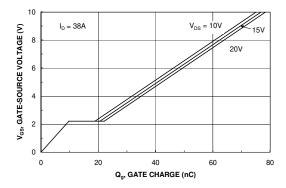


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

Typical Characteristics (continued)



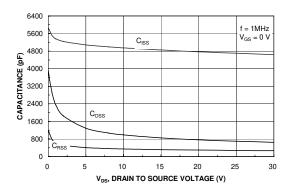


Figure 7. Gate Charge Characteristics.

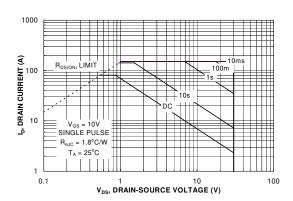


Figure 8. Capacitance Characteristics.

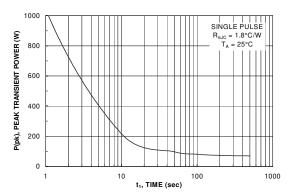


Figure 9. Maximum Safe Operating Area.



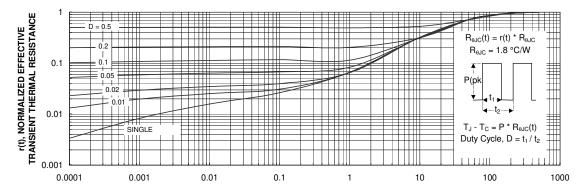


Figure 11. Transient Thermal Response Curve.

Typical Characteristics (continued)

SyncFET Schottky Body Diode Characteristics

Fairchild's SyncFET process embeds a Schottky diode in parallel with PowerTrench MOSFET. This diode exhibits similar characteristics to a discrete external Schottky diode in parallel with a MOSFET. Figure 12 FDP6676S.

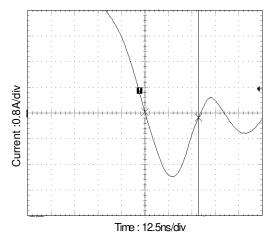


Figure 12. FDP6676S SyncFET body diode reverse recovery characteristic.

For comparison purposes, Figure 13 shows the reverse recovery characteristics of the body diode of an equivalent size MOSFET produced without SyncFET (FDP6676).

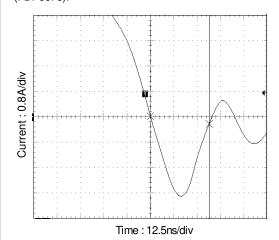


Figure 13. Non-SyncFET (FDP6676) body diode reverse recovery characteristic.

Schottky barrier diodes exhibit significant leakage at high temperature and high reverse voltage. This will increase the power in the device.

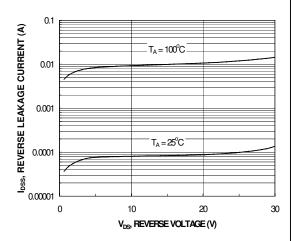


Figure 14. SyncFET diode reverse leakage versus drain-source voltage and temperature.

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