ON Semiconductor

Is Now



To learn more about onsemi™, please visit our website at www.onsemi.com

onsemi and ONSEMI. and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. onsemi reserves the right to make changes at any time to any products or information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/ or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi does not convey any license under any of its intellectual property rights nor the rights of others. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use onsemi products for any such unintended or unauthorized application,

MOSFET – Power, N-Channel, SUPERFET[®] III, Easy Drive 650 V, 10 A, 360 m Ω

FCPF360N65S3R0L-F154

Description

SUPERFET III MOSFET is ON Semiconductor's brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This advanced technology is tailored to minimize conduction loss, provides superior switching performance, and withstand extreme dv/dt rate. Consequently, SUPERFET III MOSFET Easy drive series helps manage EMI issues and allows for easier design implementation.

Features

- $700 \text{ V} @ \text{T}_{\text{J}} = 150^{\circ}\text{C}$
- Typ. $R_{DS(on)} = 310 \text{ m}\Omega$
- Ultra Low Gate Charge (Typ. Q_g = 18 nC)
- Low Effective Output Capacitance (Typ. C_{oss(eff.)} = 173 pF)
- 100% Avalanche Tested
- These Devices are Pb-Free and are RoHS Compliant

Applications

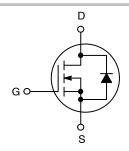
- Computing / Display Power Supplies
- Telecom / Server Power Supplies
- Industrial Power Supplies
- Lighting / Charger / Adapter



ON Semiconductor®

www.onsemi.com

| V _{DSS} | R _{DS(ON)} MAX | I _D MAX |
|------------------|-------------------------|--------------------|
| 650 V | 360 mΩ @ 10 V | 10 A |

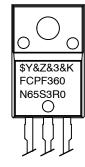


N-Channel MOSFET



TO-220F Ultra Narrow Lead CASE 221BN

MARKING DIAGRAM



\$Y = ON Semiconductor Logo &Z = Assembly Plant Code &3 = Data Code (Year & Week)

&K = Lot

FCPF360N65S3R0 = Specific Device Code

ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, Unless otherwise specified)

| Symbol | Paramet | Value | Unit | |
|-----------------------------------|--|-------------------------------------|-------------|------|
| V _{DSS} | Drain to Source Voltage | | 650 | V |
| V _{GSS} | GSS Gate to Source Voltage DC | | ±30 | V |
| | | AC (f > 1 Hz) | ±30 | V |
| I _D | I_D Drain Current Continuous ($T_C = 25^{\circ}C$) | | 10* | Α |
| | | Continuous (T _C = 100°C) | 6* | |
| I _{DM} | Drain Current | Pulsed (Note 1) | 25* | Α |
| E _{AS} | Single Pulsed Avalanche Energy (Note 2) | 40 | mJ | |
| I _{AS} | Avalanche Current (Note 2) | valanche Current (Note 2) | | Α |
| E _{AR} | Repetitive Avalanche Energy (Note 1) | | 0.27 | mJ |
| dv/dt | MOSFET dv/dt | | 100 | V/ns |
| | Peak Diode Recovery dv/dt (Note 3) | 20 | | |
| P_{D} | Power Dissipation | (T _C = 25°C) | 27 | W |
| | | Derate Above 25°C | 0.22 | W/°C |
| T _J , T _{STG} | Operating and Storage Temperature Range | | −55 to +150 | °C |
| TL | Maximum Lead Temperature for Soldering | 300 | °C | |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.
*Drain current limited by maximum junction temperature.

THERMAL CHARACTERISTICS

| Symbol | Parameter | Value | Unit |
|---------------|---|-------|------|
| $R_{	hetaJC}$ | Thermal Resistance, Junction to Case, Max. | 4.56 | °C/W |
| $R_{	hetaJA}$ | Thermal Resistance, Junction to Ambient, Max. | 62.5 | |

PACKAGE MARKING AND ORDERING INFORMATION

| I | Part Number | Top Marking | Package | Packing Method | Reel Size | Tape Width | Quantity |
|---|-----------------|----------------|---------|----------------|-----------|------------|----------|
| | FCPF360N65S3R0L | FCPF360N65S3R0 | TO-220F | Tube | N/A | N/A | 50 Units |

^{1.} Repetitive rating: pulse-width limited by maximum junction temperature.
2. $I_{AS} = 2.1 \text{ A}$, $R_G = 25 \Omega$, starting $T_J = 25^{\circ}\text{C}$.
3. $I_{SD} \le 5 \text{ A}$, di/dt $\le 200 \text{ A}/\mu\text{s}$, $V_{DD} \le 400 \text{ V}$, starting $T_J = 25^{\circ}\text{C}$.

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

| Symbol | Parameter | Test Conditions | Min | Тур | Max | Unit |
|--------------------------------|--|---|------------|------|------|------|
| OFF CHARACT | ERISTICS | | • | | • | |
| BV _{DSS} | Drain to Source Breakdown Voltage | $V_{GS} = 0 \text{ V}, I_D = 1 \text{ mA}, T_J = 25^{\circ}\text{C}$ | 650 | _ | _ | V |
| | | V _{GS} = 0 V, I _D = 1 mA, T _J = 150°C | 700 | _ | - | V |
| $\Delta BV_{DSS}/\Delta T_{J}$ | Breakdown Voltage Temperature Coefficient | I _D = 1 mA, Referenced to 25°C | - | 0.68 | _ | V/°C |
| I _{DSS} | Zero Gate Voltage Drain Current | V _{DS} = 650 V, V _{GS} = 0 V | † - | _ | 1 | μΑ |
| | | V _{DS} = 520 V, T _C = 125°C | - | 0.58 | - | 1 |
| I _{GSS} | Gate to Source Leakage Current | V _{GS} = ±30 V, V _{DS} = 0 V | - | _ | ±100 | nA |
| ON CHARACTE | ERISTICS | | | | | |
| V _{GS(th)} | Gate Threshold Voltage | $V_{GS} = V_{DS}, I_D = 0.2 \text{ mA}$ | 2.5 | _ | 4.5 | V |
| R _{DS(on)} | Static Drain to Source On Resistance | V _{GS} = 10 V, I _D = 5 A | - | 310 | 360 | mΩ |
| 9FS | Forward Transconductance | V _{DS} = 20 V, I _D = 5 A | - | 6 | - | S |
| OYNAMIC CHA | RACTERISTICS | | • | | • | |
| C _{iss} | Input Capacitance | V _{DS} = 400 V, V _{GS} = 0 V, f = 1 MHz | - | 730 | _ | pF |
| C _{oss} | Output Capacitance | | - | 15 | - | pF |
| C _{oss(eff.)} | Effective Output Capacitance | V _{DS} = 0 V to 400 V, V _{GS} = 0 V | - | 173 | - | pF |
| C _{oss(er.)} | Energy Related Output Capacitance | V _{DS} = 0 V to 400 V, V _{GS} = 0 V | † - | 26 | - | pF |
| Q _{g(tot)} | Total Gate Charge at 10 V | V _{DS} = 400 V, I _D = 5 A, V _{GS} = 10 V | - | 18 | - | nC |
| Q _{gs} | Gate to Source Gate Charge | (Note 4) | - | 4.3 | - | nC |
| Q_{gd} | Gate to Drain "Miller" Charge | | - | 7.6 | - | nC |
| ESR | Equivalent Series Resistance | f = 1 MHz | - | 1 | - | Ω |
| WITCHING CH | IARACTERISTICS | | | | | |
| t _{d(on)} | Turn-On Delay Time | V _{DD} = 400 V, I _D = 5 A, | _ | 12 | _ | ns |
| t _r | Turn-On Rise Time | $V_{GS} = 10 \text{ V}, R_g = 4.7 \Omega$ (Note 4) | - | 11 | - | ns |
| t _{d(off)} | Turn-Off Delay Time | | - | 34 | - | ns |
| t _f | Turn-Off Fall Time | | - | 10 | - | ns |
| SOURCE-DRAI | N DIODE CHARACTERISTICS | | | | | |
| I _S | Maximum Continuous Source to Drain Diode Forward Current | | _ | _ | 10 | Α |
| I _{SM} | Maximum Pulsed Source to Drain Diode Forward Current | | - | _ | 25 | Α |
| V_{SD} | Source to Drain Diode Forward Voltage | V _{GS} = 0 V, I _{SD} = 5 A | - | - | 1.2 | V |
| t _{rr} | Reverse Recovery Time | V _{DD} = 400 V, I _{SD} = 5 A, | - | 241 | - | ns |
| Q _{rr} | Reverse Recovery Charge | $dI_F/dt = 100 A/\mu s$ | - | 2.4 | - | μС |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially independent of operating temperature typical characteristics.

TYPICAL PERFORMANCE CHARACTERISTICS

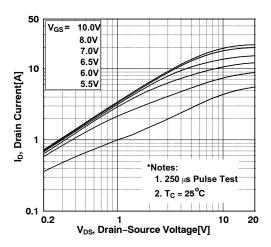


Figure 1. On-Region Characteristics

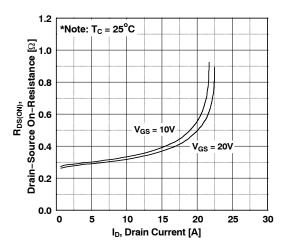


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

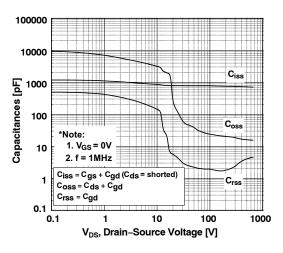


Figure 5. Capacitance Characteristics

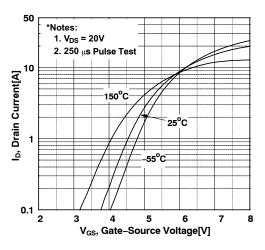


Figure 2. Transfer Characteristics

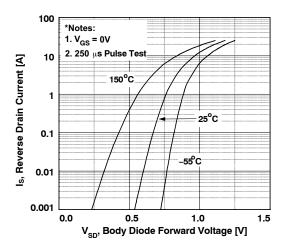


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

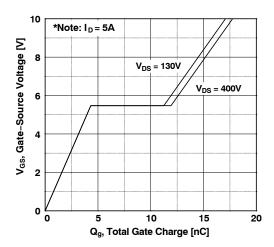


Figure 6. Gate Charge Characteristics

TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

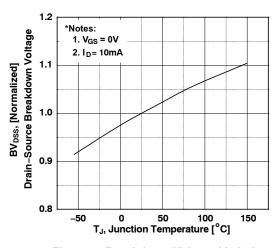


Figure 7. Breakdown Voltage Variation vs. Temperature

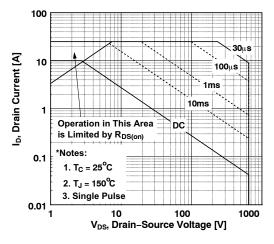


Figure 9. Maximum Safe Operation Area

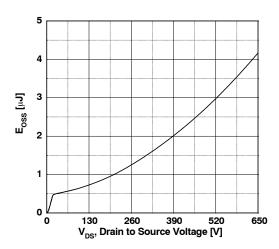


Figure 11. E_{OSS} vs. Drain to Source Voltage

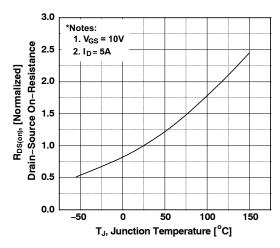


Figure 8. On-Resistance Variant vs. Temperature

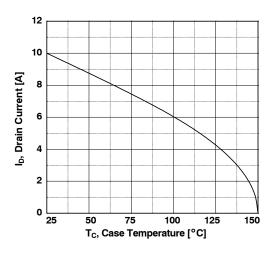


Figure 10. Maximum Drain Current vs. Case Temperature

TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

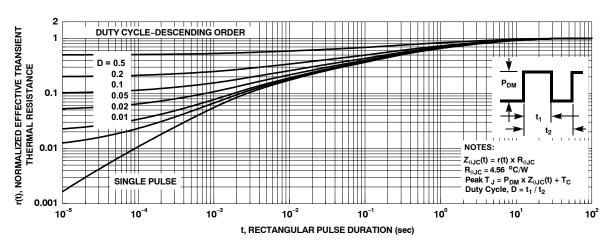


Figure 12. Transient Thermal Response Curve

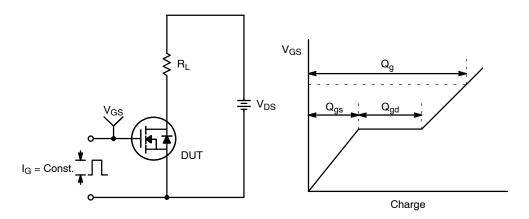


Figure 13. Gate Charge Test Circuit & Waveform

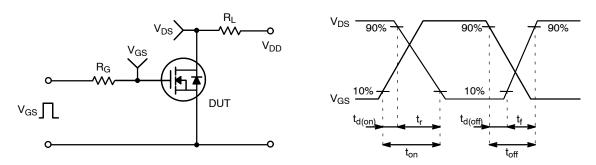


Figure 14. Resistive Switching Test Circuit & Waveforms

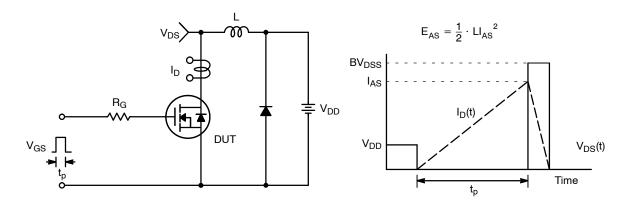


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms

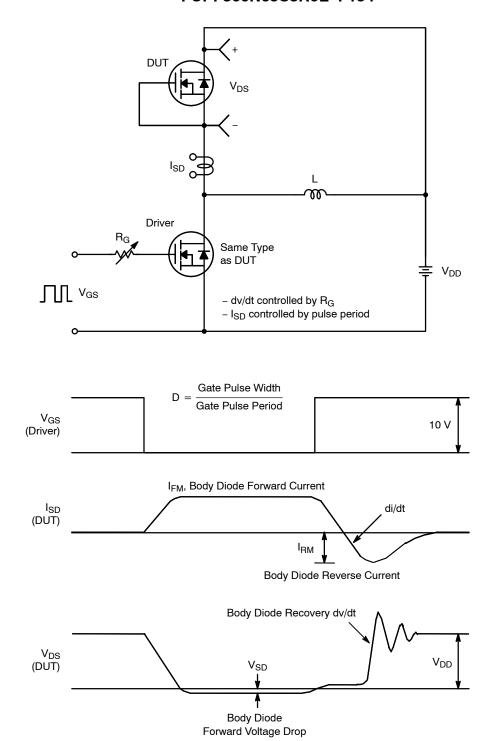


Figure 16. Peak Diode Recovery dv/dt Test Circuit & Waveforms

SUPERFET is a registered trademark of Semiconductor Components Industries, LLC (SCILLC) or its subsidiaries in the United States and/or other countries.

PACKAGE DIMENSIONS

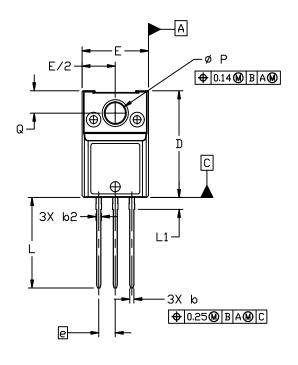
TO-220 FULLPACK, 3-LEAD

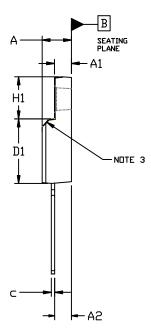
CASE 221BN ISSUE O



NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- 2. CONTROLLING DIMENSION: MILLIMETERS
- 3. CONTOUR UNCONTROLLED IN THIS AREA.
- DIMENSIONS EXCLUDE BURRS, MOLD FLASH, AND TIE BAR PROTRUSIONS.





| | MILLIMETERS | | | | | |
|-----|-------------|-------|-------|--|--|--|
| DIM | MIN. | N□M. | MAX. | | | |
| Α | 4.60 | 4.70 | 4.80 | | | |
| A1 | 2.50 | 2.60 | 2.70 | | | |
| A2 | 2.47 | 2.57 | 2.67 | | | |
| b | 0.56 | 0.63 | 0.69 | | | |
| b2 | | | 0.90 | | | |
| С | 0.46 | 0.53 | 0.59 | | | |
| D | 15.80 | 16.00 | 16.20 | | | |
| D1 | 9.58 | 9.68 | 9.78 | | | |
| E | 10.00 | 10.20 | 10.40 | | | |
| е | 2.54 BSC | | | | | |
| H1 | 6.32 REF | | | | | |
| L | 13.45 | 13.60 | 13.75 | | | |
| L1 | 1.70 | 1.80 | 1.90 | | | |
| Р | 3.00 | 3.10 | 3.20 | | | |
| Q | 3,25 | 3.35 | 3,45 | | | |

ON Semiconductor and the are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor newsers on warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemn

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:
Email Requests to: orderlit@onsemi.com

TECHNICAL SUPPORT
North American Technical Support:
Voice Mail: 1 800–282–9855 Toll Free USA/Canada

Phone: 011 421 33 790 2910

Europe, Middle East and Africa Technical Support:

Phone: 00421 33 790 2910

For additional information, please contact your local Sales Representative