



# 60V N-CHANNEL ENHANCEMENT MODE MOSFET POWERDI5060-8

#### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	I <sub>D</sub> T <sub>C</sub> = +25°C
60V	7.9mΩ @ V <sub>GS</sub> = 10V	69.2A
	10.8mΩ @ V <sub>GS</sub> = 4.5V	59.2A

#### **Features and Benefits**

- 100% Unclamped Inductive Switching (UIS) Test in Production Ensures More Reliable and Robust End Application
- High Conversion Efficiency
- Low R<sub>DS(ON)</sub>—Minimizes On State Losses
- Low Input Capacitance
- Fast Switching Speed
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

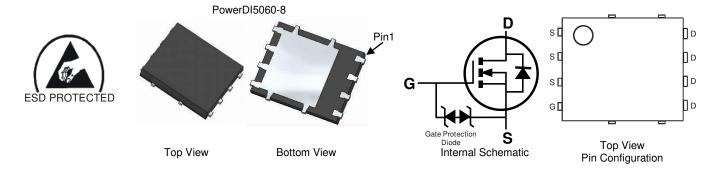
## **Description and Applications**

This MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- Synchronous Rectifier
- DC-DC Converters
- Power Management

#### **Mechanical Data**

- Case: PowerDI<sup>®</sup>5060-8
- Case Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish—Matte Tin Annealed over Copper Leadframe;
   Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.097 grams (Approximate)



#### Ordering Information (Note 4)

Part Number	Case	Packaging
DMT68M8LPS-13	PowerDI5060-8	2500/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, see https://www.diodes.com/design/support/packaging/diodes-packaging/.

#### **Marking Information**



Tesms I = Manufacturer's Marking
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# **Maximum Ratings** (@ $T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	$V_{DSS}$	60	V	
Gate-Source Voltage		$V_{GSS}$	±20	V
Continuous Drain Current (Note 5) V <sub>GS</sub> = 10V	$T_A = +25$ °C $T_A = +70$ °C	I <sub>D</sub>	14.1 11.2	А
Continuous Drain Current (Note 6) V <sub>GS</sub> = 10V	$T_{C} = +25^{\circ}C$ $T_{C} = +70^{\circ}C$	I <sub>D</sub>	69.2 55.4	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I <sub>DM</sub>	270	Α
Maximum Continuous Body Diode Forward Current (Note 6)		Is	69	Α
Pulsed Body Diode Forward Current (10μs Pulse, Duty Cycle = 1%)		I <sub>SM</sub>	270	Α
Avalanche Current, L = 0.1mH		I <sub>AS</sub>	28.1	Α
Avalanche Energy, L = 0.1mH		Eas	39.5	mJ

### **Thermal Characteristics**

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	$P_{D}$	2.4	W
Thermal Resistance, Junction to Ambient (Note 5)		$R_{\Theta JA}$	53	°C/W
Total Power Dissipation (Note 6) $T_C = +25^{\circ}C$		$P_{D}$	56.8	W
Thermal Resistance, Junction to Case (Note 6)		R <sub>eJC</sub>	2.2	°C/W
Operating and Storage Temperature Range		$T_{J}, T_{STG}$	-55 to +150	°C

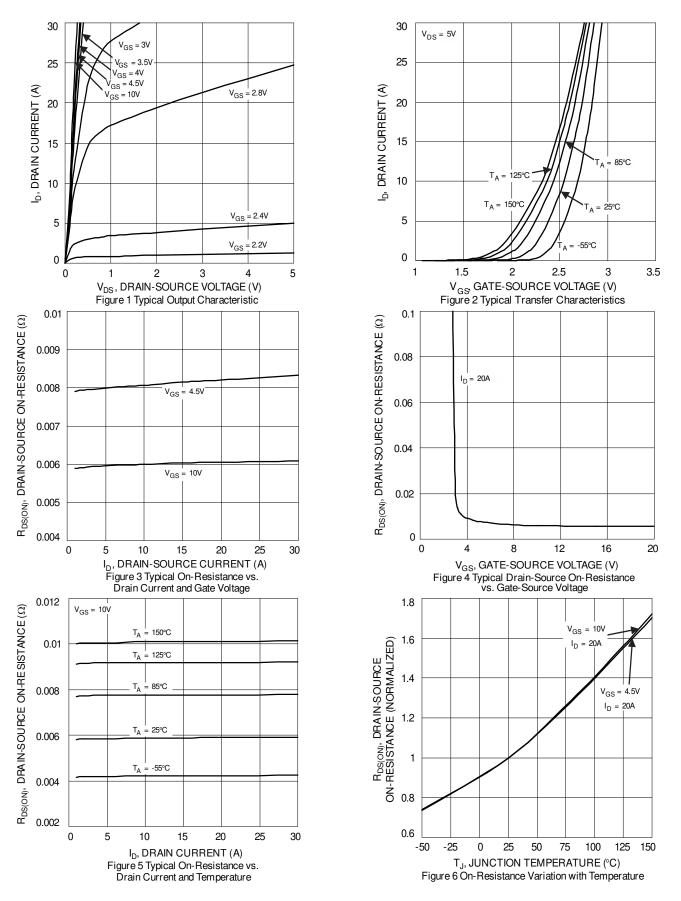
# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

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Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)	T =		ı	ı		T	
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	_	_	V	$V_{GS} = 0V$ , $I_D = 1mA$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	1	μΑ	$V_{DS} = 48V$ , $V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1	_	3	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance		_	5.9	7.9	mΩ	$V_{GS} = 10V, I_D = 20A$	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	7.8	10.8	11177	$V_{GS} = 4.5V, I_D = 20A$	
Diode Forward Voltage	$V_{SD}$	_	0.7	1.2	V	$V_{GS} = 0V$ , $I_S = 1A$	
DYNAMIC CHARACTERISTICS (Note 8)						•	
Input Capacitance	C <sub>iss</sub>	I	2078	_		V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V, f = 1MHz	
Output Capacitance	Coss	1	605	_	pF		
Reverse Transfer Capacitance	Crss	_	44	_			
Gate Resistance	$R_{G}$	_	1.71	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Qg	_	14.4	_			
Total Gate Charge (V <sub>GS</sub> = 10V)	$Q_g$	_	30	_	nC	$V_{DS} = 30V, I_D = 20A$	
Gate-Source Charge	Q <sub>gs</sub>	_	4.1	_	IIC		
Gate-Drain Charge	$Q_{gd}$	_	6.7	_			
Turn-On Delay Time	t <sub>D(ON)</sub>	_	5.2	_		V <sub>DS</sub> = 30V, V <sub>GS</sub> = 10V,	
Turn-On Rise Time	t <sub>R</sub>	_	9.6	_			
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	20.5	_	ns	$I_D = 20A, R_G = 3.3\Omega$	
Turn-Off Fall Time	t <sub>F</sub>	_	8.9	_			
Reverse Recovery Time	t <sub>RR</sub>	_	32.5	_	ns	1 004 4:/44 1004/	
Reverse Recovery Charge	Q <sub>RR</sub>		22.8	_	nC	$I_F = 20A$ , di/dt = 100A/ $\mu$ s	

Notes:

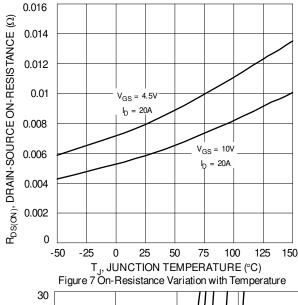
- 5. Device mounted on FR-4 substrate PCB, 2oz copper, with 1inch square copper plate.
- Thermal resistance from junction to soldering point (on the exposed drain pad).
   Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to product testing.

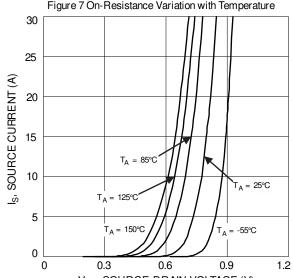


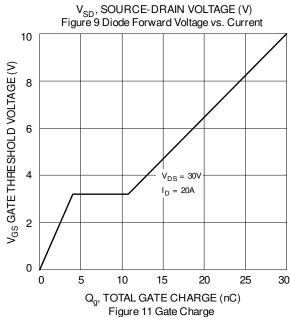


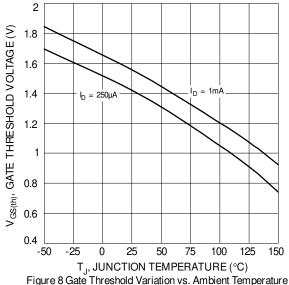


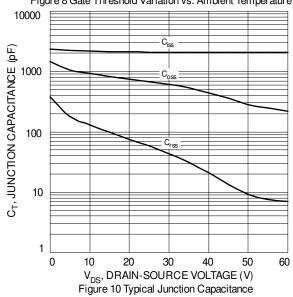


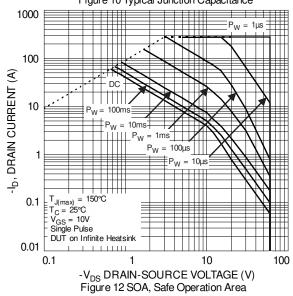




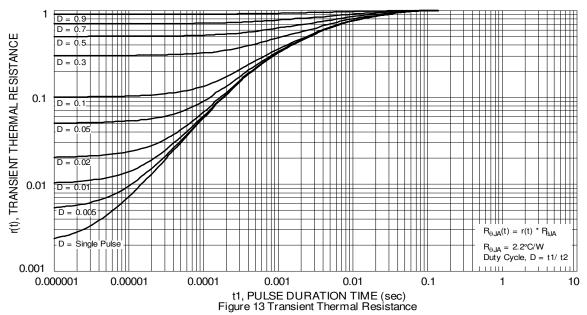










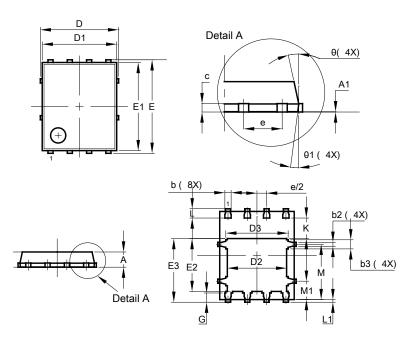




# **Package Outline Dimensions**

 $Please \ see \ http://www.diodes.com/package-outlines.html \ for \ the \ latest \ version.$ 

#### PowerDI5060-8

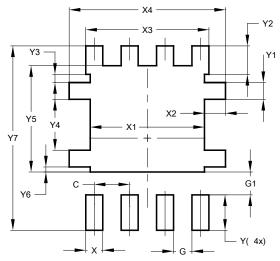


PowerDI5060-8					
Dim	Min Max		Тур		
Α	0.90	1.10	1.00		
A1	0.00	0.05	_		
b	0.33	0.51	0.41		
b2	0.200	0.350	0.273		
b3	0.40	0.80	0.60		
С	0.230	0.330	0.277		
D		5.15 BSC			
D1	4.70	5.10	4.90		
D2	3.70	4.10	3.90		
D3	3.90	4.30	4.10		
Е	(	3.15 BSC	;		
E1	5.60	6.00	5.80		
E2	3.28	3.68	3.48		
E3	3.99	4.39	4.19		
е	1.27 BSC				
G	0.51	0.71	0.61		
K	0.51	_	_		
L	0.51	0.71	0.61		
L1	0.100	0.200	0.175		
М	3.235	4.035	3.635		
M1	1.00	1.40	1.21		
Θ	10°	12°	11°		
Θ1	6°	8°	7°		
All Dimensions in mm					

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### PowerDI5060-8



Dimensions	Value (in mm)			
С	1.270			
G	0.660			
G1	0.820			
X	0.610			
X1	4.100			
X2	0.755			
Х3	4.420			
X4	5.610			
Υ	1.270			
Y1	0.600			
Y2	1.020			
Y3	0.295			
Y4	1.825			
Y5	3.810			
Y6	0.180			
<b>Y</b> 7	6.610			



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