TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

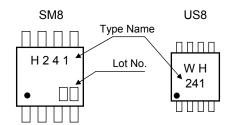
TC7WH241FU, TC7WH241FK

Dual Bus Buffer Non Inverted, 3-State Outputs

Features

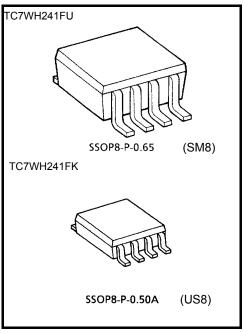
- High speed: t_{pd} = 3.6 ns (typ.) at V_{CC} = 5 V
- Low power dissipation: I_{CC} = 2 μA (max) at Ta = 25°C
- High noise immunity: V_{NIH} = V_{NIL} = 28% V_{CC} (min)
- 5.5-V Tolerant inputs.
- Balanced propagation delays: t_{pLH} ≃ t_{pHL}
- Wide operating voltage range: V_{CC} = 2 to 5.5 V
- Low Noise : V_{OLP} = 0.8 V (max)

Marking



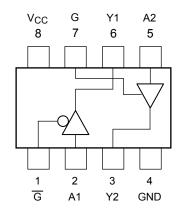
Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	-0.5 to 7.0	V	
DC input voltage	V _{IN}	-0.5 to 7.0	٧	
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5	V	
Input diode current	l _{IK}	-20	mA	
Output diode current	lok	±20 (Note 1)	mA	
DC output current	lout	±25	mA	
DC V _{CC} /ground current	Icc	±50	mA	
Power dissipation	D-	300 (SM8)	mW	
Power dissipation	PD	200 (US8)	11177	
Storage temperature	T _{stg}	-65 to 150	°C	
Lead temperature (10 s)	TL	260	°C	



Weight SSOP8-P-0.65: 0.02 g (typ.) SSOP8-P-0.50A: 0.01 g (typ.)

Pin Assignment (top view)

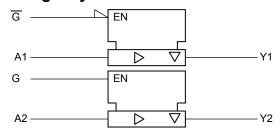


Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: V_{OUT} < GND, V_{OUT} > V_{CC}

IEC Logic Symbol



Truth Table

	INPUTS	OUTPUTS				
G	G	Α	Y			
L	Н	L				
L	Н	Н	Н			
Н	L	Х	Z			

X: Don't Care
Z: High Impedance

Operating Ranges

Characteristics	Symbol Rating		Unit
Supply voltage	V _{CC}	2.0 to 5.5	V
Input voltage	V _{IN}	0 to 5.5	V
Output voltage V _{OUT}		0 to V _{CC}	V
Operating temperature	T _{opr}	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 100 (V _{CC} = 3.3 ± 0.3 V)	ns/V
	ui/uv	0 to 20 (V _{CC} = 5.0 ± 0.5 V)	115/V



Electrical Characteristics

DC Characteristics

Characteristics Symbol					Ta = 25°C			Ta = -40		
		Test	Test Condition		Min	Тур.	Max	Min	Max	Unit
				2.0	1.5	_	_	1.5	_	
High-level input voltage V _{IH}			_	3.0 to 5.5	V _{CC} × 0.7	_	_	V _{CC} × 0.7	_	V
		_		2.0			0.5	_	0.5	٧
Low-level input voltage	V _{IL}			3.0 to 5.5			V _{CC} × 0.3	_	V _{CC} × 0.3	
			I _{OH} = -50 μA	2.0	1.9	2.0	_	1.9	_	V
	Vон	V _{IN} = V _{IH} or V _{IL}		3.0	2.9	3.0	_	2.9	_	
High-level output voltage				4.5	4.4	4.5	_	4.4	_	
			$I_{OH} = -4 \text{ mA}$	3.0	2.58		_	2.48	_	
			$I_{OH} = -8 \text{ mA}$	4.5	3.94		_	3.80		
	V _{OL}	V _{IN} = V _{IH} or V _{IL}	I _{OL} = 50 μA	2.0		0.0	0.1	_	0.1	
				3.0		0.0	0.1	_	0.1	
Low-level output voltage				4.5		0.0	0.1	_	0.1	V
			I _{OL} = 4 mA	3.0			0.36		0.44	
			$I_{OL} = 8 \text{ mA}$	4.5			0.36	_	0.44	
3-State Output Off-State Current	l _{OZ}	V _{IN} = V _{IH} or V _{IL} V _{OUT} = V _{CC} or GND		5.5		l	0.25	_	2.50	μА
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5	_	_	±0.1	_	±1.0	μА
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND		5.5	_	_	2.0	_	20.0	μА

AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition	Toot Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
Orial acteristics Syl	Syllibol	Symbol Test Condition		C _L (pF)	Min	Тур.	Max	Min	Max	Unit
Propagation Delay Time	t _{pLH}		3.3 ± 0.3	15	_	5.3	7.5	1.0	9.0	ns
				50	_	7.8	11.0	1.0	12.5	
Tropagation Delay Time	t _{pHL}		5.0 ± 0.5	15	_	3.6	5.5	1.0	6.5	113
			3.0 ± 0.5	50	_	5.1	7.5	1.0	8.5	
		$R_L = 1k\Omega$	3.3 ± 0.3	15	_	6.6	10.6	1.0	12.5	
3-State Output Enable Time	t _{pZL} I			50	_	9.1	14.1	1.0	16.0	ns
			5.0 ± 0.5	15	_	4.7	7.3	1.0	8.5	113
				50	_	6.2	9.3	1.0	10.5	
3-State Output	t _{pLZ}	$R_L = 1k\Omega$	3.3 ± 0.3	50	_	10.3	14.0	1.0	16.0	ns
Disable Time	t _{pHZ}		5.0 ± 0.5	50	_	6.7	9.2	1.0	10.5	113
Output to Output	t _{osLH}	(Note 2)	3.3 ± 0.3	50	_	_	1.5	_	1.5	ns
Skew	t _{osHL}	(NOIC Z)	5.0 ± 0.5	50	_	_	1.0	_	1.0	113
Input Capacitance	C _{IN}				_	4	10	_	10	pF
Output Capacitance	C _{I/O}				_	6	_	_	_	pF
Power Dissipation Capacitance	C _{PD}	(Note 3)			_	17	_	_	_	pF

Note 2: Parameter guaranteed by design. $t_{OSLH} = |t_{DLHm} - t_{DLHn}|, \ t_{OSHL} = |t_{DHLm} - t_{DHLn}|$

Note 3: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

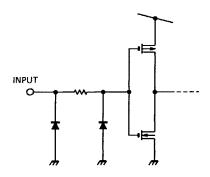
Average operating current can be obtained by the equation :

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$

Noise Characteristics (Ta = 25°C, input: $t_r = t_f = 3$ ns)

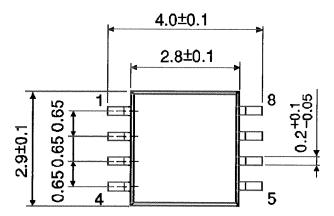
Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Limit	Unit
Quiet output maximum dynamic V_{OL}	V _{OLP}	C _L = 50 pF	5.0	0.5	0.8	V
Quiet output minimum dynamic V _{OL}	V _{OLV}	C _L = 50 pF	5.0	-0.5	-0.8	٧
Minimum high level dynamic input voltage	V _{IHD}	C _L = 50 pF	5.0	_	3.5	٧
Maximum low level dynamic input voltage	V _{ILD}	C _L = 50 pF	5.0		1.5	٧

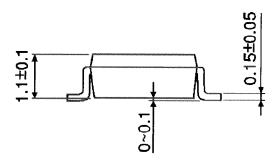
Input Equivalent Circuit



Package Dimensions

SSOP8-P-0.65 Unit: mm



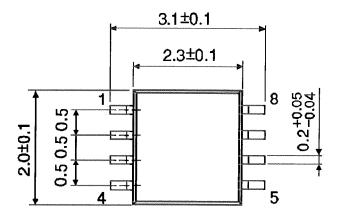


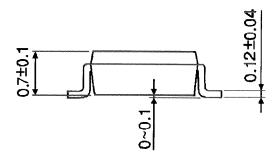
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Weight: 0.02 g (typ.)

Package Dimensions

SSOP8-P-0.50A Unit: mm





6

Weight: 0.01 g (typ.)

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