Si/SiC Hybrid Module – EliteSiC, I-Type NPC 1000 V, 350 A IGBT, 1200 V, 100 A SiC Diode, Q2 Package

NXH350N100H4Q2F2P1G, NXH350N100H4Q2F2S1G, NXH350N100H4Q2F2S1G-R, NXH350N100H4Q2F2P1G-R

This high-density, integrated power module combines high-performance IGBTs with rugged anti-parallel diodes.

Features

- Extremely Efficient Trench with Field Stop Technology
- Low Switching Loss Reduces System Power Dissipation
- Module Design Offers High Power Density
- Low Inductive Layout
- Low Package Height
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

Typical Applications

- Solar Inverters
- Uninterruptable Power Supplies Systems

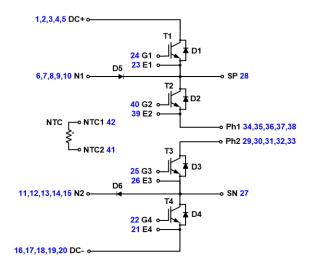
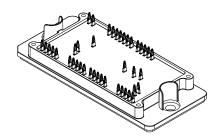


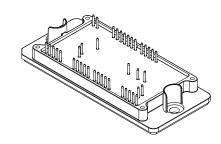
Figure 1. NXH350N100H4Q2F2P1G/S1G/S1G-R/P1G-R Schematic Diagram

1

PACKAGE PICTURE



Q2PACK INPC PRESS FIT PINS CASE 180BH



Q2PACK INPC SOLDER PINS CASE 180BS

MARKING DIAGRAM



G = Pb-Free Package AT = Assembly & Test Site Code

YYWW = Year and Work Week Code

PIN CONNECTIONS

See details pin connections on page 2 of this data sheet.

ORDERING INFORMATION

See detailed ordering and shipping information on page 5 of this data sheet.

PIN CONNECTIONS

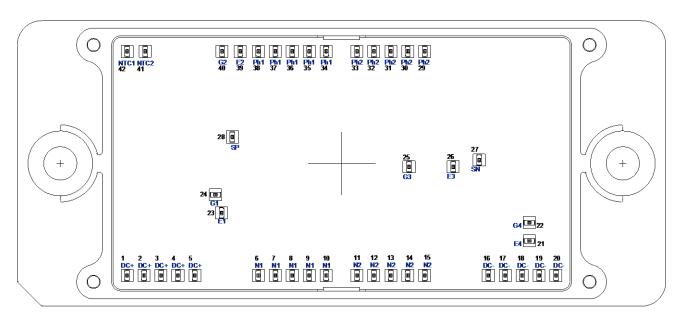


Figure 2. Pin Connections

ABSOLUTE MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
OUTER IGBT (T1, T4)			•
Collector-Emitter Voltage	V _{CES}	1000	V
Gate-Emitter Voltage Positive Transient Gate-Emitter Voltage (T_{pulse} = 5 μ s, D < 0.10)	V _{GE}	±20 30	V
Continuous Collector Current @ T _C = 80°C	Ic	303	А
Pulsed Peak Collector Current @ T _C = 80°C (T _J = 150°C)	I _{C(Pulse)}	909	А
Maximum Power Dissipation (T _J = 150°C)	P _{tot}	592	W
Minimum Operating Junction Temperature	T _{JMIN}	-40	°C
Maximum Operating Junction Temperature	T _{JMAX}	175	°C
INNER IGBT (T2, T3)	<u> </u>		
Collector-Emitter Voltage	V _{CES}	1000	V
Gate-Emitter Voltage Positive Transient Gate-Emitter Voltage (T_{pulse} = 5 μ s, D < 0.10)	V _{GE}	±20 30	V
Continuous Collector Current @ T _C = 80°C	Ic	298	А
Pulsed Peak Collector Current @ T _C = 80°C (T _J = 150°C)	I _{C(Pulse)}	894	А
Maximum Power Dissipation (T _J = 175°C)	P _{tot}	731	W
Minimum Operating Junction Temperature	T _{JMIN}	-40	°C
Maximum Operating Junction Temperature	T _{JMAX}	175	°C
IGBT INVERSE DIODE (D1, D2, D3, D4)			
Peak Repetitive Reverse Voltage	V_{RRM}	1000	V
Continuous Forward Current @ T _C = 80°C	I _F	133	А
Repetitive Peak Forward Current (T _J = 175°C)	I _{FRM}	399	А
Maximum Power Dissipation (T _J = 175°C)	P _{tot}	276	W

ABSOLUTE MAXIMUM RATINGS (T_J = 25°C unless otherwise noted) (continued)

Rating	Symbol	Value	Unit
IGBT INVERSE DIODE (D1, D2, D3, D4)			•
Minimum Operating Junction Temperature	T_JMIN	-40	°C
Maximum Operating Junction Temperature	T _{JMAX}	175	°C
NEUTRAL POINT DIODE (D5, D6)			
Peak Repetitive Reverse Voltage	V_{RRM}	1200	V
Continuous Forward Current @ T _C = 80°C	I _F	98	А
Repetitive Peak Forward Current (T _J = 175°C)	I _{FRM}	294	А
Maximum Power Dissipation (T _J = 175°C)	P _{tot}	239	W
Minimum Operating Junction Temperature	T _{JMIN}	-40	°C
Maximum Operating Junction Temperature	T _{JMAX}	175	°C
THERMAL PROPERTIES	<u>. </u>		
Operating Temperature under Switching Condition	T _{VJOP}	-40 to +150	°C
Storage Temperature Range	T _{stg}	-40 to +125	°C
INSULATION PROPERTIES			•
Isolation Test Voltage, t = 1 s, 50 Hz (Note 2)	V _{is}	4000	V _{RMS}
Creepage Distance		12.7	mm
Comparative Tracking Index	CTI	> 600	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- 1. Refer to ELECTRICAL CHARACTERISTICS and/or APPLICATION INFORMATION for Safe Operating parameters.
- 2. 4000 VAC $_{\mbox{\scriptsize RMS}}$ for 1 second duration is equivalent to 3333 VAC $_{\mbox{\scriptsize RMS}}$ for 1 minute duration.

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise specified)

Characteristic	Test Conditions	Symbol	Min	Тур	Max	Unit
OUTER IGBT (T1, T4) CHARACTER	RISTICS	•		•		
Collector-Emitter Cutoff Current	V _{GE} = 0 V, V _{CE} = 1000 V	I _{CES}	-	-	1000	μΑ
Collector-Emitter Saturation Voltage	V _{GE} = 15 V, I _C = 375 A, T _J = 25°C	V _{CE(sat)}	=	1.63	2.3	V
	V _{GE} = 15 V, I _C = 375 A, T _J = 150°C	1	=	1.92	-	
Gate-Emitter Threshold Voltage	$V_{GE} = V_{CE}$, $I_C = 375 \text{ mA}$	V _{GE(TH)}	3.8	4.84	6.1	V
Gate Leakage Current	V _{GE} = ±20 V, V _{CE} = 0 V	I _{GES}	-	-	±2000	nA
Turn-on Delay Time	T _J = 25°C	t _{d(on)}	-	85	-	ns
Rise Time	$V_{CE} = 600 \text{ V, } I_{C} = 150 \text{ A}$ $V_{GE} = -9 \text{ V, } 15 \text{ V, } R_{G} = 6 \Omega$	t _r	=	27	-	
Turn-off Delay Time		t _{d(off)}	-	319	_	
Fall Time	1	t _f	-	52	_	
Turn-on Switching Loss per Pulse	1	E _{on}	=	2.5	=	mJ
Turn-off Switching Loss per Pulse	1	E _{off}	-	4.9	_	
Turn-on Delay Time	T _J = 125°C	t _{d(on)}	-	80	_	ns
Rise Time	$V_{CE} = 600 \text{ V}, I_{C} = 150 \text{ A}$ $V_{GF} = -9 \text{ V}, 15 \text{ V}, R_{G} = 6 \Omega$	t _r	-	31	-	
Turn-off Delay Time		t _{d(off)}	-	355	-	
Fall Time	1	t _f	-	70	_	
Turn-on Switching Loss per Pulse	1	E _{on}	=	3.1	-	mJ
Turn-off Switching Loss per Pulse	1	E _{off}	-	7.3	-	

ELECTRICAL CHARACTERISTICS (T_{.I} = 25°C unless otherwise specified) (continued)

Characteristic	Test Conditions	Symbol	Min	Тур	Max	Unit
OUTER IGBT (T1, T4) CHARACTE	RISTICS	<u>. </u>				1
Input Capacitance	V _{CE} = 20 V, V _{GE} = 0 V, f = 1 MHz	C _{ies}	_	24146		pF
Output Capacitance	1	C _{oes}	_	1027	-	
Reverse Transfer Capacitance	1	C _{res}	_	106	-	
Total Gate Charge	$V_{CE} = 600 \text{ V, } I_{C} = 375 \text{ A,}$ $V_{GE} = -15 \text{ V} \sim 15 \text{ V}$	Q_g	_	1249	-	nC
Thermal Resistance – Chip-to-Heatsink	Thermal grease, Thickness = 2.1 Mil ±2%	R _{thJH}	-	0.22	-	K/W
Thermal Resistance - Chip-to-Case	$\lambda = 2.9 \text{ W/mK}$	R _{thJC}	-	0.12	_	K/W
NEUTRAL POINT DIODE (D5, D6)	CHARACTERISTICS					
Diode Forward Voltage	I _F = 100 A, T _J = 25°C	V _F	_	1.50	1.85	V
	I _F = 100 A, T _J = 150°C		-	2.07	-	
Reverse Recovery Time	T _J = 25°C	t _{rr}	-	19	_	ns
Reverse Recovery Charge	V_{CE}^{c} = 600 V, I_{C} = 150 A V_{GE} = -8 V, 15 V, R_{G} = 6 Ω	Q _{rr}	_	229	_	nC
Peak Reverse Recovery Current		I _{RRM}	_	19	-	Α
Reverse Recovery Energy		E _{rr}	_	164	_	μJ
Reverse Recovery Time	$T_J = 125^{\circ}C$ $V_{CE} = 600 \text{ V, I}_C = 150 \text{ A}$	t _{rr}	_	34	=	ns
Reverse Recovery Charge	$V_{GE} = 600 \text{ V}, I_{C} = 150 \text{ A}$ $V_{GE} = -8 \text{ V}, 15 \text{ V}, R_{G} = 6 \Omega$	Q _{rr}	=	359	=	nC
Peak Reverse Recovery Current		I _{RRM}	=	17	=	Α
Reverse Recovery Energy		E _{rr}	_	211	-	μJ
Thermal Resistance – Chip-to-Heatsink	Thermal grease, Thickness = 2.1 Mil $\pm 2\%$ λ = 2.9 W/mK	R _{thJH}	_	0.42	-	K/W
Thermal Resistance - Chip-to-Case	7 – 2.9 W/IIII	R_{thJC}	=	0.29	=	K/W
INNER IGBT (T2, T3) CHARACTER	ISTICS					
Collector-Emitter Cutoff Current	V _{GE} = 0 V, V _{CE} = 1000 V	I _{CES}	_	-	500	μΑ
Collector-Emitter Saturation Voltage	V _{GE} = 15 V, I _C = 400 A, T _J = 25°C	` '		1.75	2.3	V
	$V_{GE} = 15 \text{ V}, I_{C} = 400 \text{ A}, T_{J} = 150^{\circ}\text{C}$			2.11		
Gate-Emitter Threshold Voltage	$V_{GE} = V_{CE}$, $I_C = 400 \text{ mA}$	$V_{GE(TH)}$	4.1	5	6.1	V
Gate Leakage Current	$V_{GE} = \pm 20 \text{ V}, V_{CE} = 0 \text{ V}$	I _{GES}	_	-	±2000	nA
Turn-on Delay Time	$T_J = 25^{\circ}C$ $V_{CE} = 600 \text{ V, I}_C = 150 \text{ A}$	t _{d(on)}	_	70	_	ns
Rise Time	$V_{GE} = -9 \text{ V}, 15 \text{ V}, R_{G} = 11 \Omega$	t _r	_	31	_	
Turn-off Delay Time	1	t _{d(off)}	-	423	_	
Fall Time	1	t _f	_	74	-	
Turn-on Switching Loss per Pulse		E _{on}	=	6.4	=	mJ
Turn-off Switching Loss per Pulse	1	E _{off}	_	4.2	-	
Turn-on Delay Time	T _J = 125°C	t _{d(on)}	_	66	_	ns
Rise Time	V_{CE}^{o} = 600 V, I_{C} = 150 A V_{GE} = -9 V, 15 V, R_{G} = 11 Ω	t _r	_	31	_	
Turn-off Delay Time		t _{d(off)}	=	509	_	
Fall Time	-				_	
	4	t _f		88		pm 1
Turn-on Switching Loss per Pulse	4	E _{on}		9.7	-	mJ
Turn-off Switching Loss per Pulse		E _{off}		8.2	-	
Input Capacitance	V _{CE} = 20 V, V _{GE} = 0 V, f = 1 MHz	C _{ies}		26093	-	pF
Output Capacitance	4	C _{oes}	_	1012	-	
Reverse Transfer Capacitance		C_{res}	=	104	=	

$\begin{array}{c} {\sf NXH350N100H4Q2F2P1G,\ NXH350N100H4Q2F2S1G,\ NXH350N100H4Q2F2S1G-R,\ NXH350N100H4Q2F2P1G-R} \end{array}$

ELECTRICAL CHARACTERISTICS (T_{.1} = 25°C unless otherwise specified) (continued)

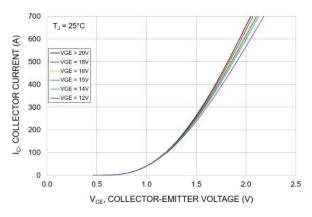
Characteristic	Test Conditions	Symbol	Min	Тур	Max	Unit
INNER IGBT (T2, T3) CHARACTE	RISTICS					•
Internal Gage Resistor		R_{gint}	_	1.25	-	Ω
Total Gate Charge	V _{CE} = 600 V, I _C = 400 A, V _{GE} = -15 V~15 V	Qg	=	1304	=	nC
Thermal Resistance – Chip-to-Heatsink	Thermal grease, Thickness = 2.1 Mil ±2%	R _{thJH}	=	0.24	=	K/W
Thermal Resistance - Chip-to-Case	$\lambda = 2.9 \text{ W/mK}$	R _{thJC}	-	0.13	-	K/W
IGBT INVERSE DIODE (D1, D2, D	3, D4) CHARACTERISTICS					
Diode Forward Voltage	I _F = 150 A, T _J = 25°C	V _F	_	2.06	2.6	V
	I _F = 150 A, T _J = 150°C		-	1.77	-	1
Reverse Recovery Time	T _J = 25°C	t _{rr}	_	105	-	ns
Reverse Recovery Charge	$V_{CE} = 600 \text{ V}, I_{C} = 150 \text{ A}$ $V_{GE} = -8 \text{ V}, 15 \text{ V}, R_{G} = 6 \Omega$	Q _{rr}	-	4179	-	nC
Peak Reverse Recovery Current		I _{RRM}	-	97	_	Α
Reverse Recovery Energy	7	E _{rr}	-	4665	-	μJ
Reverse Recovery Time	T _J = 125°C	t _{rr}	-	179	-	ns
Reverse Recovery Charge	$V_{CE} = 600 \text{ V}, I_{C} = 150 \text{ A}$ $V_{GF} = -8 \text{ V}, 15 \text{ V}, R_{G} = 6 \Omega$	Q _{rr}	=	11900	=	nC
Peak Reverse Recovery Current		I _{RRM}	=	133	=	Α
Reverse Recovery Energy		E _{rr}	=	3783	=	μJ
Thermal Resistance – Chip-to-Heatsink	Thermal grease, Thickness = 2.1 Mil ±2%	R_{thJH}	-	0.39	-	K/W
Thermal Resistance - Chip-to-Case	$\lambda = 2.9 \text{ W/mK}$	R _{thJC}	=	0.25		K/W
THERMISTOR CHARACTERISTIC	es					
Nominal Resistance	T = 25°C	R ₂₅	-	22	-	kΩ
Nominal Resistance	T = 100°C	R ₁₀₀		1486	-	kΩ
Deviation of R25		ΔR/R	-5	-	5	%
Power Dissipation		P _D		200	-	mW
Power Dissipation Constant			-	2	-	mW/K
B-value	B(25/50), tolerance ±3%		_	3950	-	K
B-value	B(25/100), tolerance ±3%	İ	_	3998	_	K

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

ORDERING INFORMATION

Part Number	Marking	Package	Shipping
NXH350N100H4Q2F2P1G, NXH350N100H4Q2F2P1G-R PRESS FIT PINS	NXH350N100H4Q2F2P1G, NXH350N100H4Q2F2P1G-R	Q2PACK (Pb-Free/Halide-Free)	12 Units / Blister Tray
NXH350N100H4Q2F2S1G, NXH350N100H4Q2F2S1G-R SOLDER PINS	NXH350N100H4Q2F2S1G, NXH350N100H4Q2F2S1G-R	Q2PACK (Pb-Free/Halide-Free)	12 Units / Blister Tray

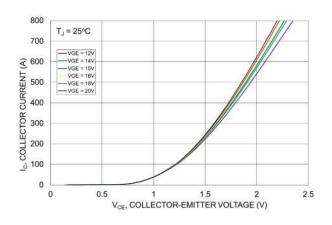
TYPICAL CHARACTERISTICS - OUTER IGBT, INNER IGBT



700 T₁ = 150°C € 600 -VGE = 20V VGE = 16V -VGE = 14V _[⊙] 100 0 0.0 2.0 0.5 1.0 1.5 2.5 3.0 V_{CE}, COLLECTOR-EMITTER VOLTAGE (V)

Figure 3. Typical Output Characteristics - Outer IGBT

Figure 4. Typical Output Characteristics – Outer IGBT



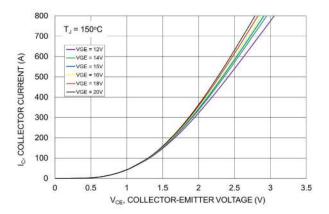
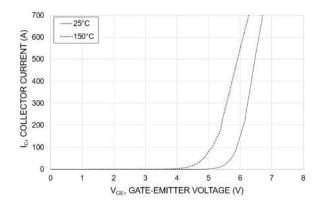


Figure 5. Typical Output Characteristics - Inner IGBT

Figure 6. Typical Output Characteristics – Inner IGBT





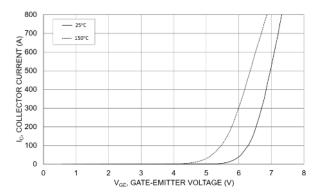


Figure 8. Transfer Characteristics - Inner IGBT

TYPICAL CHARACTERISTICS – OUTER IGBT, INNER IGBT, IGBT INVERSE DIODE AND NEUTRAL POINT DIODE

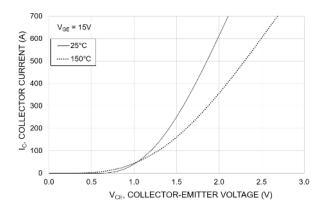


Figure 9. Typical Saturation Voltage Characteristics – Outer IGBT

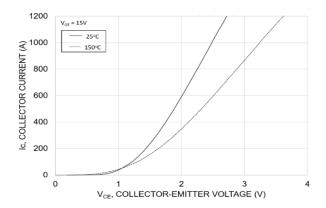


Figure 10. Typical Saturation Voltage Characteristics – Inner IGBT

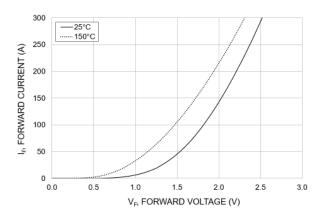


Figure 11. Inverse Diode Forward Characteristics

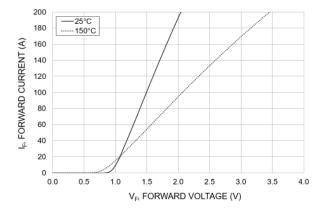


Figure 12. Buck Diode Forward Characteristics

TYPICAL SWITCHING CHARACTERISTICS - OUTER IGBT

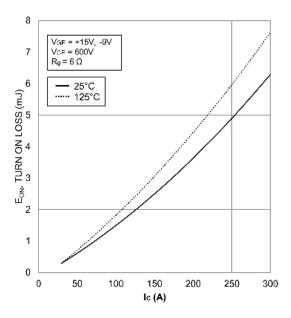


Figure 13. Typical Turn On Loss vs. I_C

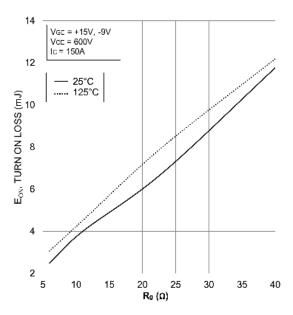


Figure 15. Typical Turn On Loss vs. R_G

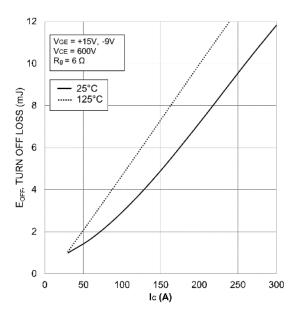


Figure 14. Typical Turn Off Loss vs. I_C

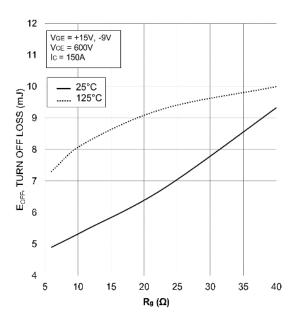


Figure 16. Typical Turn Off Loss vs. R_G

TYPICAL SWITCHING CHARACTERISTICS - OUTER IGBT (CONTINUED)

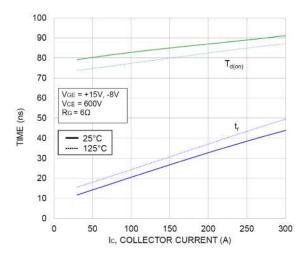


Figure 17. Typical Turn On Switching Time vs. I_C

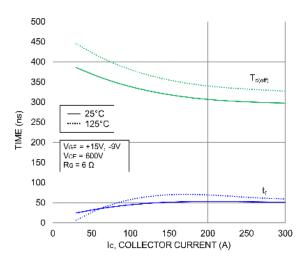


Figure 18. Typical Turn Off Switching Time vs. I_C

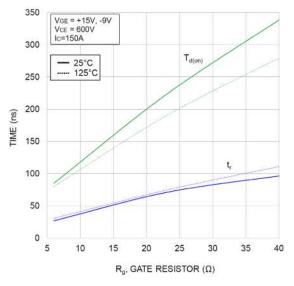


Figure 19. Typical Turn On Switching Time vs. R_G

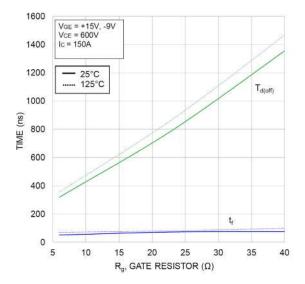


Figure 20. Typical Turn Off Switching Time vs. R_G

TYPICAL SWITCHING CHARACTERISTICS - INNER IGBT

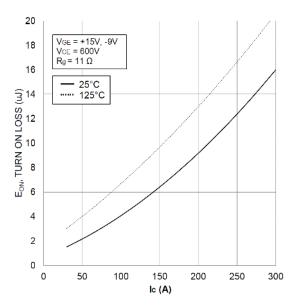


Figure 21. Typical Turn On Loss vs. I_C

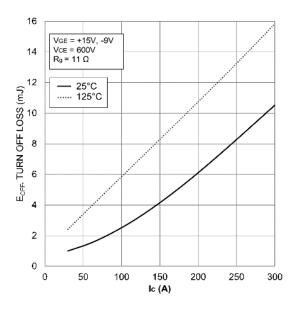


Figure 22. Typical Turn Off Loss vs. I_C

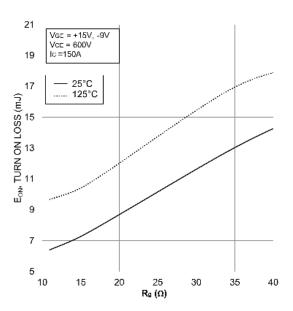


Figure 23. Typical Turn On Loss vs. R_G

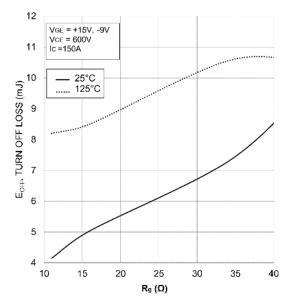


Figure 24. Typical Turn Off Loss vs. R_G

800

700

600

500

400

300

TYPICAL SWITCHING CHARACTERISTICS - INNER IGBT (CONTINUED)

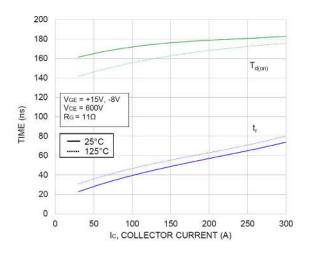
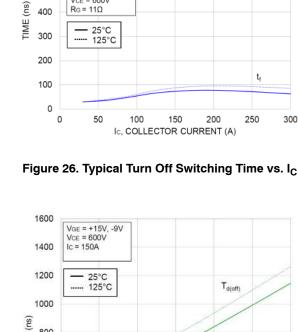


Figure 25. Typical Turn On Switching Time vs. I_C



V_{GE} = +15V, -9V V_{CE} = 600V R_G = 11Ω

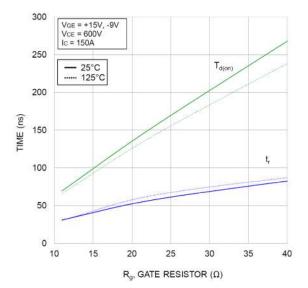
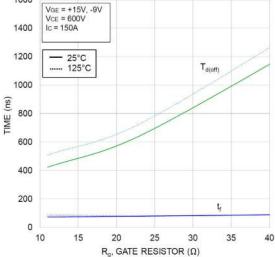


Figure 27. Typical Turn On Switching Time vs. R_G



 $T_{d(off)}$

200

250

300

Figure 28. Typical Turn Off Switching Time vs. R_G

5000

TYPICAL SWITCHING CHARACTERISTICS - INVERSE DIODE

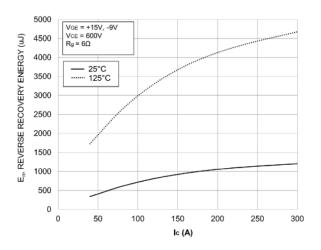
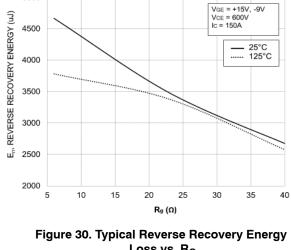


Figure 29. Typical Reverse Recovery Energy Loss vs. I_C



Loss vs. R_G

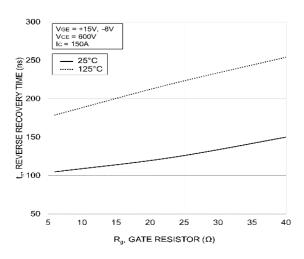


Figure 31. Typical Reverse Recovery Time vs. R_G

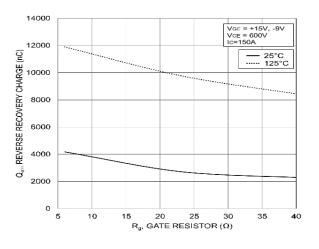


Figure 32. Typical Reverse Recovery Charge vs. R_G

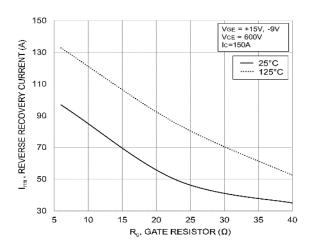


Figure 33. Typical Reverse Recovery Peak Current vs. R_G

TYPICAL SWITCHING CHARACTERISTICS - NEUTRAL POINT DIODE

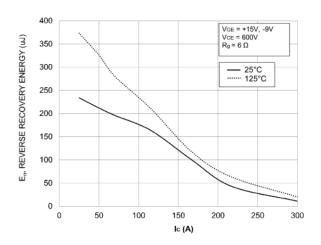


Figure 34. Typical Reverse Recovery Energy Loss vs. I_C

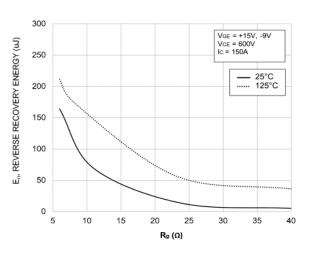


Figure 35. Typical Reverse Recovery Energy Loss vs. R_G

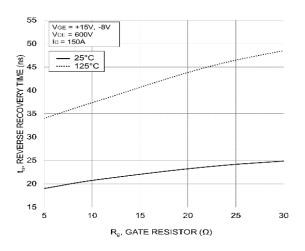


Figure 36. Typical Reverse Recovery Time vs. R_G

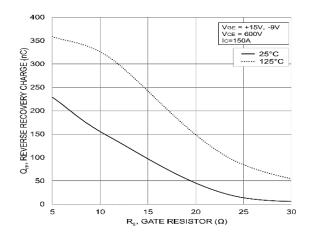


Figure 37. Typical Reverse Recovery Charge vs. R_G

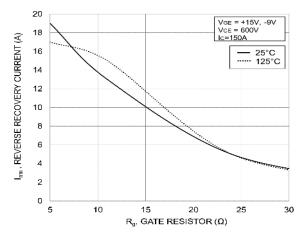


Figure 38. Typical Reverse Recovery Peak Current vs. R_G

$\begin{array}{c} {\sf NXH350N100H4Q2F2P1G,\ NXH350N100H4Q2F2S1G,\ NXH350N100H4Q2F2S1G-R,\ NXH350N100H4Q2F2P1G-R} \end{array}$

TRANSIENT THERMAL IMPEDANCE

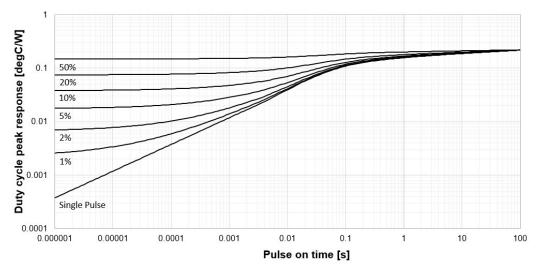


Figure 39. Transient Thermal Impedance - Outer IGBT

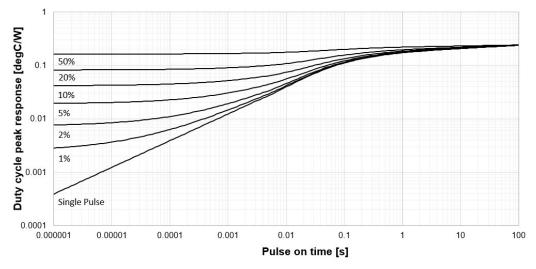


Figure 40. Transient Thermal Impedance - Inner IGBT

TRANSIENT THERMAL IMPEDANCE (CONTINUED)

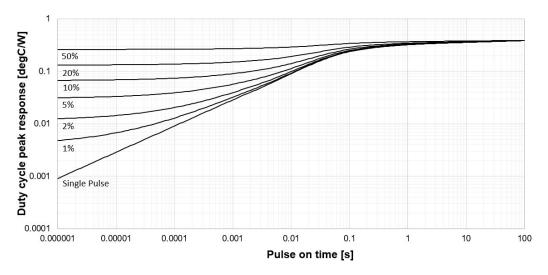


Figure 41. Transient Thermal Impedance – Inverse Diode

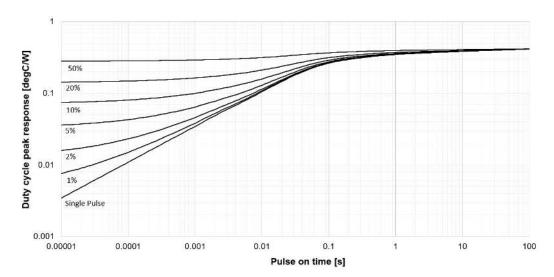


Figure 42. Transient Thermal Impedance – Neutral Point Diode

SAFE OPERATING AREA

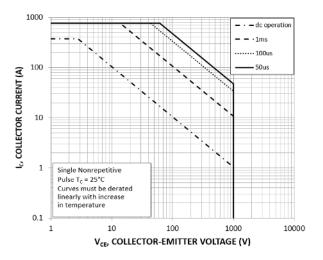


Figure 43. FBSOA – Outer IGBT

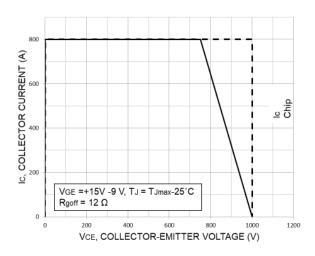


Figure 44. RBSOA – Outer IGBT

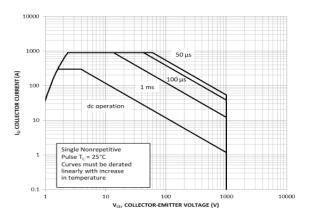


Figure 45. FBSOA - Inner IGBT

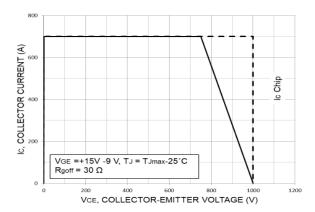
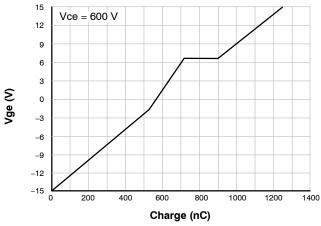


Figure 46. RBSOA - Inner IGBT

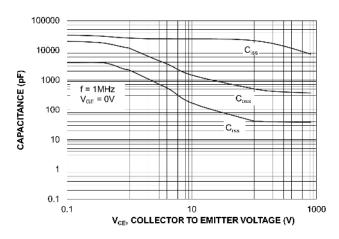
GATE CHARGE AND CAPACITANCE



15 Vce = 600 V 12 9 6 3 Vge (V) 0 -3 -6 -9 -12 400 1000 1200 1400 Charge (nC)

Figure 47. Gate Voltage vs. Gate Charge - Outer IGBT

Figure 48. Gate Voltage vs. Gate Charge - Inner IGBT



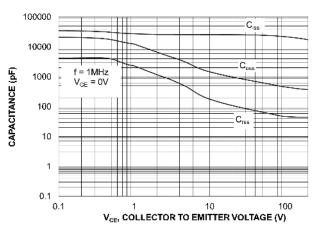


Figure 49. Capacitance Charge - Outer IGBT

Figure 50. Capacitance Charge - Inner IGBT

TYPICAL CHARCTERISTICS - THERMISTOR

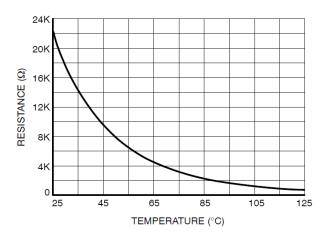
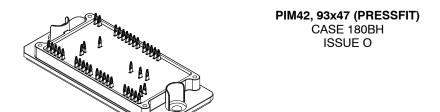


Figure 51. Thermistor Characteristics



DATE 06 AUG 2019

PACKAGE MARKING LOCATION D1 A1 A2X b A2X b A2X b1

NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009.
- 2. CONTROLLING DIMENSION: MILLIMETERS

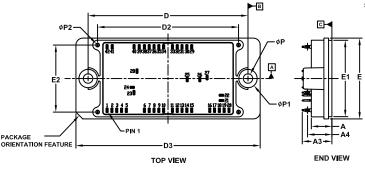
MILLIMETERS

11.70

NOM. MAX. 12.00 12.30

- 3. DIMENSIONS 6 AND 61 APPLY TO THE PLATED TERMINALS AND ARE MEASURED AT DIMENSION A4.
- 4. POSITION OF THE CENTER OF THE TERMINALS IS DETERMINED FROM DATUM B THE CENTER OF DIMENSION D, X DIRECTION, AND FROM DATUM A, Y DIRECTION. POSITIONAL TOLERANCE, AS NOTED IN DRAWING, APPLIES TO EACH TERMINAL IN BOTH DI
- 5. PACKAGE MARKING IS LOCATED AS SHOWN ON THE SIDE OPPOSITE THE PACKAGE ORIENTATION FEATURES.

NOTE 4



RECOMMENDED MOUNTING PATTERN

E2 - 1(1 ~'	7.70	4.70	0.00	1
	A2	16.40	16.70	17.00	ĺ
	A3	16.90	17.30	17.70	ı
228	A4	13.97	14.18	14.39	ĺ
	b	1.61	1.66	1.71	ĺ
PIN 1	b1	0.75	0.80	0.85	ĺ
+ A3+	D	92.90	93.00	93.10	ĺ
N FEATURE	D1	104.45	104.75	105.05	ĺ
TOP VIEW END VIEW	D2	81.80	82.00	82.20	ĺ
	D3	106.90	107.20	107.50	ı
93.00	E	46.70	47.00	47.30	ĺ
82.00	E1	44.10	44.40	44.70	ı
	E2	38.80	39.00	39.10	ĺ
	Р	5.40	5.50	5.60	ĺ
◆ · · · · · · · · · · · · · · · · · · ·	P1	10.60	10.70	10.80	ĺ
THRU HOLE	P2	1.80	2.00	2.20	ĺ
$\qquad \qquad $					

Ø1.45 PLATED THRU HOLE

ſ		PIN POSITION		Г		PIN POSITION	
ı	PIN	Х	Υ		PIN	х	Υ
ſ	1	0.00	0.00	ı	22	66.50	8.70
[2	2.80	0.00		23	15.60	10.30
[3	5.60	0.00		24	14.60	13.30
[4	8.40	0.00		25	46.60	17.90
[5	11.20	0.00		26	53.90	17.90
[6	21.70	0.00		27	58.20	19.00
[7	24.50	0.00		28	17.40	22.80
[8	27.30	0.00		29	49.20	36.90
Γ	9	30.10	0.00		30	46.40	36.90
[10	32.90	0.00		31	43.60	36.90
[11	38.00	0.00		32	40.80	36.90
Γ	12	40.80	0.00		33	38.00	36.90
[13	43.60	0.00		34	32.90	36.90
Γ	14	46.40	0.00		35	30.10	36.90
[15	49.20	0.00		36	27.30	36.90
Γ	16	59.70	0.00		37	24.50	36.90
[17	62.50	0.00		38	21.70	36.90
[18	65.30	0.00		39	18.70	36.90
[19	68.10	0.00		40	15.70	36.90
[20	70.90	0.00		41	3.00	36.90
ſ	21	66.50	5.70	ı	42	0.00	36.90

XXXXX = Specific Device Code

G = Pb-Free Package

AT = Assembly & Test Site Code

YYWW= Year and Work Week Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.

DOCUMENT NUMBER:	98AON09951H	Electronic versions are uncontrolled except when accessed directly from the Document Reposit Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.				
DESCRIPTION:	PIM42 93X47 (PRESS FIT)		PAGE 1 OF 1			

ON Semiconductor and are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.

PACKAGE MARKING

LOCATION

DATE 03 DEC 2019



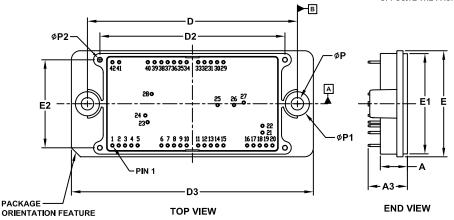
SIDE VIEW

42X b

⊕ 0.80**③** A B

NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009.
- 2. CONTROLLING DIMENSION: MILLIMETERS
- 3. DIMENSIONS b AND b1 APPLY TO THE PLATED TERMINALS AND ARE MEASURED AT DIMENSION A4.
- 4. POSITION OF THE CENTER OF THE TERMINALS IS DETERMINED FROM DATUM B THE CENTER OF DIMENSION D, X DIRECTION, AND FROM DATUM A, Y DIRECTION. POSITIONAL TOLERANCE, AS NOTED IN DRAWING, APPLIES TO EACH TERMINAL IN BOTH DIRECTIONS
- 5. PACKAGE MARKING IS LOCATED AS SHOWN ON THE SIDE OPPOSITE THE PACKAGE ORIENTATION FEATURES.



□ 0.30

	MI	MILLIMETERS					
DIM	MIN.	NOM.	MAX.				
Α	11.70	12.00	12.30				
A1	4.40	4.70	5.00				
A2	16.40	16.70	17.00				
A3	16.80	17.20	17.60				
b	0.95	1.00	1.05				
D	92.90	93.00	93.10				
D1	104.45	104.75	105.05				
D2	81.80	82.00	82.20				
D3	106.90	107.20	107.50				
E	46.70	47.00	47.30				
E1	44.10	44.40	44.70				
E2	38.80	39.00	39.10				
Р	5.40	5.50	5.60				
P1	10.60	10.70	10.80				
P2	1.80	2.00	2,20				

NOTE 4					
	PIN POSITION			PIN POS	SITION
PIN	х	Υ	PIN	х	Υ
1	0.00	0.00	22	66.50	8.70
2	2.80	0.00	23	15.60	10.30
3	5.60	0.00	24	14.60	13,30
4	8.40	0.00	25	46.60	17.90
5	11.20	0.00	26	53.90	17.90
6	21.70	0.00	27	58,20	19,00
7	24.50	0.00	28	17.40	22,80
8	27.30	0.00	29	49.20	36,90
9	30,10	0.00	30	46.40	36,90
10	32,90	0.00	31	43,60	36,90
11	38.00	0.00	32	40.80	36,90
12	40.80	0.00	33	38.00	36.90
13	43.60	0.00	34	32.90	36.90
14	46.40	0.00	35	30.10	36,90
15	49.20	0.00	36	27.30	36.90
16	59.70	0.00	37	24.50	36.90
17	62.50	0.00	38	21.70	36.90
18	65.30	0.00	39	18.70	36.90
19	68.10	0.00	40	15.70	36.90
20	70.90	0.00	41	3.00	36.90
21	66.50	5.70	42	0.00	36.90

XXXXX = Specific Device Code
G = Pb-Free Package
AT - Assembly & Test Site Co

AT = Assembly & Test Site Code YYWW= Year and Work Week Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

DOCUMENT NUMBER:	98AON15232H	Electronic versions are uncontrolled except when accessed directly from the Document Repos Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.			
DESCRIPTION:	PIM42 93X47 (SOLDER PIN	N)	PAGE 1 OF 1		

ON Semiconductor and (III) are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.

onsemi, Onsemi, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. Onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi does not convey any license under any of its intellectual property rights nor the rights of others. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA class 3 medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase

ADDITIONAL INFORMATION

TECHNICAL PUBLICATIONS:

 $\textbf{Technical Library:} \ \underline{www.onsemi.com/design/resources/technical-documentation}$

onsemi Website: www.onsemi.com

ONLINE SUPPORT: www.onsemi.com/support

For additional information, please contact your local Sales Representative at www.onsemi.com/support/sales