



# PJA3428

## 30V N-Channel Enhancement Mode MOSFET

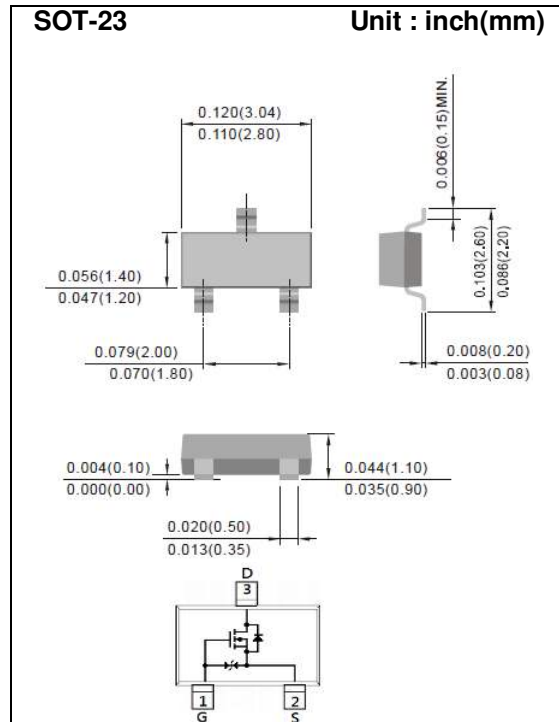
**Voltage** 30 V **Current** 300mA

### Features

- Advanced Trench Process Technology
- ESD Protected
- Specially Designed for Relay driver, Speed line drive, etc.
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std. (Halogen Free)

### Mechanical Data

- Case: SOT-23 Package
- Terminals : Solderable per MIL-STD-750, Method 2026



## Maximum Ratings and Thermal Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	$V_{DS}$	30	V
Gate-Source Voltage	$V_{GS}$	$\pm 10$	V
Continuous Drain Current	$I_D$	300	mA
Pulsed Drain Current	$I_{DM}$	600	mA
Power Dissipation	$T_A=25^\circ\text{C}$	500	mW
	Derate above $25^\circ\text{C}$	4	mW/ $^\circ\text{C}$
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55~150	$^\circ\text{C}$
Typical Thermal Resistance	$R_{\theta JA}$	250	$^\circ\text{C}/\text{W}$
- Junction to Ambient (Note 3)			



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## Electrical Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
<b>Static</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	30	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	0.4	0.75	1.0	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=4.5V, I_D=300mA$	-	0.7	1.2	$\Omega$
		$V_{GS}=2.5V, I_D=200mA$	-	0.8	1.6	
		$V_{GS}=1.8V, I_D=100mA$	-	0.9	2.0	
		$V_{GS}=1.5V, I_D=50mA$	-	1.1	3.0	
		$V_{GS}=1.2V, I_D=20mA$	-	1.5	4.0	
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=24V, V_{GS}=0V$	-	-	1	$\mu A$
Gate-Source Leakage Current	$I_{GSS}$	$V_{GS}=\pm 8V, V_{DS}=0V$	-	-	$\pm 10$	$\mu A$
<b>Dynamic</b> (Note 4)						
Total Gate Charge	$Q_g$	$V_{DS}=10V, I_D=300mA,$ $V_{GS}=4.5V$	-	0.9	-	nC
Gate-Source Charge	$Q_{gs}$		-	0.3	-	
Gate-Drain Charge	$Q_{gd}$		-	0.2	-	
Input Capacitance	$C_{iss}$	$V_{DS}=10V, V_{GS}=0V,$ $f=1.0MHz$	-	45	-	pF
Output Capacitance	$C_{oss}$		-	14	-	
Reverse Transfer Capacitance	$C_{rss}$		-	0.8	-	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=10V, I_D=300mA,$ $V_{GS}=4V,$ $R_G=10\Omega$ (Note 1,2)	-	8.3	-	ns
Turn-On Rise Time	$t_r$		-	5.7	-	
Turn-Off Delay Time	$t_{d(off)}$		-	35	-	
Turn-Off Fall Time	$t_f$		-	12	-	
<b>Drain-Source Diode</b>						
Maximum Continuous Drain-Source Diode Forward Current	$I_S$	---	-	-	300	mA
Diode Forward Voltage	$V_{SD}$	$I_S=300mA, V_{GS}=0V$	-	0.9	1.3	V

NOTES:

1. Pulse width  $\leq 300\mu s$ , Duty cycle  $\leq 2\%$
2. Essentially independent of operating temperature typical characteristics.
3.  $R_{\theta JA}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. mounted on a 1 inch square pad of copper
4. Guaranteed by design, not subject to production testing



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## TYPICAL CHARACTERISTIC CURVES

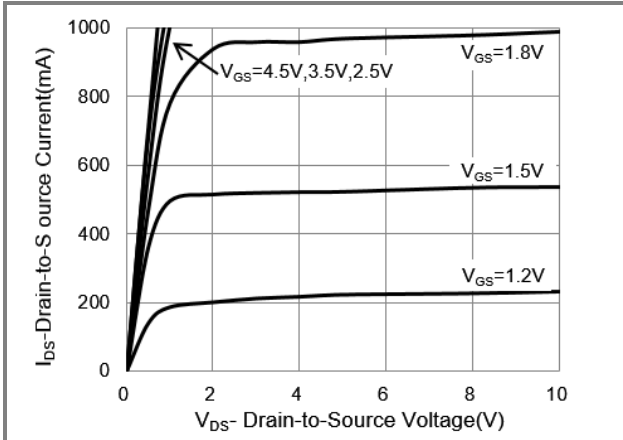


Fig.1 On-Region Characteristics

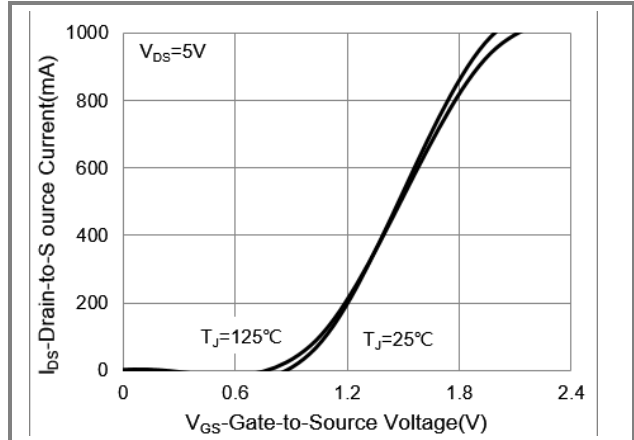


Fig.2 Transfer Characteristics

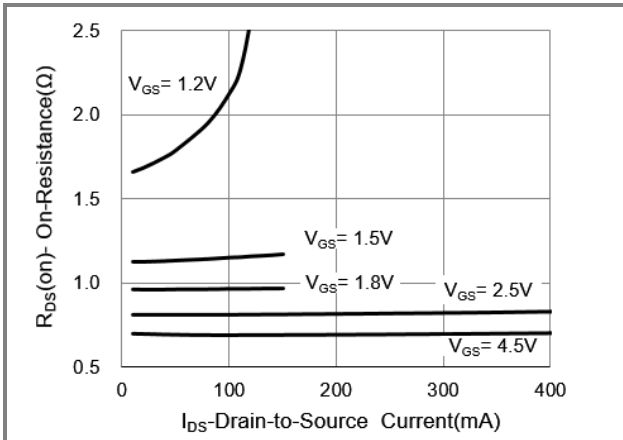


Fig.3 On-Resistance vs. Drain Current

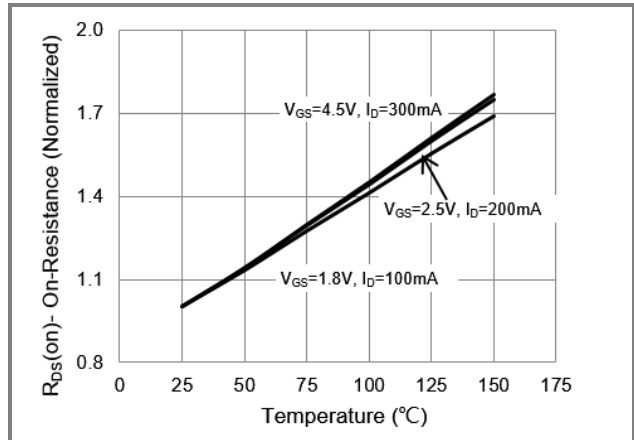


Fig.4 On-Resistance vs. Junction temperature

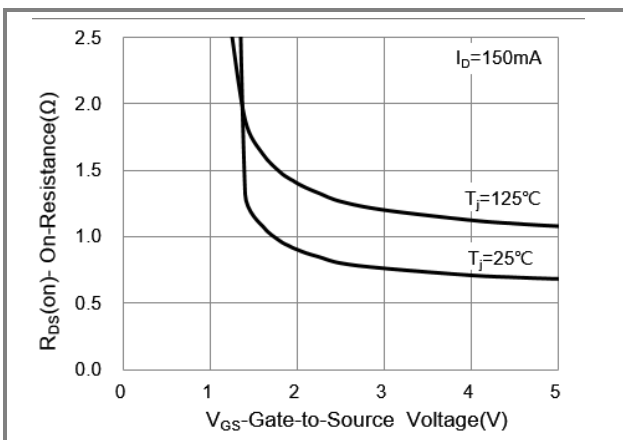


Fig.5 On-Resistance Variation with VGS.

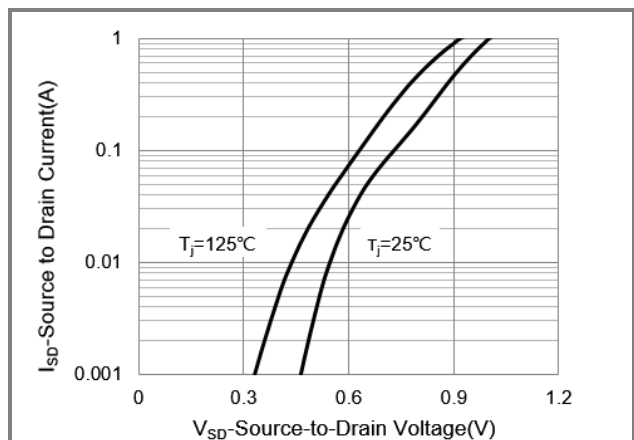


Fig.6 Body Diode Characteristics



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## TYPICAL CHARACTERISTIC CURVES

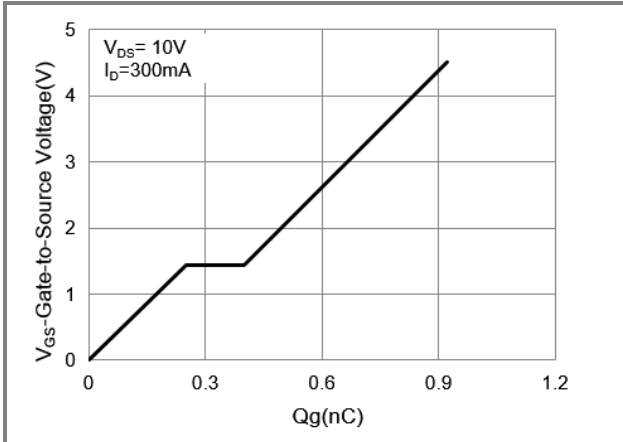


Fig.7 Gate-Charge Characteristics

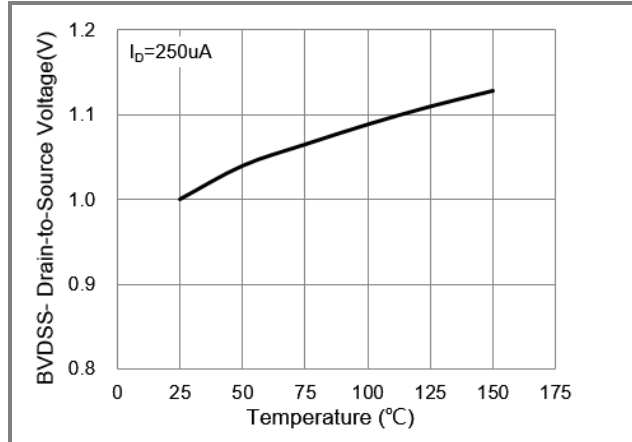


Fig.8 Breakdown Voltage Variation vs. Temperature

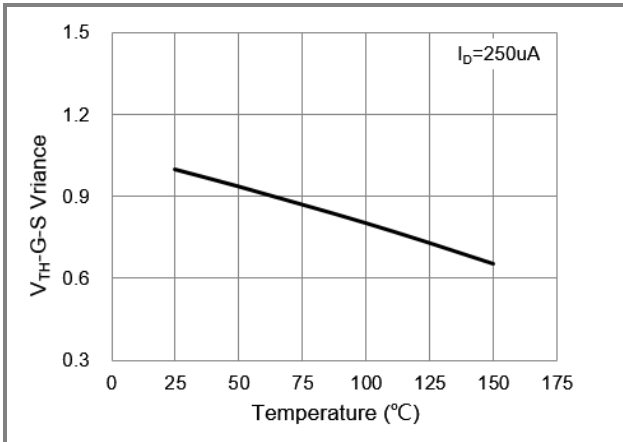


Fig.9 Threshold Voltage Variation with Temperature.

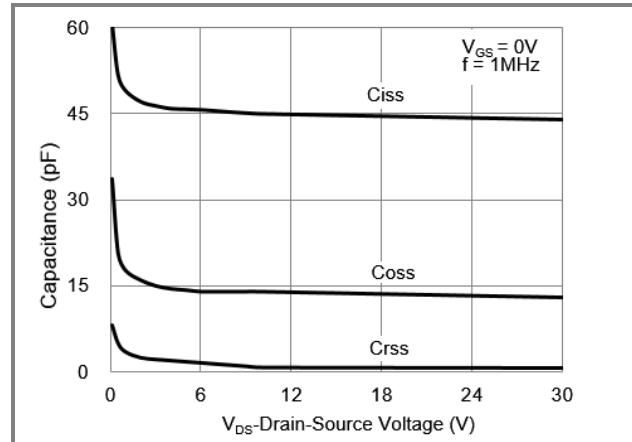


Fig.10 Capacitance vs. Drain-Source Voltage.

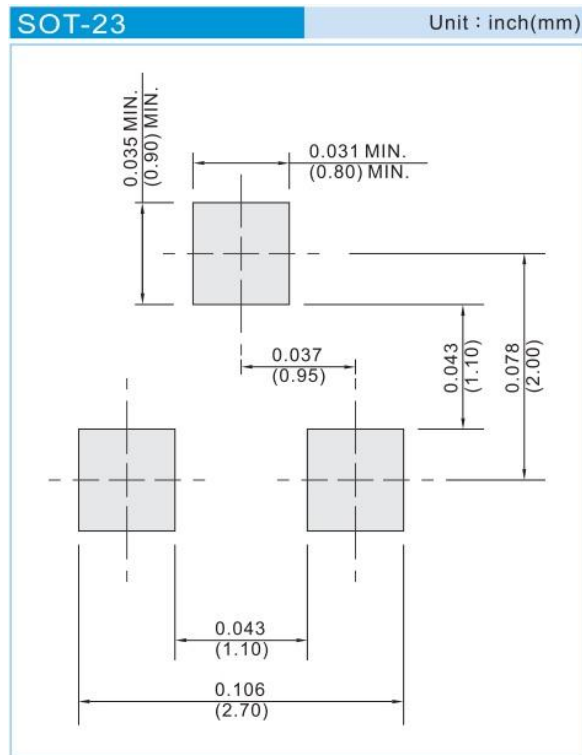


# PJA3428

## PART NO PACKING CODE VERSION

PART NO PACKING CODE	Package Type	Packing Type	Marking	Version
PJA3428_R1_00001	SOT-23	3K pcs / 7" reel	A28	Halogen free
PJA3428_R2_00001	SOT-23	12K pcs / 13" reel	A28	Halogen free

## MOUNTING PAD LAYOUT





## PJA3428

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