HLMP-LD15, HLMP-LM17, HLMP-LB17

Precision Optical Performance Red, Green and Blue 4mm Standard Oval LEDs



Data Sheet



Description

These Precision Optical Performance Oval LEDs are specifically designed for full color/video and passenger information signs. The oval shaped radiation pattern and high luminous intensity ensure that these devices are excellent for wide field of view outdoor applications where a wide viewing angle and readability in sunlight are essential. These lamps have very smooth, matched radiation patterns ensuring consistent color mixing in full color applications, message uniformity across the viewing angle of the sign. High efficiency LED material is used in these lamps: Aluminum Indium Gallium Phosphide (AlInGaP II) for red and Indium Gallium Nitride for blue and green. Each lamp is made with an advanced optical grade epoxy offering superior high temperature and high moisture resistance in outdoor applications.

The package epoxy contains both UV-A and UV-B inhibitors to reduce the effects of long term exposure to direct sunlight.

Features

- Well defined spatial radiation pattern
- High brightness material
- Available in red, green and blue color.
 - Red AlInGaP 630mm
 - Green InGaN 525nm
 - Blue InGaN 470nm
- Superior resistance to moisture
- Tinted and diffused

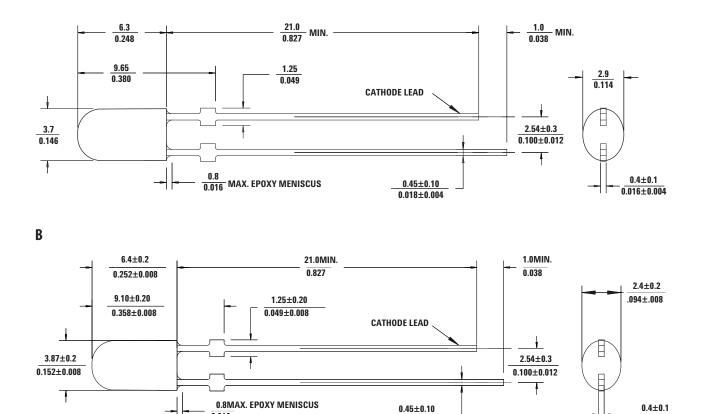
Benefits

- Viewing angle designed for wide filed of view applications
- Superior performance for outdoor environments

Applications

- Full color signs
- Commercial outdoor advertising.

Caution: InGaN devices are Class 1C HBM ESD sensitive per JEDEC standard. Please observe appropriate precautions during handling and processing. Refer to Application Note AN-1142 for additional details.



Note:

A

1. Dimension in millimeters (inches).

2. Tolerance is ±0.2mm unless otherwise noted.

3. For InGaN Blue and Green (package B), if heat-sinking application is required, the terminal for heat sink is anode.

Device Selection Guide

Color and Dominant		Luminous Intensity Iv (mcd) at 20 mA		_	
Part Number	Wavelength $\lambda \textbf{d}$ (nm) Typ.	Min	Max	Tinting Type	Package Drawing
HLMP-LD15-MQTxx	Red 630	520	1500	Red	A
HLMP-LD15-NRTxx	Red 630	680	1900	Red	A
HLMP-LM17-SV0xx	Green 525	1900	5500	Green	В
HLMP-LB17-LP0xx	Blue 470	400	1150	Blue	В

0.018±0.004

 0.016 ± 0.004

Notes:

1. The luminous intensity is measured on the mechanical axis of the lamp package

0.016

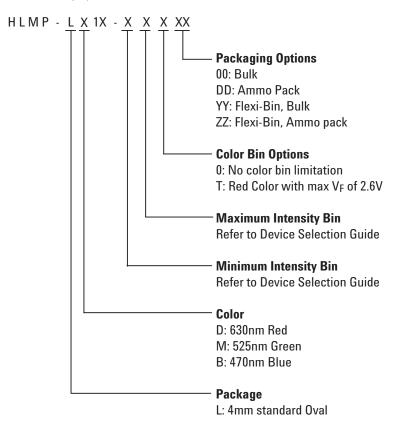
2. The tolerance for intensity limit is $\pm 15\%$

3. The optical axis is closely aligned with the package mechanical axis

4. The dominant wavelength, λ_d , is derived from the Chromaticity Diagram and represents the color of the lamp.

2

Part Numbering System



Note: Please refer to AB 5337 for complete information about part numbering system.

Absolute Maximum Rating $(T_A = 25^{\circ}C)$

Parameter	Red	Blue and Green	Unit
DC Forward Current ^[1]	50	30	mA
Peak Forward Current	100 ^[2]	100 ^[3]	mA
Power Dissipation	130	116	mW
Reverse Voltage	5 (I _R = 100 μA)	5 ($I_R = 10 \ \mu A$)	V
LED Junction Temperature	130	130	°C
Operating Temperature Range	-40 to +100	-40 to +85	°C
Storage Temperature Range	-40 to +100	-40 to +100	٥C

Notes:

1. Derate linearly as shown in Figure 4 and Figure 8.

2. Duty Factor 30%, frequency 1KHz.

3. Duty Factor 10%, frequency 1KHz.

Electrical/Optical Characteristics

Parameter	Symbol	Min.	Тур.	Max.	Units	Test Condition
Forward Voltage	V _F				V	$I_F = 20 \text{ mA}$
Red ^[1]		2.0	2.3	2.6		
Blue		2.8	3.2	3.85		
Green		2.8	3.3	3.85		
Reverse Voltage	V _R				V	
Red		5				I _R = 100 μA
Blue		5				$I_{\rm R} = 10 \mu {\rm A}$
Green		5				$I_R = 10 \mu A$
Peak Wavelength	λ_{peak}				nm	Peak of wavelength of spectral
Red	P		639			distribution at $I_F = 20 \text{ mA}$
Blue			464			
Green			516			
Dominant wavelength ^[2,3]	λ _d				nm	$I_{\rm F} = 20 \rm mA$
Red		622	630	634		
Green		520	525	540		
Blue		460	470	480		
Spectral Half width	$\Delta\lambda_{1/2}$				nm	Wavelength width at spectral distri
Red			17			bution $1/2$ power point at
Blue			23			$I_{\rm F} = 20 {\rm mA}$
Green			32			
Capacitance	С				pF	$V_{F} = 0, F = 1 MHz$
Red			40			
Blue			65			
Green			64			
Thermal Resistance ^[4]	Rθ _{J-PIN}		240		°C/W	LED Junction-to-pin
Luminous Efficacy ^[5]	ην				lm/W	Emitted luminous power/emitted
Red			155			radiant power
Blue			75			
Green			520			
Luminous Flux						
Red	φγ		1300		mlm	$I_F = 20 \text{ mA}$
Green			3000			
Blue			600			
Luminous Efficiency ^[6]						
Red	η_{e}		30		lm/W	Luminous Flux/Electrical Power
Green			50			$I_F = 20 \text{ mA}$
Blue			10			

Notes:

1. For option -xxTxx, V_{F} maximum is 2.6V. Refer to V_{F} bin table.

2. Tolerance for each color bin limit is \pm 0.5 nm

3. The dominant wavelength λ_d is derived from the Chromaticity Diagram and represents the color of the lamp.

4. For AllnGaP Red, thermal resistance applied to LED junction to cathode lead, and for InGaN Blue and Green, thermal resistance applied to LED junction to anode lead.

5. The radiant intensity, I_e in watts per steradian, may be found from the equation $I_e = I_v/\eta_v$ where I_v is the luminous intensity in candelas and η_v is the luminous efficacy in lumens/watt.

6. $\eta_e = \phi_V / I_F \times V_F$, where ϕ_V is the emitted luminous flux, I_F is electrical forward current and V_F is the forward voltage.

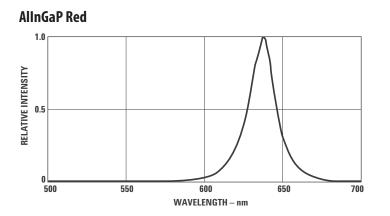


Figure 1. Relative intensity vs. wavelength

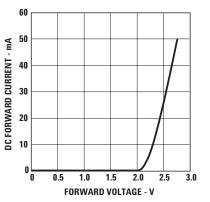
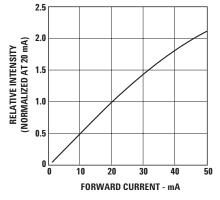


Figure 2. Forward current vs. forward voltage



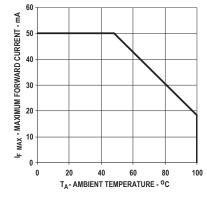


Figure 3. Relative luminous intensity vs. forward current

Figure 4. Forward current vs. ambient temperature

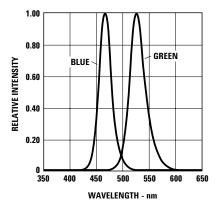


Figure 5. Relative intensity vs. wavelength

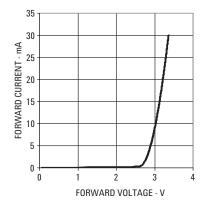


Figure 6. Forward current vs. forward voltage.

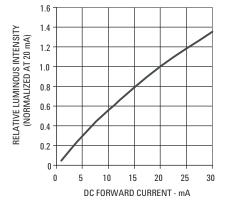
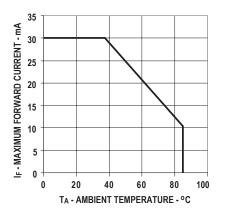


Figure 7. Relative luminous intensity vs. forward current.

InGaN Blue and Green



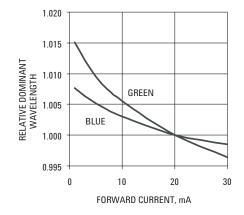


Figure 8. Forward current vs. ambient temperature.

Figure 9. Relative dominant wavelength vs. forward current

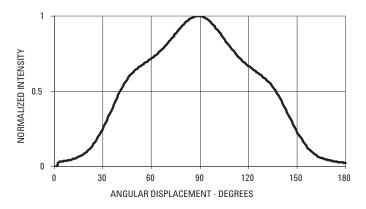


Figure 10a. Spatial radiation pattern – major axis for RGB

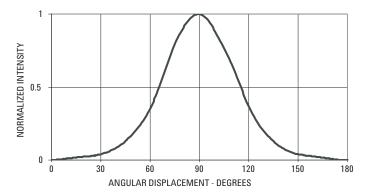


Figure 10b. Spatial radiation pattern – minor axis for RGB

Intensity Bin Limit Table

	Intensity (I	ncd) at 20 mA
Bin	Min	Мах
L	400	520
М	520	680
Ν	680	880
Р	880	1150
Q	1150	1500
R	1500	1900
S	1900	2500
Т	2500	3200
U	3200	4200
V	4200	5500

Bin	Min Dom	Max Dom	Xmin	Ymin	Xmax	Ymax
1	460.0	464.0	0.1440	0.0297	0.1766	0.0966
			0.1818	0.0904	0.1374	0.0374
2	464.0	468.0	0.1374	0.0374	0.1699	0.1062
			0.1766	0.0966	0.1291	0.0495
3	468.0	472.0	0.1291	0.0495	0.1616	0.1209
			0.1699	0.1062	0.1187	0.0671
4	472.0	476.0	0.1187	0.0671	0.1517	0.1423
			0.1616	0.1209	0.1063	0.0945
5	476.0	480.0	0.1063	0.0945	0.1397	0.1728
			0.1517	0.1423	0.0913	0.1327

Tolerance for each bin limit is $\pm 15\%$

Tolerance for each bin limit is ± 0.5 nm

Green Color Bin Table

VF bin Table (V at 20mA)^[2]

Bin ID	Min.	Max.
VA	2.0	2.2
VB	2.2	2.4
VC	2.4	2.6

Tolerance for each bin limit is ± 0.05 V.

Bin	Min Dom	Max Dom	Xmin	Ymin	Xmax	Ymax
1	520.0	524.0	0.0743	0.8338	0.1856	0.6556
			0.1650	0.6586	0.1060	0.8292
2	524.0	528.0	0.1060	0.8292	0.2068	0.6463
			0.1856	0.6556	0.1387	0.8148
3	528.0	532.0	0.1387	0.8148	0.2273	0.6344
			0.2068	0.6463	0.1702	0.7965
4	532.0	536.0	0.1702	0.7965	0.2469	0.6213
			0.2273	0.6344	0.2003	0.7764
5	536.0	540.0	0.2003	0.7764	0.2659	0.6070
			0.2469	0.6213	0.2296	0.7543

Tolerance for each bin limit is ± 0.5 nm

Red Color Range

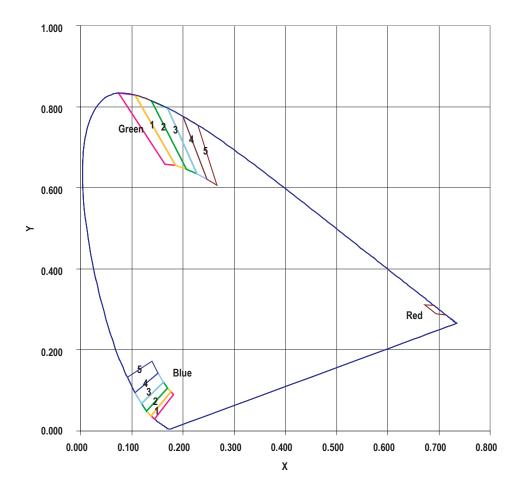
Min Dom	Max Dom	Xmin	Ymin	Xmax	Ymax
622	634	0.6904	0.3094	0.6945	0.2888
		0.6726	0.3106	0.7135	0.2865

Tolerance for each bin limit is \pm 0.5nm

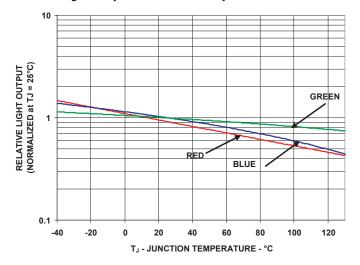
Note:

- All bin categories are established for classification of products. Products may not be available in all bin categories. Please contact your Avago Technologies representative for further information.
- 2. VF bin table only available for those AllnGaP Red devices with options -xxTxx.

Avago Color Bin on CIE 1931 Chromaticity Diagram



Relative Light Output vs Junction Temperature



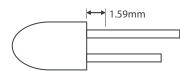
Precautions:

Lead Forming:

- The leads of an LED lamp may be preformed or cut to length prior to insertion and soldering on PC board.
- For better control, it is recommended to use proper tool to precisely form and cut the leads to applicable length rather than doing it manually.
- If manual lead cutting is necessary, cut the leads after the soldering process. The solder connection forms a mechanical ground which prevents mechanical stress due to lead cutting from traveling into LED package. This is highly recommended for hand solder operation, as the excess lead length also acts as small heat sink.

Soldering and Handling:

- Care must be taken during PCB assembly and soldering process to prevent damage to the LED component.
- LED component may be effectively hand soldered to PCB. However, it is only recommended under unavoidable circumstances such as rework. The closest manual soldering distance of the soldering heat source (soldering iron's tip) to the body is 1.59mm. Soldering the LED using soldering iron tip closer than 1.59mm might damage the LED.



- ESD precaution must be properly applied on the soldering station and personnel to prevent ESD damage to the LED component that is ESD sensitive. Do refer to Avago application note AN 1142 for details. The soldering iron used should have grounded tip to ensure electrostatic charge is properly grounded.
- Recommended soldering condition:

	Wave Soldering ^[1, 2]	Manual Solder Dipping
Pre-heat temperature	105 °C Max.	-
Preheat time	60 sec Max	-
Peak temperature	250 °C Max.	260 °C Max.
Dwell time	3 sec Max.	5 sec Max

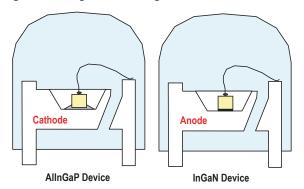
Note:

- 1) Above conditions refers to measurement with thermocouple mounted at the bottom of PCB.
- 2) It is recommended to use only bottom preheaters in order to reduce thermal stress experienced by LED.
- Wave soldering parameters must be set and maintained according to the recommended temperature and dwell time. Customer is advised to perform daily check on the soldering profile to ensure that it is always conforming to recommended soldering conditions.

Note:

- PCB with different size and design (component density) will have different heat mass (heat capacity). This might cause a change in temperature experienced by the board if same wave soldering setting is used. So, it is recommended to re-calibrate the soldering profile again before loading a new type of PCB.
- 2. Avago Technologies' high brightness LED are using high efficiency LED die with single wire bond as shown below. Customer is advised to take extra precaution during wave soldering to ensure that the maximum wave temperature does not exceed 250°C and the solder contact time does not exceeding 3sec. Over-stressing the LED during soldering process might cause premature failure to the LED due to delamination.

Avago Technologies LED configuration



Note: Electrical connection between bottom surface of LED die and the lead frame is achieved through conductive paste.

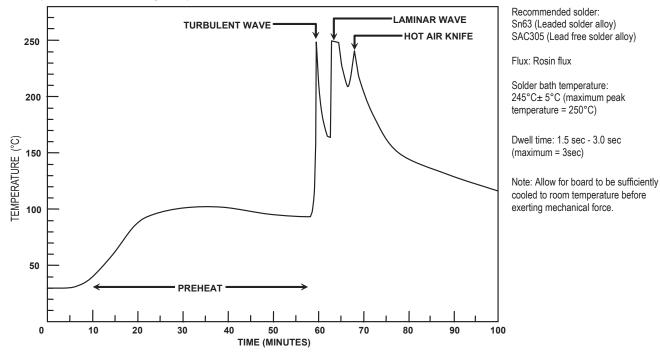
- Any alignment fixture that is being applied during wave soldering should be loosely fitted and should not apply weight or force on LED. Non metal material is recommended as it will absorb less heat during wave soldering process.
- At elevated temperature, LED is more susceptible to mechanical stress. Therefore, PCB must allowed to cool down to room temperature prior to handling, which includes removal of alignment fixture or pallet.
- If PCB board contains both through hole (TH) LED and other surface mount components, it is recommended that surface mount components be soldered on the top side of the PCB. If surface mount need to be on the bottom side, these components should be soldered using reflow soldering prior to insertion the TH LED.
- Recommended PC board plated through holes (PTH) size for LED component leads.

LED component lead size	Diagonal	Plated through hole diameter
0.45 x 0.45 mm	0.636 mm	0.98 to 1.08 mm
(0.018x 0.018 inch)	(0.025 inch)	(0.039 to 0.043 inch)
0.50 x 0.50 mm	0.707 mm	1.05 to 1.15 mm
(0.020x 0.020 inch)	(0.028 inch)	(0.041 to 0.045 inch)

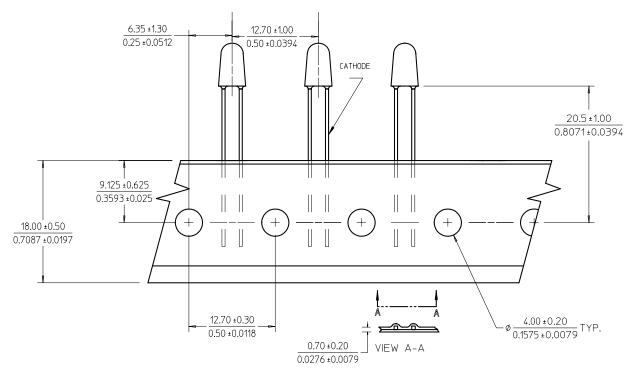
• Over-sizing the PTH can lead to twisted LED after clinching. On the other hand under sizing the PTH can cause difficulty inserting the TH LED.

Refer to Application Note 5334 for more information about soldering and handling of high brightness TH LED lamps.

Example of Wave Soldering Temperature Profile for TH LED

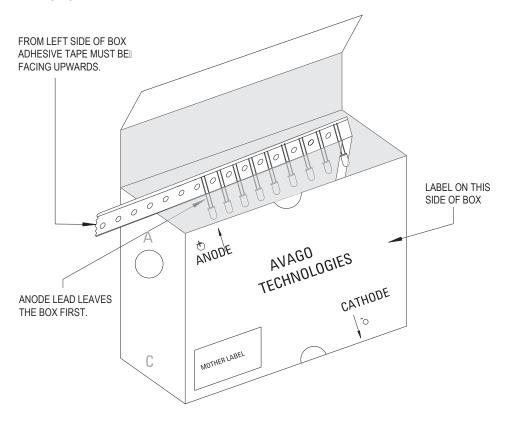


Ammo Packs Drawing



Note: The ammo-packs drawing is applicable for packaging option -DD & -ZZ and regardless standoff or non-standoff

Packaging Box for Ammo Packs



Note: For InGaN device, the ammo pack packaging box contain ESD logo

Packaging Label

(i) Avago Mother Label: (Available on packaging box of ammo pack and shipping box)

(1P) Item: Part Number 	CAT: Intensity Bin		
(P) Customer Item:			
(V) Vendor ID: ┃	(9D) Date Code: Date Code		
DeptID:	Made In: Country of Origin		

(ii) Avago Baby Label (Only available on bulk packaging)

	RoHS Compliant
Lamps Baby Label	e3 max temp 250C
(1P) PART #: Part Number	
(1T) LOT #: Lot Number	
(9D)MFG DATE: Manufacturing Date	QUANTITY: Packing Quantity
C/O: Country of Origin	
Customer P/N:	CAT: Intensity Bin
Supplier Code:	BIN: Refer to below information
	DATECODE: Date Code

Acronyms and Definition:

BIN:

(i) Color bin only or VF bin only

(Applicable for part number with color bins but without VF bin OR part number with VF bins and no color bin) OR

(ii) Color bin incorporated with VF Bin

(Applicable for part number that have both color bin and VF bin)

Example:

(i) Color bin only or VF bin only BIN: 2 (represent color bin 2 only)

BIN: VB (represent VF bin "VB" only)

(ii) Color bin incorporate with VF Bin

BIN: 2VB VB: VF bin "VB" 2: Color bin 2 only

DISCLAIMER: AVAGO'S PRODUCTS AND SOFTWARE ARE NOT SPECIFICALLY DESIGNED, MANUFACTURED OR AUTHORIZED FOR SALE AS PARTS, COMPONENTS OR ASSEMBLIES FOR THE PLANNING, CONSTRUCTION, MAIN-TENANCE OR DIRECT OPERATION OF A NUCLEAR FACILITY OR FOR USE IN MEDICAL DEVICES OR APPLICATIONS. CUSTOMER IS SOLELY RESPONSIBLE, AND WAIVES ALL RIGHTS TO MAKE CLAIMS AGAINST AVAGO OR ITS SUP-PLIERS, FOR ALL LOSS, DAMAGE, EXPENSE OR LIABILITY IN CONNECTION WITH SUCH USE.

For product information and a complete list of distributors, please go to our web site: www.avagotech.com

Avago, Avago Technologies, and the A logo are trademarks of Avago Technologies in the United States and other countries. Data subject to change. Copyright © 2005-2008 Avago Technologies. All rights reserved. Obsoletes AV01-0293EN AV02-0364EN - September 5, 2008

