Vishay High Power Products

Schottky Rectifier, 1.0 A



- Small foot print, surface mountable
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 $^\circ\mathrm{C}$
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level

DESCRIPTION

The VS-MBRA120TRPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	1.0	А		
V _{RRM}		20	V		
I _{FSM}	t _p = 5 μs sine	310	А		
V _F	1.0 Apk, T _J = 125 °C	0.34	V		
TJ	Range	- 65 to 150	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-MBRA120TRPbF	UNITS		
Maximum DC reverse voltage	V _R	20	V		
Maximum working peak reverse voltage	V _{RWM}	20	v		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T_L = 136 °C, rectangular waveform		1.0	
Maximum peak one cycle	I _{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	310	А
non-repetitive surge current		10 ms sine or 6 ms rect. pulse		40	
Non-repetitive avalanche energy	E _{AS}	$T_{J} = 25 \text{ °C}, I_{AS} = 1 \text{ A}, L = 4 \text{ mH}$		2.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1.0	А



Cathode

SMA

PRODUCT SUMMARY

I_{F(AV)}

 V_{R}

 I_{RM}

Anode

1.0 A

20 V

20 mA at 125 °C



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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
Maximum forward voltage drop		1 A	T.I = 25 °C	0.42	0.45	V
		2 A	1)=25 0	0.46	0.52	
	V _{EM} ⁽¹⁾	1 A	T.I = 100 °C	0.33	0.37	
	VFM (*)	2 A	1j = 100 C	0.39	0.45	
		1 A	T,I = 125 °C	0.30	0.35	
		2 A	$-1_{\rm J} = 125$ C	0.36	0.43	
		T _J = 25 °C		0.015	0.2	
Maximum reverse leakage current		T _J = 100 °C	V _R = Rated V _R	2.0	6.0	mA
		T _J = 125 °C		7.0	20	
Typical junction capacitance	CT	V_{R} = 5 V_{DC} (test signal range 100 kHz to 1 MHz), 25 °C		110	-	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		2.0	-	nH
Maximum voltage rate of change	dV/dt	Rated V _R		-	10 000	V/µs

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 $\,\%$

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		- 65 to 150	°C	
Maximum thermal resistance, junction to lead	R _{thJL} ⁽²⁾	DC operation	35	- °C/W	
Maximum thermal resistance, junction to ambient	R _{thJA}		80		
Approximate weight			0.07	g	
Approximate weight			0.002	oz.	
Device marking		Case style SMA (similar D-64)	V12	2A	

Notes

 $^{(1)} \quad \frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$

⁽²⁾ Mounted 1" square PCB



VS-MBRA120TRPbF

 $\dot{D} = 0.20$ D = 0.25

D = 0.33 D = 0.50D = 0.75

DC

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Square wave

See note (1)

0.4

0.6 0.8

I_{F(AV)} - Average Forward Current (A)

1.0 1.2 1.4 1.6

0.2

155

150

145

140

135

130

0

Allowable Case Temperature (°C)

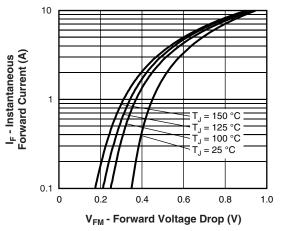
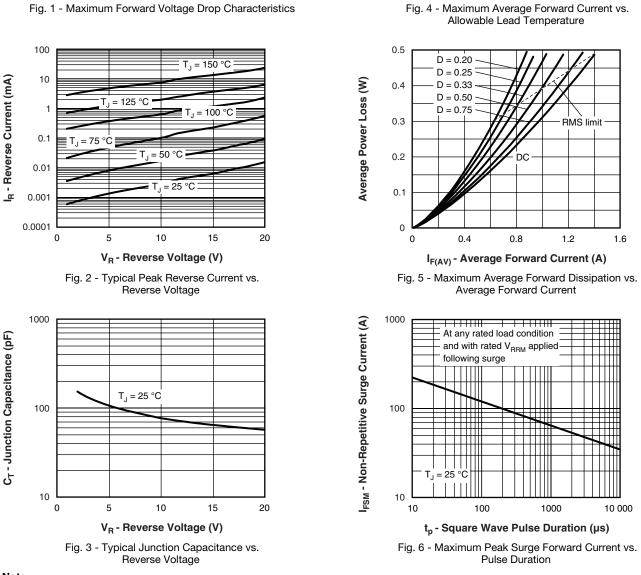


Fig. 1 - Maximum Forward Voltage Drop Characteristics





⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

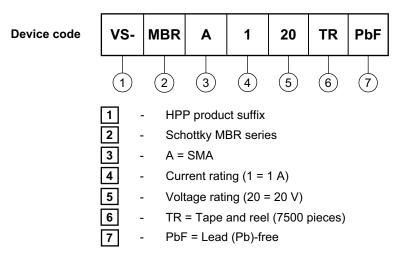
Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}/D$) (see fig. 6); Pd_{REV} = Inverse power loss = $V_{R1} \times I_R$ (1 - D);

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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95018			
Part marking information	www.vishay.com/doc?95029			
Packaging information	www.vishay.com/doc?95034			

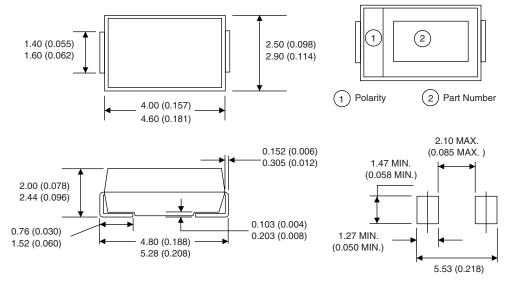


Outline Dimensions

Vishay High Power Products

SMA

DIMENSIONS in millimeters (inches)



Soldering pad



Vishay

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