

# QUICKSWITCH® PRODUCTS HIGH-SPEED CMOS QUICKSWITCH 8-BIT BUS SWITCH

**IDTQS32245** 

### **FEATURES:**

- Enhanced N channel FET with no inherent diode to Vcc
- $5\Omega$  bidirectional switches connect inputs to outputs
- Pin compatible with the 74F245, 74FCT245, and 74FCT245T
- · Low power CMOS proprietary technology
- · Zero propagation delay, zero ground bounce
- · Undershoot clamp diodes on all switch and control inputs
- 25 $\Omega$  resistors for low noise
- · TTL-compatible control inputs
- Available in QSOP and TSSOP packages

### **APPLICATIONS:**

- · Hot-swapping, hot-docking
- Voltage translation (5V to 3.3V)
- Power conservation
- · Capacitance reduction and isolation
- · Logic replacement (data processing)
- Clock gating
- · Bus switching and isolation

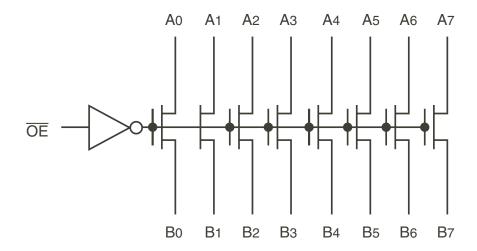
### **DESCRIPTION:**

The QS32245 provides a set of eight high-speed CMOS TTL-compatible bus switches in a pinout compatible with 74FCT245, 74F245, 74ALS/AS/LS245 8-bit transceivers. The Output Enable  $(\overline{OE})$  signal turns the switches on similar to the  $\overline{OE}$  signal of the 74'245. The QS32245 has  $25\Omega$  series resistors to reduce ground bounce noise.

QuickSwitch devices provide an order of magnitude faster speed than conventional logic devices.

The QS32245 is characterized for operation at -40°C to +85°C.

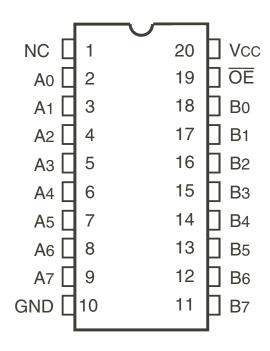
### **FUNCTIONAL BLOCK DIAGRAM**



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**FEBRUARY 2011** 

### **PIN CONFIGURATION**



QSOP/TSSOP TOP VIEW

# **ABSOLUTE MAXIMUM RATINGS**(1)

Symbol	Description	Max	Unit
VTERM <sup>(2)</sup>	Supply Voltage to Ground	-0.5 to +7	٧
VTERM <sup>(3)</sup>	DC Switch Voltage Vs	-0.5 to +7	٧
VTERM <sup>(3)</sup>	DC Input Voltage VIN	-0.5 to +7	V
VAC	AC Input Voltage (pulse width ≤ 20ns)	-3	V
lout	DC Output Current	120	mA
Рмах	Maximum Power Dissipation (TA = 85°C)	0.5	W
Tstg	Storage Temperature	-65 to +150	°C

#### NOTE:

- 1. Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.
- 2. Vcc terminals.
- 3. All terminals except Vcc.

### **CAPACITANCE**

 $(TA = +25^{\circ}C, f = 1.0MHz, VIN = 0V, VOUT = 0V)$ 

Pins	Тур.	Max. <sup>(1)</sup>	Unit
Control Pins	3	5	pF
Quickswitch Channels (Switch OFF)	5	7	pF

#### NOTE:

1. This parameter is measured at characterization but not tested.

### **PIN DESCRIPTION**

Pin Names	Description	
ŌĒ	Output Enable	
An	Data I/Os	
Bn	Data I/Os	

# **FUNCTION TABLE(1)**

ŌĒ	Outputs		
Н	Disconnected		
L	An = Bn		

#### NOTE:

1. H = HIGH Voltage Level L = LOW Voltage Level

# DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

 $Following\ Conditions\ Apply\ Unless\ Otherwise\ Specified:$ 

Industrial: TA = -40°C to +85°C, VCC = 5.0V  $\pm 5$ %

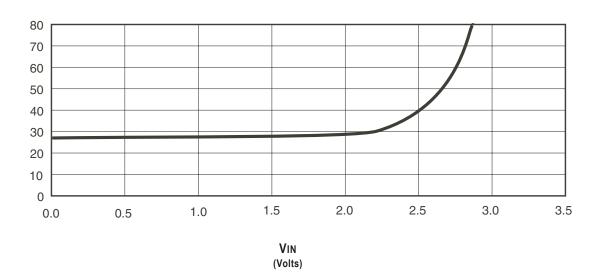
Symbol	Parameter	Test Conditions	Min.	Typ. <sup>(1)</sup>	Max.	Unit
VIH	Input HIGH Level	Guaranteed Logic HIGH for Control Pins	2	_	_	V
VIL	Input LOW Level	Guaranteed Logic LOW for Control Pins	_	_	0.8	V
lin	Input LeakageCurrent (Control Inputs) <sup>(2)</sup>	$0V \le VIN \le VCC$	_	_	±1	μΑ
loz	Off-State Output Current (Hi-Z)	0V ≤ Vouт ≤ Vcc, Switches OFF	_	±0.001	±1	μA
Ron <sup>(3)</sup>	Switch ON Resistance	Vcc = Min., Vin = 0V, Ion = 30mA	18	23	35	Ω
		Vcc = Min., VIN = 2.4V, ION =15mA	18	25	40	
VP	Pass Voltage <sup>(2)</sup>	$V_{\text{IN}}$ = VCC = 5V, lout = -5 $\mu$ A	3.7	4	4.2	V

#### NOTES:

- 1. Typical values are at Vcc = 5.0V, TA = 25°C.
- 2. Pass Voltage is guaranteed but not production tested.
- 3. Rout changed on March 8, 2002. See rear page for more information.

# TYPICAL ON RESISTANCE vs Vin AT Vcc = 5V

RON (ohms)



### POWER SUPPLY CHARACTERISTICS

Symbol	Parameter	Test Conditions <sup>(1)</sup> Max.		Unit
Iccq	Quiescent Power Supply Current	Vcc = Max., Vin = GND or Vcc, f = 0	3	μΑ
Δlcc	Power Supply Current per Control Input HIGH (2)	Vcc = Max., Vin = 3.4V, f = 0	1.5	mA
ICCD	Dynamic Power Supply Current per MHz <sup>(3)</sup>	Vcc = Max., A and B pins open	0.25	mA/MHz
		Control Inputs Toggling at 50% Duty Cycle		

#### NOTES:

- 1. For conditions shown as Min. or Max., use the appropriate values specified under DC Electrical Characteristics.
- 2. Per TLL driven input ( $V_{IN} = 3.4V$ , control inputs only). A and B pins do not contribute to  $\Delta lcc$ .
- 3. This current applies to the control inputs only and represents the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is guaranteed but not production tested.

### SWITCHING CHARACTERISTICS OVER OPERATING RANGE

 $T_A = -40$ °C to +85°C,  $V_{CC} = 5.0V \pm 5\%$ ;

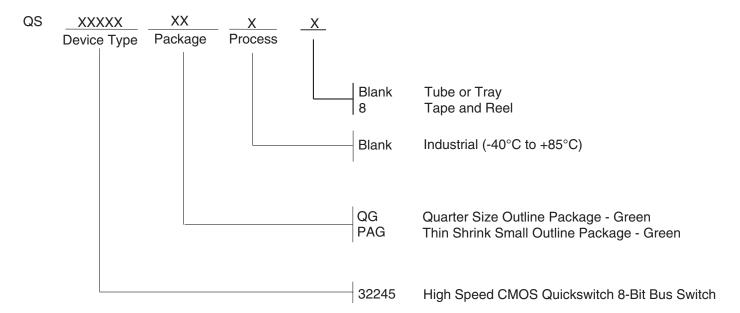
CLOAD = 50pF, RLOAD =  $500\Omega$  unless otherwise noted.

Symbol	Parameter	Min. <sup>(1)</sup>	Тур.	Max.	Unit
<b>t</b> PLH	Data Propagation Delay (2,3)	_	_	1.25	ns
tphL	An to/from Bn				
tpzL	Switch Turn-on Delay	0.5	_	6.6	ns
tpzh	OE to An/Bn				
tPLZ	Switch Turn-off Delay (2)	0.5	_	4.5	ns
tPHZ	OE to An/Bn				

#### NOTES:

- 1. Minimums are guaranteed but not production tested.
- 2. This parameter is guaranteed but not production tested.
- 3. The bus switch contributes no propagation delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 1.25ns for C<sub>L</sub> = 50pF. Since this time constant is much smaller than the rise and fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the bus switch, when used in a system, is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

### ORDERING INFORMATION



# **Datasheet Document History**

02/09/2011

Pg. 5

Updated the ordering information by removing the "IDT" notation, non RoHS part and by adding Tape and Reel information.



**CORPORATE HEADQUARTERS** 

6024 Silver Creek Valley Road San Jose, CA 95138

for SALES:

800-345-7015 or 408-284-8200 fax: 408-284-2775

www.idt.com

for Tech Support: logichelp@idt.com