

PS9821-1,-2

HIGH CMR, 15 Mbps OPEN COLLECTOR OUTPUT TYPE 8-PIN SSOP (SO-8) 3.3 V HIGH-SPEED PHOTOCOUPLER

R08DS0260EJ0100 Rev.1.00 Dec 17, 2021

DESCRIPTION

The PS9821-1 and PS9821-2 are active-low type high-speed photocouplers that use an AlGaAs light-emitting diode on the input side and a photodetector IC that includes a photodiode and a signal processor on the same chip on the output side.

The PS9821-1, -2 are designed specifically for high common mode transient immunity (CMR) and low pulse width distortion, PS9821-2 is suitable for high density applications.

FEATURES

- Low power consumption (Vcc = 3.3 V)
- Pulse width distortion ($|t_{PHL}-t_{PLH}| = 35 \text{ ns MAX.}$)
- High common mode transient immunity (CM_H, CM_L = ± 15 kV/ μ s MIN.)
- 40% reduction of mounting area (5-pin SOP × 2)
- High-speed (15 Mbps)
- High isolation voltage (BV = 2 500 Vr.m.s.)
- Open collector output
- Ordering number of tape product: PS9821-1-F3 : 1 500 pcs/reel

: PS9821-2-F3 : 1 500 pcs/reel

- Pb-Free product
- Safety standards
 - UL : UL1577, Single protection
 - CSA : CAN/CSA-C22.2 No.62368-1, Basic insulation
 - VDE : DIN EN 60747-5-5 (Option)

PIN CONNECTION (Top View) PS9821-1 1. NC 2. Anode 3. Cathode 4. NC 5. GND 6. V_O 7. NC 8. V_{CC} PS9821-2 1. Anode1 2. Cathode1 3. Cathode2 4. Anode2 5. GND 6. V_{O2} 7. V_{O1} 8. V_{CC}

APPLICATIONS

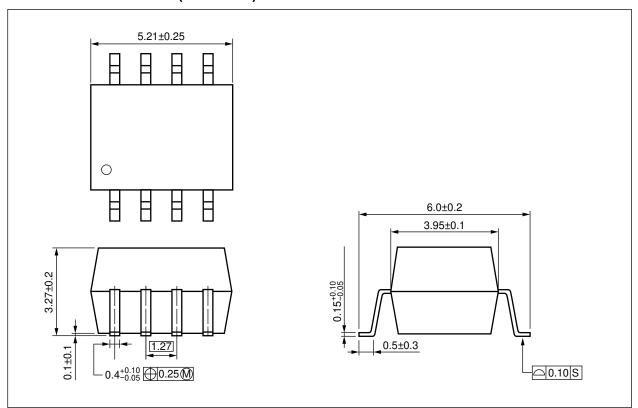
- Measurement equipment
- PDP
- FA Network

TRUTH TABLE

LED	Output
ON	L
OFF	Н

Start of mass production Sep.2004

PACKAGE DIMENSIONS (UNIT: mm)



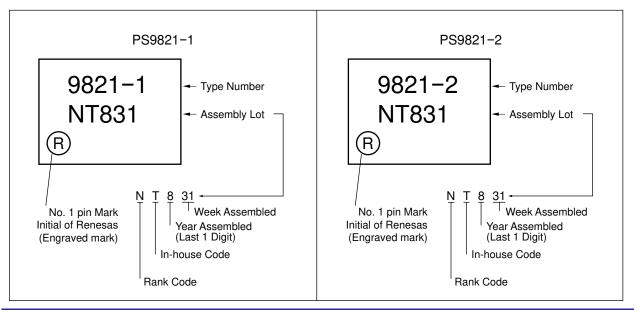
Weight: 0.14g (typ.)

PHOTOCOUPLER CONSTRUCTION

Parameter	Unit (MIN.)
Air Distance	4 mm
Creepage Distance	4 mm
Isolation Distance	0.2 mm

MARKING EXAMPLE

Ni/Pd/Au PLATING



ORDERING INFORMATION

Part Number	Order Number	Solder Plating Specification	Packing Style	Safety Standard Approval	Application Part Number*1
PS9821-1	PS9821-1-AX	Pb-Free	20 pcs (Tape 20 pcs cut)	Standard products	PS9821-1
PS9821-1-F3	PS9821-1-F3-AX	(Ni/Pd/Au)	Embossed Tape 1 500 pcs/reel	(UL, CSA	
PS9821-2	PS9821-2-AX		20 pcs (Tape 20 pcs cut)	- approved)	PS9821-2
PS9821-2-F3	PS9821-2-F3-AX		Embossed Tape 1 500 pcs/reel		
PS9821-1-V	PS9821-1-V-AX		20 pcs (Tape 20 pcs cut)	UL, CSA,	PS9821-1
PS9821-1-V-F3	PS9821-1-V-F3-AX		Embossed Tape 1 500 pcs/reel	DIN EN 60747-5-5 approved	
PS9821-2-V	PS9821-2-V-AX		20 pcs (Tape 20 pcs cut)	арргочес	PS9821-2
PS9821-2-V-F3	PS9821-2-V-F3-AX		Embossed Tape 1 500 pcs/reel		

Notes*: 1. For the application of the Safety Standard, following part number should be used.

ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C, unless otherwise specified)

	Parameter		Rat	Unit			
			PS9821-1	PS9821-2			
Diode	Forward Current	l _F	20 ^{*1}	15 ^{*2}	mA		
	Reverse Voltage	V _R		5	V/ch		
Detector	Supply Voltage	Vcc	7		V		
	Output Voltage	Vo	7		V/ch		
	Output Current	lo	25		mA/ch		
	Power Dissipation *3	Pc	40		mW/ch		
Isolation V	∕oltage ^{*4}	BV	2 500		2 500		Vr.m.s.
Operating	Ambient Temperature	TA	- 40 to + 85		°C		
Storage T	emperature	T _{stg}	- 55 to + 125		°C		

Notes*:1. Reduced to 0.3 mA/ $^{\circ}$ C at T_A = 60 $^{\circ}$ C or more.

- 2. Reduced to 0.1 mA/ $^{\circ}$ C at T_A = 60 $^{\circ}$ C or more.
- 3. Applies to output pin Vo (collector pin). Reduced to 1.5 mW/ $^{\circ}$ C at T_A = 65 $^{\circ}$ C or more.
- 4. AC voltage for 1 minute at $T_A = 25$ °C, RH = 60 % between input and output. Pins 1-4 shorted together, 5-8 shorted together.

RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Low Level Input Voltage	V _{FL}	0		0.8	V
High Level Input Current	I _{FH}	6.3	10	12.5	mA
Supply Voltage	V _{CC}	2.7		3.6	٧
Pull-up Resistance	RL	330		4 k	Ω
TLL ($R_L = 1.0 \text{ k}\Omega$, loads)	N			5	

ELECTRICAL CHARACTERISTICS (1/2) ($T_A = -40$ to +85 °C, unless otherwise specified)

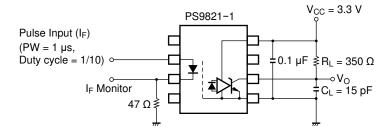
	Parameter	Symbol	Condi	itions	MIN.	TYP.*1	MAX.	Unit
Diode	Forward Voltage	VF	IF = 10 mA, T _A = 25	5 °C	1.4	1.65	1.8	V
	Reverse Current		VR = 3.0 V, TA = 25	5 °C			10	μА
	Terminal Capacitance	Ct	V _F = 0 V, f = 1 MHz, T _A = 25 °C			30		pF
Detector	High Level Output Current	Іон	Vcc = Vo = 3.3 V, I	F = 0.8 mA		1	80	μА
			$Vcc = Vo = 5.5 \text{ V}, I_F = 0.8 \text{ mA}$			1*2		
	Low Level Output Voltage *3	Vol	Vcc = 3.3 V, I _F = 5.0	0 mA, lo _L = 13 mA		0.2	0.6	V
			Vcc = 5.5 V, I _F = 5.0	Vcc = 5.5 V, IF = 5.0 mA, IoL = 13 mA				
	High Level Supply Current	Іссн	Vcc = 3.3 V, IF = 0	Vcc = 3.3 V, I _F = 0 mA, Vo = open		4	7	mA
	(PS9821-1)		Vcc = 5.5 V, IF = 0	mA, Vo = open		5*²		
	High Level Supply Current		Vcc = 3.3 V, IF = 0	mA, Vo = open		8	14	
	(PS9821-2)		Vcc = 5.5 V, I _F = 0	mA, Vo = open		10*2		
	Low Level Supply Current	Iccl	Vcc = 3.3 V, IF = 10	0 mA, Vo = open		7	10	
	(PS9821-1)		Vcc = 5.5 V, IF = 10	0 mA, Vo = open		9*2		
	Low Level Supply Current		Vcc = 3.3 V, IF = 10	0 mA, Vo = open		14	20	
	(PS9821-2)		Vcc = 5.5 V, IF = 10	0 mA, Vo = open		18*²		
Coupled	Threshold Input Current	IFHL	Vcc = 3.3 V, Vo = 0	0.8 V, R _L = 350 Ω		2.5	5	mA
	$(H \rightarrow L)$		$Vcc = 5 \text{ V}, Vo = 0.8 \text{ V}, RL = 350 \Omega$			2.5*2		
	Isolation Resistance	R _{I-O}	$V_{I\text{-O}} = 1 \text{ kV}_{DC}, \text{ RH} = 40 \text{ to } 60 \text{ \%},$ $T_A = 25 \text{ °C}$		10 ¹¹			Ω
	Insulation Resistance (Input-Input), (PS9821-2)	R⊩	V_{H} = 1 kVpc, RH = 40 to 60 %, T_{A} = 25 °C		10 ¹⁰			Ω
	Isolation Capacitance	CI-O	V = 0 V, f = 1 MHz	z, T _A = 25 °C		0.6		pF
	Insulation Capacitance (Input-Input), (PS9821-2)	Cı-ı	V = 0 V, f = 1 MHz	V = 0 V, f = 1 MHz, T _A = 25 °C		0.3		pF
	Propagation Delay Time	t PHL		T _A = 25 °C		45	75	ns
	$(H \rightarrow L)^{*4}$		Vcc = 3.3 V, RL = 3	50Ω , IF = 7.5 mA			100	
			Vcc = 5 V, RL = 350	Ω , IF = 7.5 mA		38*²		
	Propagation Delay Time	t PLH		T _A = 25 °C		50	75	
	$(L \rightarrow H)^{*4}$		Vcc = 3.3 V, RL = 3	50Ω , IF = 7.5 mA			100	
			Vcc = 5 V, RL = 350	Ω , I _F = 7.5 mA		43*2		
	Rise Time	tr	Vcc = 3.3 V, RL = 3	50Ω , IF = 7.5 mA		20		
			Vcc = 5 V, RL = 350	Ω , IF = 7.5 mA		20*²		
	Fall Time	t f	Vcc = 3.3 V, R _L = 350 Ω , I _F = 7.5 mA Vcc = 5 V, R _L = 350 Ω , I _F = 7.5 mA			5		
						5*2		
	Pulse Width Distortion (PWD) *4	tplh-tphl	$V_{CC} = 3.3 \text{ V}, \text{ R}_{L} = 350 \ \Omega, \text{ I}_{F} = 7.5 \text{ mA}$ $V_{CC} = 5 \text{ V}, \text{ R}_{L} = 350 \ \Omega, \text{ I}_{F} = 7.5 \text{ mA}$			5	35	
						5*2		
	Propagation Delay Skew	t psk	Vcc = 3.3 V, RL = 3	50Ω , I _F = 7.5 mA			40	

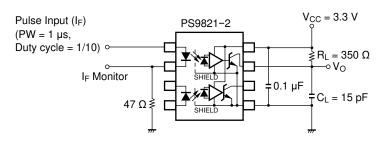
ELECTRICAL CHARACTERISTICS (2/2) (TA = -40 to +85 °C, unless otherwise specified)

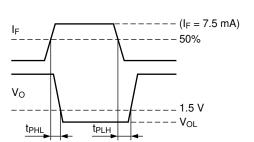
	Parameter	Symbol	Conditions	MIN.	TYP.*1	MAX.	Unit
Coupled	Common Mode Transient Immunity at High	СМн	$V_{CC} = 3.3 \text{ V}, \text{ RL} = 350 \ \Omega, \text{ TA} = 25 \ ^{\circ}\text{C},$ $I_{F} = 0 \text{ mA}, \text{ Vo} > 2 \text{ V}, \text{ V}_{CM} = 1 \text{ kV}$	15	20		kV/μs
	Level Output*5		$\begin{aligned} &V_{\text{CC}} = 5 \text{ V}, \text{ RL} = 350 \ \Omega, \text{ TA} = 25 \ ^{\circ}\text{C}, \\ &I_{\text{F}} = 0 \text{ mA}, \text{ Vo} > 2 \text{ V}, \text{ V}_{\text{CM}} = 1 \text{ kV} \end{aligned}$		20*²		
	Common Mode Transient Immunity at Low	CML	$\label{eq:Vcc} \begin{array}{l} \mbox{Vcc} = 3.3 \ \mbox{V}, \ \mbox{RL} = 350 \ \Omega, \ \mbox{Ta} = 25 \ \mbox{°C}, \\ \mbox{IF} = 7.5 \ \mbox{mA}, \ \mbox{Vo} < 0.8 \ \mbox{V}, \ \mbox{Vcm} = 1 \ \mbox{kV} \end{array}$	15	20		
	Level Output*5		$\label{eq:Vcc} \begin{array}{l} \mbox{Vcc} = 5 \mbox{ V, } \mbox{R}_{L} = 350 \Omega, \mbox{ T}_{A} = 25 ^{\circ}\mbox{C}, \\ \mbox{IF} = 7.5 \mbox{ mA, Vo} < 0.8 \mbox{ V, V}_{CM} = 1 \mbox{ kV} \end{array}$		20*2		

Notes*: 1. Typical values at T_A = 25 °C

- 2. These values are reference values.
- 3. Because Vol of 2 V or more may be output when LED current input and when output supply of Vcc = 2.6 V or less, it is important to confirm the characteristics (operation with the power supply on and off) during design, before using this device.
- 4. Test circuit for propagation delay time.

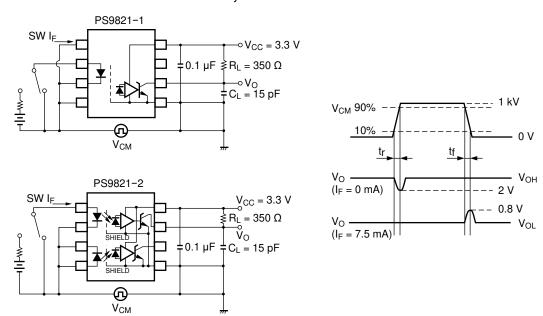






Remark: C_L includes probe and stray wiring capacitance.

5. Test circuit for common mode transient immunity

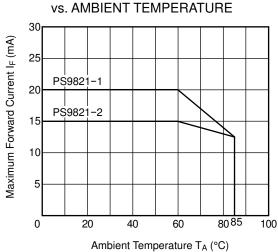


Remark: C_L includes probe and stray wiring capacitance.

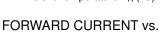
USAGE CAUTIONS

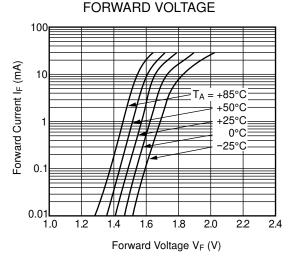
- 1. This product is weak for static electricity by designed with high-speed integrated circuit so protect against static electricity when handling.
- 2. By-pass capacitor of 0.1 μ F is used between V_{CC} and GND near device. Also, ensure that the distance between the leads of the photocoupler and capacitor is no more than 10 mm.
- 3. Avoid storage at a high temperature and high humidity.
- 4. Avoid cleaning with Freon based or halogen-based (chlorinated etc.) solvents.
- 5. Do not use fixing agents or coatings containing halogen-based substances.

TYPICAL CHARACTERISTICS (T_A = 25 °C unless otherwise specified)

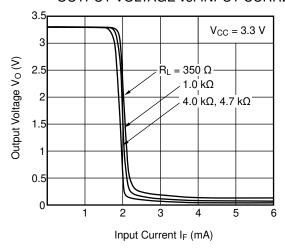


MAXIMUM FORWARD CURRENT

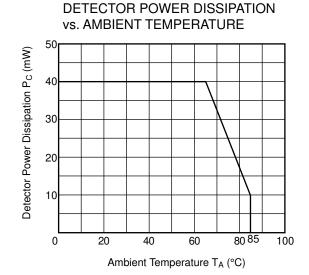




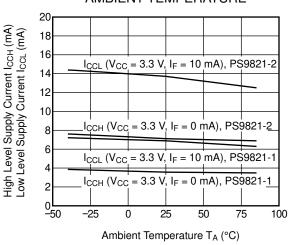
OUTPUT VOLTAGE vs. INPUT CURRENT



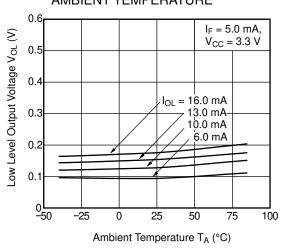
Remark The graphs indicate nominal characteristics.



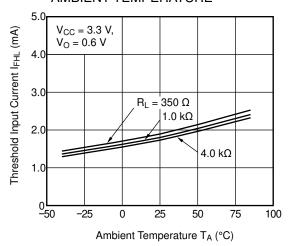
SUPPLY CURRENT vs. AMBIENT TEMPERATURE



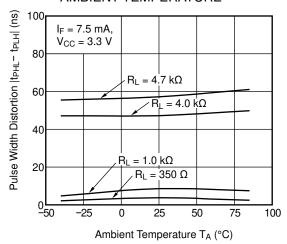
LOW LEVEL OUTPUT VOLTAGE vs. AMBIENT TEMPERATURE



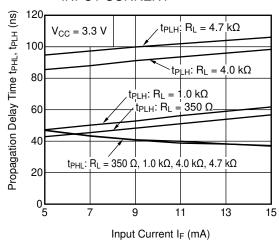
THRESHOLD INPUT CURRENT vs. AMBIENT TEMPERATURE



PULSE WIDTH DISTORTION vs. AMBIENT TEMPERATURE

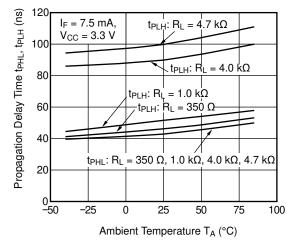


PROPAGATION DELAY TIME vs. INPUT CURRENT

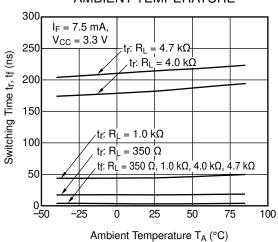


Remark The graphs indicate nominal characteristics.

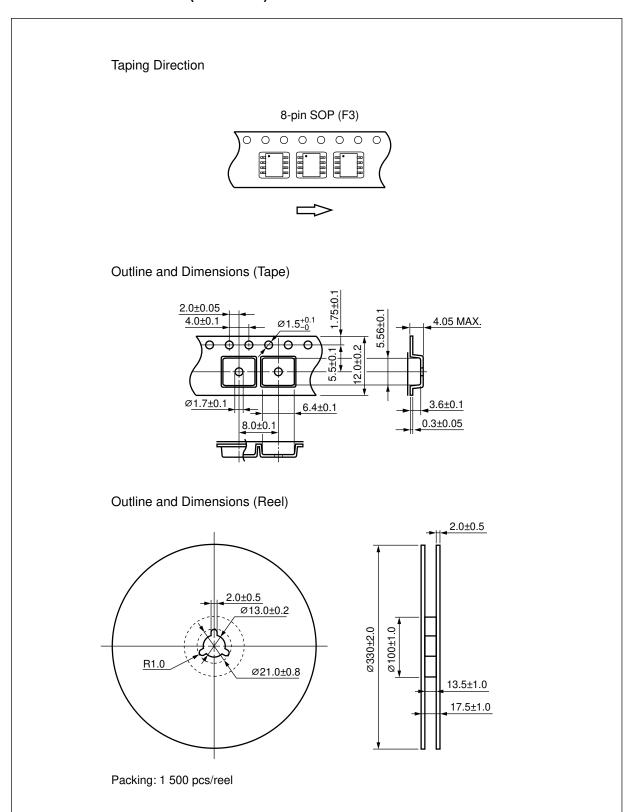
PROPAGATION DELAY TIME vs. AMBIENT TEMPERATURE



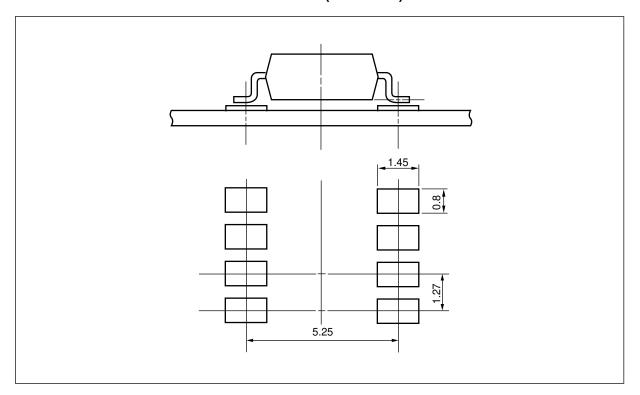
SWITCHING TIME vs. AMBIENT TEMPERATURE



TAPING SPECIFICATIONS (UNIT: mm)



RECOMMENDED MOUNT PAD DIMENSIONS (UNIT: mm)



Remark All dimensions in this figure must be evaluated before use.

NOTES ON HANDLING

- 1. Recommended soldering conditions
 - (1) Infrared reflow soldering

 Peak reflow temperature 260 °C or below (package surface temperature)

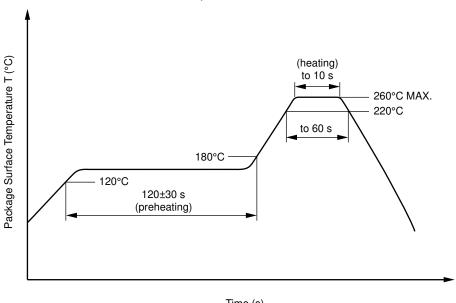
· Time of peak reflow temperature 10 seconds or less Time of temperature higher than 220 °C 60 seconds or less

• Time to preheat temperature from 120 to 180 °C $\,$ 120 \pm 30 s Number of reflows

• Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of

0.2 Wt% is recommended.)

Recommended Temperature Profile of Infrared Reflow



Time (s)

(2) Wave soldering

 Temperature 260 °C or below (molten solder temperature)

 Time 10 seconds or less

· Preheating conditions 120 °C or below (package surface temperature)

One (Allowed to be dipped in solder including plastic mold portion.) Number of times • Flux Rosin flux containing small amount of chlorine (The flux with a maximum

chlorine content of 0.2 Wt% is recommended.)

(3) Soldering by Soldering Iron

 Peak Temperature (lead part temperature) 350 °C or below Time (each pins) 3 seconds or less

• Flux Rosin flux containing small amount of chlorine

(The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

- (a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead
- (b) Please be sure that the temperature of the package would not be heated over 100 °C
- (4) Cautions
 - Flux Cleaning

Avoid cleaning with Freon based or halogen-based (chlorinated etc.) solvents.

• Do not use fixing agents or coatings containing halogen-based substances.

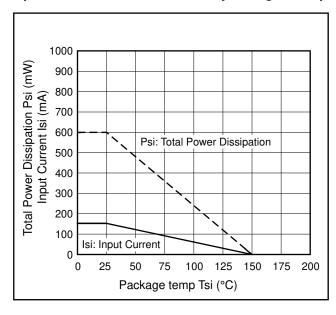
2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output or between collector-emitters at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.

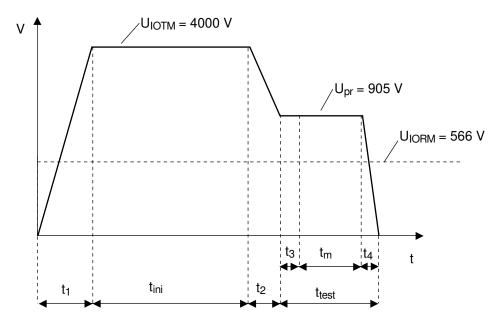
SPECIFICATION OF VDE MARKS LICENSE DOCUMENT

Parameter	Symbol	Rating	Unit
Climatic test class (IEC 60068-1/DIN EN 60068-1)		55/100/21	
Dielectric strength			
maximum operating isolation voltage	UIORM	566	V_{peak}
Test voltage (partial discharge test, procedure a for type test and random test)	U_pr	849	V_{peak}
$U_{pr} = 1.6 \times U_{IORM.}, P_d < 5 pC$			
Test voltage (partial discharge test, procedure b for all devices)	U_pr	1 061	V_{peak}
$U_{pr} = 1.875 \times U_{IORM.}, P_d < 5 pC$			
Highest permissible overvoltage	U_{IOTM}	4 000	V_{peak}
Degree of pollution (IEC 60664-1/DIN EN 60664-1 (VDE 0110-1))		2	
Comparative tracking index (IEC 60112/DIN EN 60112 (VDE 0303-11))	CTI	175	
Material group (IEC 60664-1/DIN EN 60664-1 (VDE 0110-1))		III a	
Storage temperature range	T _{stg}	- 55 to +125	°C
Operating temperature range	T _A	- 40 to +85	°C
Isolation resistance, minimum value			
V_{IO} = 500 V dc at T_A = 25 °C	Ris MIN.	10 ¹²	Ω
V _{IO} = 500 V dc at T _A MAX. at least 100 °C	Ris MIN.	10 ¹¹	Ω
Safety maximum ratings (maximum permissible in case of fault, see thermal			
derating curve)			
Package temperature	Tsi	150	°C
Current (input current I _F , Psi = 0)	Isi	150	mA
Power (output or total power dissipation)	Psi	600	mW
Isolation resistance			
$V_{IO} = 500 \text{ V dc at T}_A = Tsi$	Ris MIN.	10 ⁹	Ω

Dependence of maximum safety ratings with package temperature



Method a) Destructive Test, Type and Sample Test



 t_1 , $t_2 = 1$ to 10 sec

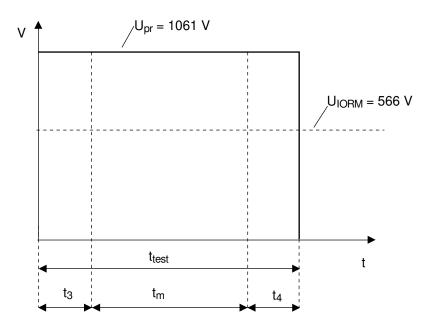
 $t_3, t_4 = 1 sec$

 $t_{m(PARTIAL\ DISCHARGE)} = 10\ sec$

 $t_{test} = 12 sec$

 $t_{ini} = 60 \text{ sec}$

Method b) Non-destructive Test, 100% Production Test



 t_3 , $t_4 = 0.1$ sec

 $t_{m(PARTIAL\ DISCHARGE)} = 1.0\ sec$

 $t_{test} = 1.2 sec$

Caution

GaAs Products

This product uses gallium arsenide (GaAs).

GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.

- Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
 - Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
- 2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
- Do not burn, destroy, cut, crush, or chemically dissolve the product.
- Do not lick the product or i any way allow it to enter the mouth.

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(Rev.5.0-1 October 2020)

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