# **PDTC123T series**

NPN resistor-equipped transistors; R1 = 2.2 k $\Omega$ , R2 = open Rev. 01 — 10 March 2006

**Product data sheet** 

## **Product profile**

## 1.1 General description

NPN Resistor-Equipped Transistors (RET) family in Surface Mounted Device (SMD) plastic packages.

Table 1. **Product overview** 

Type number	Package	Package			
	Philips	JEITA	JEDEC		
PDTC123TE	SOT416	SC-75	-	PDTA123TE	
PDTC123TK	SOT346	SC-59A	TO-236	PDTA123TK	
PDTC123TM	SOT883	SC-101	-	PDTA123TM	
PDTC123TS[1]	SOT54	SC-43A	TO-92	PDTA123TS	
PDTC123TT	SOT23	-	TO-236AB	PDTA123TT	
PDTC123TU	SOT323	SC-70	-	PDTA123TU	

<sup>[1]</sup> Also available in SOT54A and SOT54 variant packages (see Section 2).

### 1.2 Features

- Built-in bias resistors
- Simplifies circuit design
- 100 mA output current capability
- Reduces component count
- Reduces pick and place costs

### 1.3 Applications

- Digital applications
- Control of IC inputs

- Cost-saving alternative for BC847 series in digital applications
- Switching loads

### 1.4 Quick reference data

Table 2. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{CEO}$	collector-emitter voltage	open base	-	-	50	V
I <sub>O</sub>	output current		-	-	100	mA
R1	bias resistor 1 (input)		1.54	2.2	2.86	kΩ



# 2. Pinning information

Table 3. Pinning

Pin Description Simplified outline Symbol

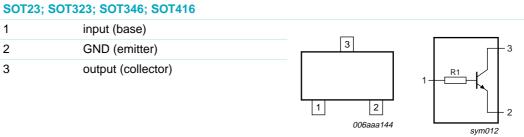
SOT54

1 input (base)
2 output (collector)
3 GND (emitter)

Quadratic Pinning

SOT54A			
1	input (base)		
2	output (collector)		2
3	GND (emitter)	001aab348	1 R1 3

SOT54 varia	ant			
1	input (base)	r		
2	output (collector)		₹:	1 2
3	GND (emitter)	ه الله الله الله الله الله الله الله ال	001aab447	1 R1 3



		006aaa144	sym012
<b>SOT883</b>			
1	input (base)		
2	GND (emitter)	1 3	3
3	output (collector)	2 Transparent top view	1 R1 2 sym012

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NPN resistor-equipped transistors; R1 = 2.2 kΩ, R2 = open

# 3. Ordering information

Table 4. Ordering information

Type number	Package	Package						
	Name	Description	Version					
PDTC123TE	SC-75	plastic surface mounted package; 3 leads	SOT416					
PDTC123TK	SC-59A	plastic surface mounted package; 3 leads	SOT346					
PDTC123TM	SC-101	leadless ultra small plastic package; 3 solder lands; body $1.0\times0.6\times0.5~\text{mm}$	SOT883					
PDTC123TS[1]	SC-43A	plastic single-ended leaded (through hole) package; 3 leads	SOT54					
PDTC123TT	-	plastic surface mounted package; 3 leads	SOT23					
PDTC123TU	SC-70	plastic surface mounted package; 3 leads	SOT323					

<sup>[1]</sup> Also available in SOT54A and SOT54 variant packages (see Section 2 and Section 9).

## 4. Marking

Table 5. Marking codes

rabio or marking course	
Type number	Marking code <sup>[1]</sup>
PDTC123TE	2B
PDTC123TK	GB
PDTC123TM	FB
PDTC123TS	TC123T
PDTC123TT	ZM*
PDTC123TU	*1T

<sup>[1] \* = -:</sup> made in Hong Kong

<sup>\* =</sup> p: made in Hong Kong

<sup>\* =</sup> t: made in Malaysia

<sup>\* =</sup> W: made in China

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NPN resistor-equipped transistors; R1 = 2.2 k $\Omega$ , R2 = open

## 5. Limiting values

Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{CBO}$	collector-base voltage	open emitter	-	50	V
$V_{CEO}$	collector-emitter voltage	open base	-	50	V
$V_{EBO}$	emitter-base voltage	open collector	-	5	V
I <sub>O</sub>	output current		-	100	mA
I <sub>CM</sub>	peak collector current	single pulse; $t_p \le 1 \text{ ms}$	-	100	mA
P <sub>tot</sub>	total power dissipation	$T_{amb} \le 25  ^{\circ}C$			
	SOT416		<u>[1]</u> -	150	mW
	SOT346		<u>[1]</u> -	250	mW
	SOT883		[2][3]	250	mW
	SOT54		<u>[1]</u> -	500	mW
	SOT23		<u>[1]</u> -	250	mW
	SOT323		<u>[1]</u> -	200	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		-	150	°C
T <sub>amb</sub>	ambient temperature		-65	+150	°C

<sup>[1]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

## 6. Thermal characteristics

Table 7. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air				
	SOT416		<u>[1]</u> -	-	833	K/W
	SOT346		<u>[1]</u> -	-	500	K/W
	SOT883		[2][3]	-	500	K/W
	SOT54		<u>[1]</u> _	-	250	K/W
	SOT23		<u>[1]</u> _	-	500	K/W
	SOT323		<u>[1]</u> _	-	625	K/W

<sup>[1]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

<sup>[2]</sup> Reflow soldering is the only recommended soldering method.

<sup>[3]</sup> Device mounted on an FR4 PCB with 60 µm copper strip line, standard footprint.

<sup>[2]</sup> Reflow soldering is the only recommended soldering method.

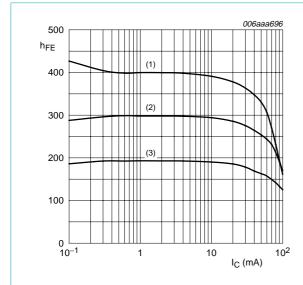
<sup>[3]</sup> Device mounted on an FR4 PCB with 60  $\mu m$  copper strip line, standard footprint.

## 7. Characteristics

Table 8. Characteristics

T<sub>amb</sub> = 25 °C unless otherwise specified.

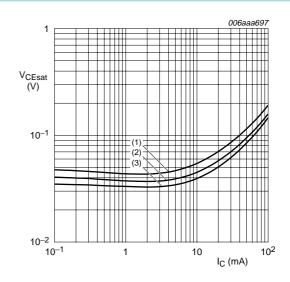
ramb – 20	o unicos cinerwise opecinica.					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$I_{CBO}$	collector-base cut-off current	$V_{CB} = 50 \text{ V}; I_E = 0 \text{ A}$	-	-	100	nA
$I_{CEO}$	collector-emitter cut-off	$V_{CE} = 30 \text{ V}; I_{B} = 0 \text{ A}$	-	-	1	50 μΑ
	current	$V_{CE} = 30 \text{ V; } I_{B} = 0 \text{ A;}$ $T_{j} = 150 \text{ °C}$	-	-	50	μΑ
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB} = 5 \text{ V}; I_C = 0 \text{ A}$	-	-	100	nA
h <sub>FE</sub>	DC current gain	$V_{CE} = 5 \text{ V}; I_{C} = 20 \text{ mA}$	30	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C = 10 \text{ mA}; I_B = 0.5 \text{ mA}$	-	-	150	mV
R1	bias resistor 1 (input)		1.54	2.2	2.86	kΩ
C <sub>c</sub>	collector capacitance	$V_{CB} = 10 \text{ V}; I_E = I_e = 0 \text{ A};$ f = 1 MHz	-	-	2.5	pF





- (1)  $T_{amb} = 100 \, ^{\circ}C$
- (2)  $T_{amb} = 25 \,^{\circ}C$
- (3)  $T_{amb} = -40 \, ^{\circ}C$

Fig 1. DC current gain as a function of collector current; typical values

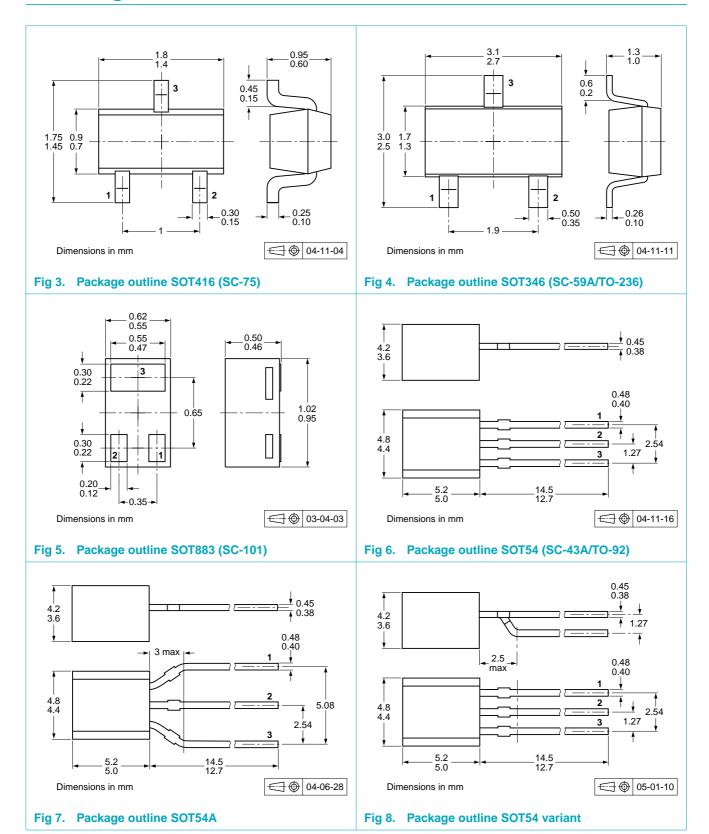


$$I_{\rm C}/I_{\rm B} = 20$$

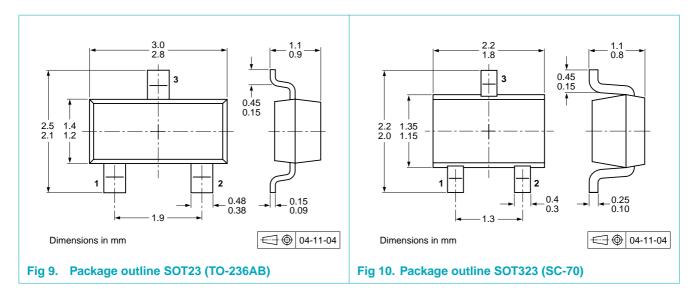
- (1)  $T_{amb} = 100 \, ^{\circ}C$
- (2)  $T_{amb} = 25 \, ^{\circ}C$
- (3)  $T_{amb} = -40 \, ^{\circ}C$

Fig 2. Collector-emitter saturation voltage as a function of collector current; typical values

# **Package outline**



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# 9. Packing information

Table 9. Packing methods
The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Package	Description	Packing quantity		
		3000	5000	10000
SOT416	4 mm pitch, 8 mm tape and reel	-115	-	-135
SOT346	4 mm pitch, 8 mm tape and reel	-115	-	-135
SOT883	2 mm pitch, 8 mm tape and reel	-	-	-315
SOT54	bulk, straight leads	-	-412	-
SOT54A	tape and reel, wide pitch	-	-	-116
	tape ammopack, wide pitch	-	-	-126
SOT54 variant	bulk, delta pinning	-	-112	-
SOT23	4 mm pitch, 8 mm tape and reel	-215	-	-235
SOT323	4 mm pitch, 8 mm tape and reel	-115	-	-135
	SOT416 SOT346 SOT883 SOT54 SOT54A SOT54 variant SOT23	SOT416 4 mm pitch, 8 mm tape and reel SOT346 4 mm pitch, 8 mm tape and reel SOT883 2 mm pitch, 8 mm tape and reel SOT54 bulk, straight leads SOT54A tape and reel, wide pitch tape ammopack, wide pitch SOT54 variant bulk, delta pinning SOT23 4 mm pitch, 8 mm tape and reel	3000           SOT416         4 mm pitch, 8 mm tape and reel         -115           SOT346         4 mm pitch, 8 mm tape and reel         -115           SOT883         2 mm pitch, 8 mm tape and reel         -           SOT54         bulk, straight leads         -           SOT54A         tape and reel, wide pitch         -           tape ammopack, wide pitch         -           SOT54 variant         bulk, delta pinning         -           SOT23         4 mm pitch, 8 mm tape and reel         -215	3000         5000           SOT416         4 mm pitch, 8 mm tape and reel         -115         -           SOT346         4 mm pitch, 8 mm tape and reel         -115         -           SOT883         2 mm pitch, 8 mm tape and reel         -         -           SOT54         bulk, straight leads         -         -412           SOT54A         tape and reel, wide pitch         -         -           tape ammopack, wide pitch         -         -           SOT54 variant         bulk, delta pinning         -         -112           SOT23         4 mm pitch, 8 mm tape and reel         -215         -

<sup>[1]</sup> For further information and the availability of packing methods, see Section 12.

# **PDTC123T series**

NPN resistor-equipped transistors; R1 = 2.2 kΩ, R2 = open

# 10. Revision history

### Table 10. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
PDTC123T_SER_1	20060310	Product data sheet	-	-

## 11. Legal information

#### 11.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
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# **Philips Semiconductors**

# **PDTC123T series**

NPN resistor-equipped transistors; R1 = 2.2 kΩ, R2 = open

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