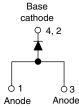


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High Performance Schottky Rectifier, 5.5 A





D-PAK	(TO-252AA)
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†	4, 2
O 1 Anode	○3 Anode

PRODUCT SUMMARY						
Package	D-PAK (TO-252AA)					
I _{F(AV)}	5.5 A					
V _R	30 V					
V _F at I _F	See Electrical table					
I _{RM}	58 mA at 125 °C					
T _J max.	150 °C					
Diode variation	Single die					
E _{AS}	10 mJ					

FEATURES

- Popular D-PAK outline
- Small foot print, surface mountable



- Low forward voltage drop
- High frequency operation
- · Guard ring for enhanced ruggedness and long term reliability
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C

DESCRIPTION

The VS-50WQ03FNPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I _{F(AV)}	Rectangular waveform	5.5	А						
V _{RRM}		30	V						
I _{FSM}	t _p = 5 μs sine	320	А						
V _F	5 A _{pk} , T _J = 125 °C	0.35	V						
T _J	Range	-40 to +150	°C						

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-50WQ03FNPbF	UNITS					
Maximum DC reverse voltage	V_{R}	30	V					
Maximum working peak reverse voltage	V_{RWM}	30	V					

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CONDI	VALUES	UNITS				
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 136 °C	5.5	Α				
Maximum peak one cycle non-repetitive surge current	1	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	320	А			
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	130				
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 2 \text{A}, L = 5 \text{mH}$	10	mJ				
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero Frequency limited by T _J maximum	2.0	Α				



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ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS					
Maximum forward voltage drop See fig. 1		5 A	T _{.1} = 25 °C	0.46				
	V _{FM} ⁽¹⁾	10 A	11=23 0	0.53	V			
	V FM ('')	5 A	T _{.1} = 125 °C	0.35				
		10 A	1j = 125 C	0.46				
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	3	m A			
See fig. 2		T _J = 125 °C	VR = nateu VR	58	- mA			
Threshold voltage	V _{F(TO)}	T - T movimum						
Forward slope resistance	r _t	$T_J = T_J$ maximum	22.22	mΩ				
Typical junction capacitance	C _T	V _R = 5 V _{DC} (test signal ran	590	pF				
Typical series inductance	L _S	Measured lead to lead 5 r	nm from package body	5.0	nH			

Note

 $^{^{(1)}\,}$ Pulse width $<300~\mu s,$ duty cycle <2~%

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		-40 to +150	°C				
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	3.0	°C/W				
Approximate weight			0.3	g				
Approximate weight			0.01	OZ.				
Marking device		Case style D-PAK (similar to TO-252AA)	50WG	03FN				

Note

(1)
$$\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$$
 thermal runaway condition for a diode on its own heatsink

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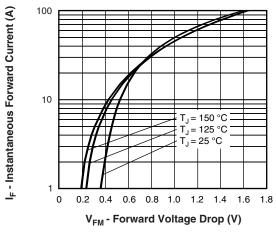


Fig. 1 - Maximum Forward Voltage Drop Characteristics

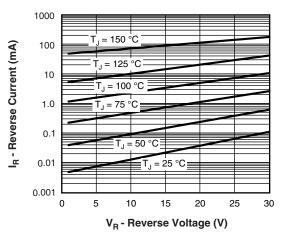


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

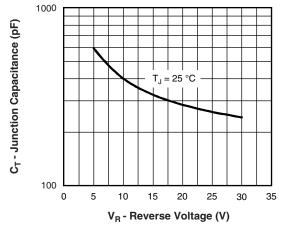


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

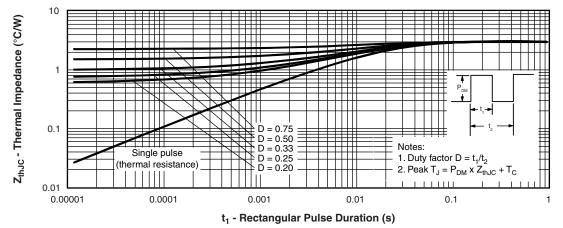


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics





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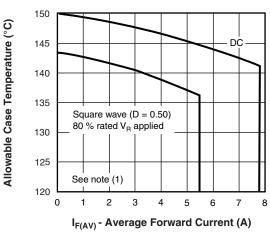


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

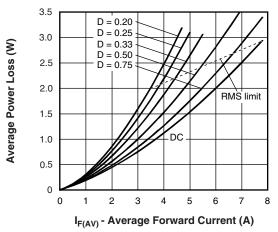


Fig. 6 - Forward Power Loss Characteristics

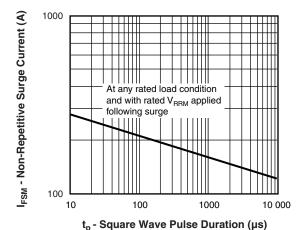


Fig. 7 - Maximum Non-Repetitive Surge Current

Note

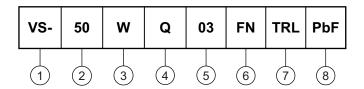
 $^{(1)}$ Formula used: $T_C = T_J$ - (Pd + Pd_{REV}) x R_{th,JC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R



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ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- Current rating (5.5 A)
- Package identifier:

W = D-PAK

- 4 Schottky "Q" series
- 5 Voltage rating (03 = 30 V)
- 6 FN = TO-252AA (D-PAK)
- 7 None = tube (50 pieces)
 - TR = tape and reel
 - TRL = tape and reel (left oriented)
 - TRR = tape and reel (right oriented)
- 8 PbF = lead (Pb)-free

LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95016					
Part marking information	www.vishay.com/doc?95059					
Packaging information	www.vishay.com/doc?95033					
SPICE model	www.vishay.com/doc?95408					



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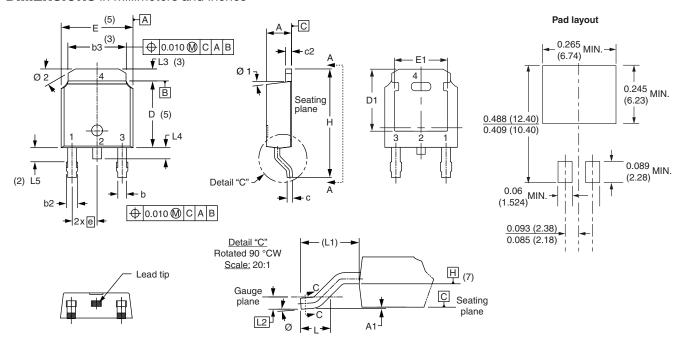
NOTES

3

2

D-PAK (TO-252AA)

DIMENSIONS in millimeters and inches



CVMBOL	MILLIN	MILLIMETERS		HES		NOTES	CVMDOL	MILLIN	IETERS	INC	HES
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		SYMBOL	MIN.	MAX.	MIN.	MAX.
Α	2.18	2.39	0.086	0.094			е	2.29 BSC		0.090	BSC
A1	-	0.13	-	0.005			Н	9.40	10.41	0.370	0.410
b	0.64	0.89	0.025	0.035			L	1.40	1.78	0.055	0.070
b2	0.76	1.14	0.030	0.045		L1 2.74 BSC		2.74 BSC		0.108 REF.	
b3	4.95	5.46	0.195	0.215	3		L2	0.51	BSC	0.020	BSC
С	0.46	0.61	0.018	0.024			L3	0.89	1.27	0.035	0.050
c2	0.46	0.89	0.018	0.035			L4	-	1.02	-	0.040
D	5.97	6.22	0.235	0.245	5		L5	1.14	1.52	0.045	0.060
D1	5.21	-	0.205	-	3		Ø	0°	10°	0°	10°
Е	6.35	6.73	0.250	0.265	5		Ø1	0°	15°	0°	15°
E1	4.32	-	0.170	-	3		Ø2	25°	35°	25°	35°

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- Lead dimension uncontrolled in L5
- Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad
- Section C C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip
- Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- Dimension b1 and c1 applied to base metal only
- (7) Datum A and B to be determined at datum plane H
- Outline conforms to JEDEC outline TO-252AA



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