

Lead-free Green DMTH4014LDVWQ 40V +175°C DUAL N-CHANNEL ENHANCEMENT MODE MOSFET POWERDI3333-8

Product Summary

BV _{DSS}	Rds(on) Max	I⊵ Max Tc = +25°C
40V	15mΩ @ V _{GS} = 10V	27.5A
	$25m\Omega @ V_{GS} = 4.5V$	22.0A

Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Wireless Charging
- DC-DC Converters
- Power Management

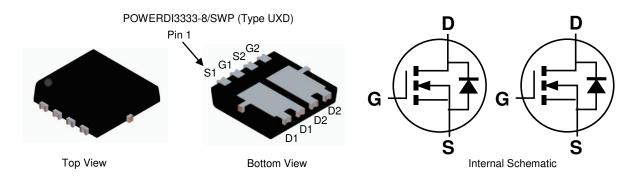
Features and Benefits

- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching (UIS) Test in Production Ensures More Reliable and Robust End Application
- Low RDS(ON) Ensures On-State Losses are Minimized
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Wettable Flank for Improved Optical Inspection
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DMTH4014LDVWQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/guality/product-definitions/

Mechanical Data

- Case: POWERDI[®]3333-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (2)
- Weight: 0.072 grams (Approximate)



Ordering Information (Note 4)

Part Number	Case	Packaging
DMTH4014LDVWQ-7	POWERDI3333-8/SWP (Type UXD)	2,000/Tape & Reel
DMTH4014LDVWQ-13	POWERDI3333-8/SWP (Type UXD)	3,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. 2. See http://www.diodes.com/quality/lead_free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green"

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



 $\frac{\overline{YY}WW}{\overline{YY}} = \text{Date Code Marking}$ $\frac{\overline{YY}}{\overline{YY}} = \text{Last Two Digits of Year (ex: 21 = 2021)}$ WW = Week Code (01 to 53)

H4D = Product Type Marking Code

and Lead-free.



Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit			
Drain-Source Voltage	V _{DSS}	40	V			
Gate-Source Voltage	Vgss	±20	V			
		Tc = +25°C	1	27.5	^	
Continuous Drain Current, V _{GS} = 10V (Note 6)		Tc = +100°C	ID	19.5	A	
Continuous Drain Current Mar 10M (Nata C)	Steady	TA = +25°C	- Io	10.2	А	
Continuous Drain Current, V _{GS} = 10V (Note 6)	State	T _A = +100°C		7.2		
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	ldм	110	A			
Maximum Continuous Body Diode Forward Current (Net	ls	3.7	A			
Pulsed Body Diode Forward Current (10µs Pulse, Duty	I _{SM}	110	A			
Avalanche Current, L = 0.1mH			las	19.8	A	
Avalanche Energy, L = 0.1mH			Eas	19.6	mJ	

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	PD	1.16	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Reja	129	°C/W
Total Power Dissipation (Note 6)	TA = +25°C	PD	2.6	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Reja	57.5	°C/W
Thermal Resistance, Junction to Case (Note 6)		Rejc	7.8	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +175	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

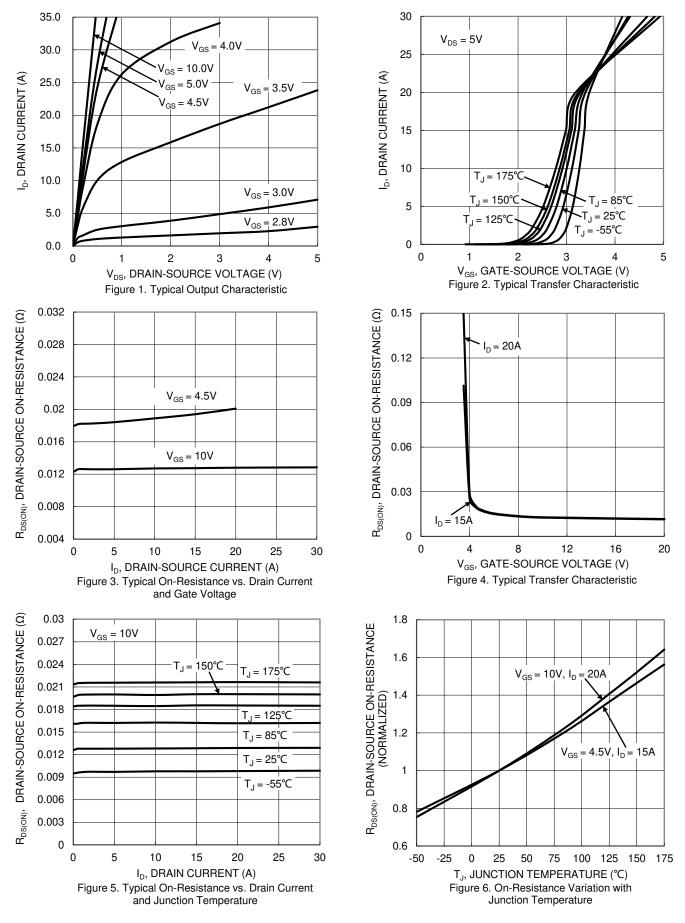
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Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)				1			
Drain-Source Breakdown Voltage	BV _{DSS}	40	—		V	$V_{GS} = 0V, I_D = 1mA$	
Zero Gate Voltage Drain Current	IDSS	_	—	1	μΑ	$V_{DS} = 32V, V_{GS} = 0V$	
Gate-Source Leakage	lgss	—	—	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)					-		
Gate Threshold Voltage	VGS(TH)	1	_	3	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	
Static Drain-Source On-Resistance	Design	—	12.8	15	mΩ	$V_{GS} = 10V, I_D = 20A$	
Static Drain-Source On-Resistance	RDS(ON)	_	19.4	25		V _{GS} = 4.5V, I _D = 15A	
Diode Forward Voltage	Vsd	_	1.0	1.2	V	V _{GS} = 0V, I _S = 20A	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	—	750	-	pF		
Output Capacitance	Coss	_	225	—	pF	VDS = 20V, VGS = 0V, f = 1MHz	
Reverse Transfer Capacitance	Crss	_	21	—	pF		
Gate Resistance	Rg	_	1.1	—	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	5.7	—	nC		
Total Gate Charge (V _{GS} = 10V)	Qg	_	11.2	—	nC		
Gate-Source Charge	Qgs	_	2.0	—	nC	$V_{DS} = 20V, I_D = 20A$	
Gate-Drain Charge	Qgd	_	2.2	—	nC		
Turn-On Delay Time	t _{D(ON)}	_	3.5	_	ns		
Turn-On Rise Time	tR		4.6	_	ns	$V_{GS} = 10V, V_{DD} = 20V,$	
Turn-Off Delay Time	tD(OFF)		12.4	—	ns	R _g = 1.6Ω, I _D = 20A	
Turn-Off Fall Time	tF	_	4.9	_	ns	7	
Body Diode Reverse Recovery Time	trr	_	11.3	_	ns	L 454 - 1/-1 4004/-	
Body Diode Reverse Recovery Charge	Qrr	_	9.5	—	nC	I _F = 15A, di/dt = 400A/μs	

Notes:

Device mounted on FR-4 PCB, with minimum recommended pad layout, single sided.
Device mounted on FR-4 substrate PCB, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to product testing.



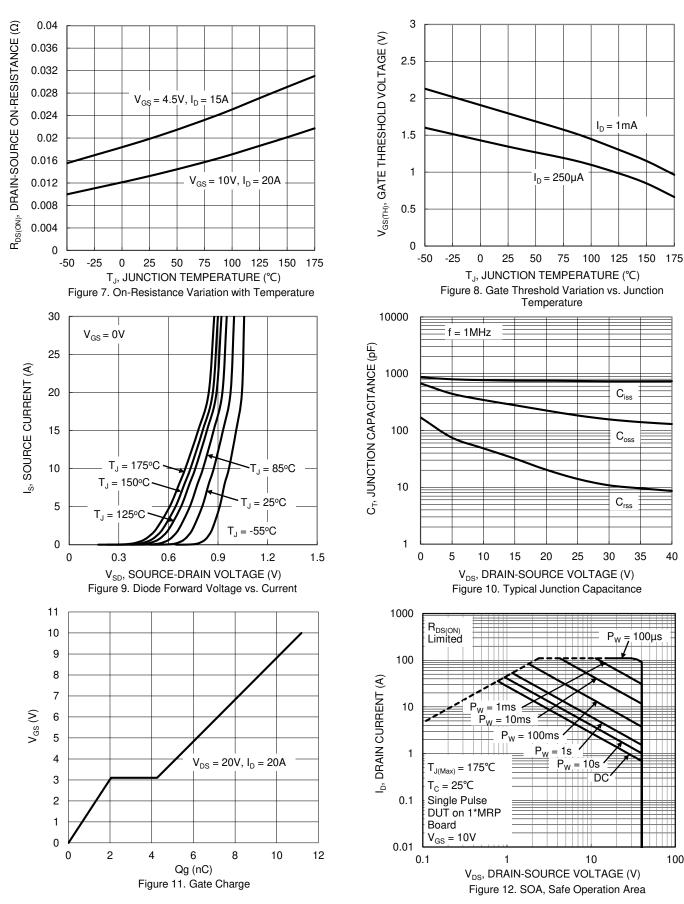
DMTH4014LDVWQ



DMTH4014LDVWQ Document number: DS42792 Rev. 2 - 2



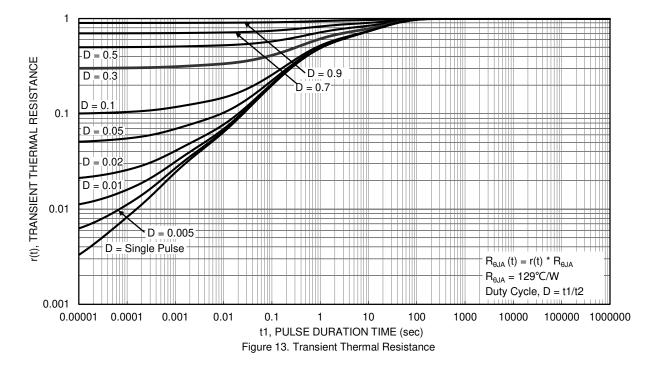
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100

40

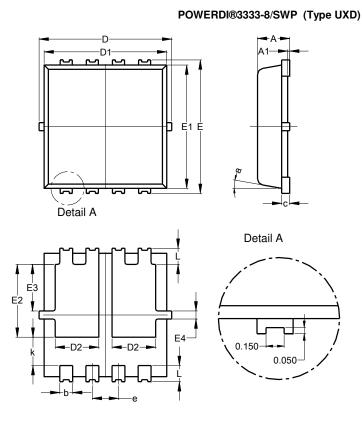






Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

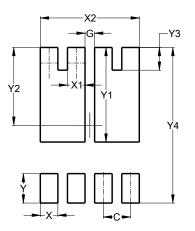


POWERDI®3333-8/SWP					
(Type UXD)					
Dim	Min	Max	Тур		
Α	0.75	0.85	0.80		
A 1	0.00	0.05	-		
b	0.25	0.40	0.32		
С	0.10	0.25	0.15		
D	3.20	3.40	3.30		
D1	2.95	3.15	3.05		
D2	1.00	1.20	1.10		
Ε	3.20	3.40	3.30		
E1	2.95	3.15	3.05		
E2	1.60	2.00	1.80		
E3	0.95	1.35	1.15		
E4	0.10	0.30	0.20		
е	-	_	0.65		
L	0.30	0.50	0.40		
k	0.50	0.90	0.70		
а	0°	12°	10°		
All I	All Dimensions in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

POWERDI®3333-8/SWP (Type UXD)



Dimensions	Value (in mm)
С	0.650
G	0.230
Х	0.420
X1	0.420
X2	2.370
Y	0.700
Y1	2.250
Y2	1.850
Y3	0.540
Y4	3.700



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