

TO-220



**Pin Definition:**

1. Gate
2. Drain
3. Source

**PRODUCT SUMMARY**

V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (mΩ)	I <sub>D</sub> (A)
75	4.2 @ V <sub>GS</sub> =10V	190

**Features**

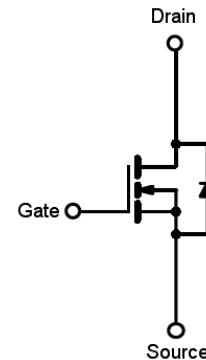
- Advanced Trench Technology
- Low R<sub>DS(ON)</sub> 4.2mΩ (Max.)
- Low gate charge typical @ 160nC (Typ.)
- Low Crss typical @ 300pF (Typ.)

**Ordering Information**

Part No.	Package	Packing
TSM190N08CZ C0G	TO-220	50pcs / Tube

Note: "G" denote for Halogen Free Product.

**Block Diagram**



N-Channel MOSFET

**Absolute Maximum Rating** (Ta = 25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	75	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Continuous Drain Current	I <sub>D</sub>	T <sub>C</sub> =25°C	190
		T <sub>C</sub> =70°C	150
		T <sub>A</sub> =25°C	17
		T <sub>A</sub> =70°C	14
Drain Current-Pulsed Note 1	I <sub>DM</sub>	600	A
Avalanche Current, L=0.3mH	I <sub>AS</sub> , I <sub>AR</sub>	113	A
Avalanche Energy, L=0.3mH	E <sub>AS</sub> , E <sub>AR</sub>	1900	mJ
Maximum Power Dissipation	P <sub>D</sub>	T <sub>C</sub> =25°C	250
		T <sub>C</sub> =70°C	160
		T <sub>A</sub> =25°C	2
		T <sub>A</sub> =70°C	1.3
Storage Temperature Range	T <sub>STG</sub>	-55 to +150	°C
Operating Junction Temperature Range	T <sub>J</sub>	-55 to +150	°C

\* Limited by maximum junction temperature

**Thermal Performance**

Parameter	Symbol	Limit	Unit
Thermal Resistance - Junction to Case	Rθ <sub>JC</sub>	0.5	°C/W
Thermal Resistance - Junction to Ambient	Rθ <sub>JA</sub>	62.5	°C/W

Notes: Surface mounted on FR4 board t ≤ 10sec

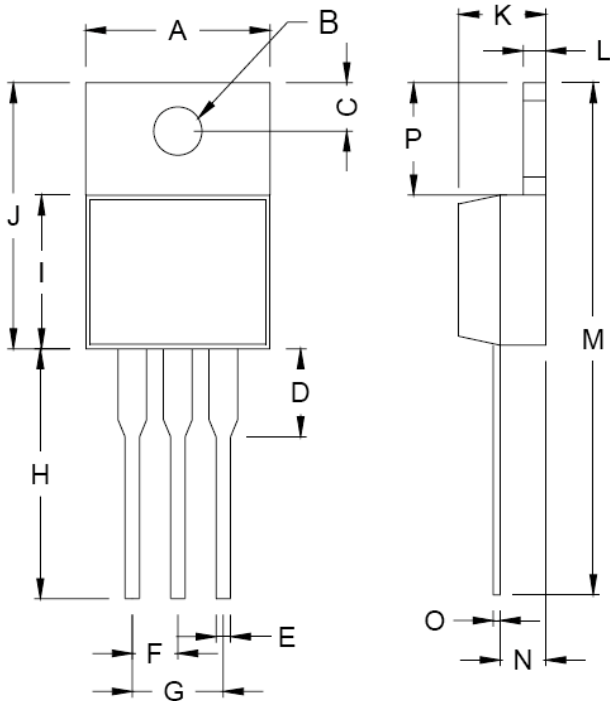
### Electrical Specifications (Ta = 25°C unless otherwise noted)

Parameter	Conditions	Symbol	Min	Typ	Max	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A$	$BV_{DSS}$	75	--	--	V
Drain-Source On-State Resistance	$V_{GS} = 10V, I_D = 90A$	$R_{DS(ON)}$	--	3.4	4.2	mΩ
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu A$	$V_{GS(TH)}$	2	3	4	V
Zero Gate Voltage Drain Current	$V_{DS} = 75V, V_{GS} = 0V$	$I_{DSS}$	--	--	1	μA
Gate Body Leakage	$V_{GS} = \pm 25V, V_{DS} = 0V$	$I_{GSS}$	--	--	±100	nA
<b>Dynamic</b>						
Total Gate Charge	$V_{DS} = 30V, I_D = 90A,$ $V_{GS} = 10V$	$Q_g$	--	160	--	nC
Gate-Source Charge		$Q_{gs}$	--	35	--	
Gate-Drain Charge		$Q_{gd}$	--	40	--	
Input Capacitance	$V_{DS} = 30V, V_{GS} = 0V,$ $f = 1.0MHz$	$C_{iss}$	--	8600	--	pF
Output Capacitance		$C_{oss}$	--	780	--	
Reverse Transfer Capacitance		$C_{rss}$	--	300	--	
<b>Switching</b>						
Turn-On Delay Time	$V_{GS} = 10V, V_{DS} = 30V,$ $R_G = 3.3\Omega$	$t_{d(on)}$	--	25	--	nS
Turn-On Rise Time		$t_r$	--	40	--	
Turn-Off Delay Time		$t_{d(off)}$	--	85	--	
Turn-Off Fall Time		$t_f$	--	45	--	
<b>Drain-Source Diode Characteristics and Maximum Rating</b>						
Drain-Source Diode Forward Voltage	$V_{GS}=0V, I_S=90A$	$V_{SD}$	-	0.8	1.3	V
Reverse Recovery Time	$I_S = 90A, T_J=25^\circ C$ $di/dt = 100A/\mu s$	$t_{fr}$		70		nS
Reverse Recovery Charge		$Q_{fr}$		115		nC

#### Notes:

1. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
2.  $R_{\theta JA}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins.  $R_{\theta JC}$  is guaranteed by design while  $R_{\theta CA}$  is determined by the user's board design.  $R_{\theta JA}$  shown below for single device operation on FR-4 in still air

### TO-220 Mechanical Drawing



TO-220 DIMENSION				
DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	10.000	10.500	0.394	0.413
B	3.740	3.910	0.147	0.154
C	2.440	2.940	0.096	0.116
D	-	6.350	-	0.250
E	0.381	1.106	0.015	0.040
F	2.345	2.715	0.092	0.058
G	4.690	5.430	0.092	0.107
H	12.700	14.732	0.500	0.581
J	14.224	16.510	0.560	0.650
K	3.556	4.826	0.140	0.190
L	0.508	1.397	0.020	0.055
M	27.700	29.620	1.060	1.230
N	2.032	2.921	0.080	0.115
O	0.255	0.610	0.010	0.024
P	5.842	6.858	0.230	0.270

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