


SANYO Semiconductors

DATA SHEET

An ON Semiconductor Company

LV5028TT — Bi-CMOS IC LED Driver IC

Overview

LV5028TT is a High Voltage LED drive controller which drives LED current up to 3A with external MOSFET. LV5028TT is realized very simple LED circuits with a few external parts. It corresponds various wide dimming controls including the TRIAC dimming control.

Functions

- High Voltage LED Controller
- Various Dimming Control
 - TRIAC & Analog input
- Switching frequency 50kHz
- Low noise switching system
 - 5 stages skip mode Frequency
 - Soft driving
- Selectable Reference voltage
 - Internal 0.605V & External input voltage
- Short Protection Circuit
- Built-in stabilized function

Specifications

Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum Input voltage	V _{IN} max		-0.3 to 42	V
REF_OUT, REF_IN, CS, ACS			-0.3 to 7	V
OUT	V _{OUT_abs}		-0.3 to 42	V
OUT2	V _{OUT2_abs}		-0.3 to 42	V
Allowable power dissipation	P _d max	With specified board*	1.0	W
Junction temperature	T _j max		150	°C
Operating temperature	T _{opr}		-30 to +125	°C
Storage temperature	T _{stg}		-40 to +150	°C

*Specified board: 58.0×54.0×1.6mm (glass epoxy board)

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LV5028TT

Recommended Operating Conditions at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Input voltage	V _{IN}		8.5 to 24	V

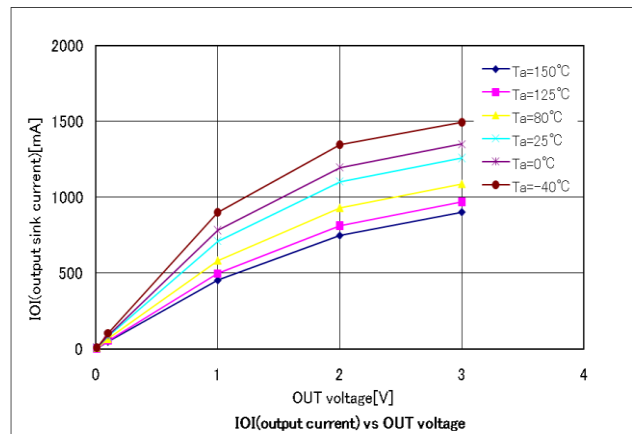
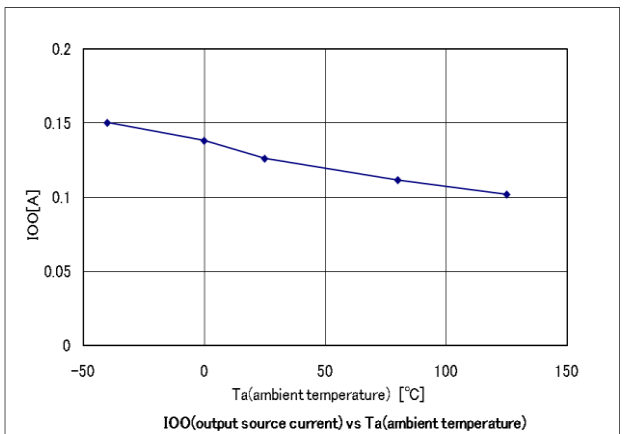
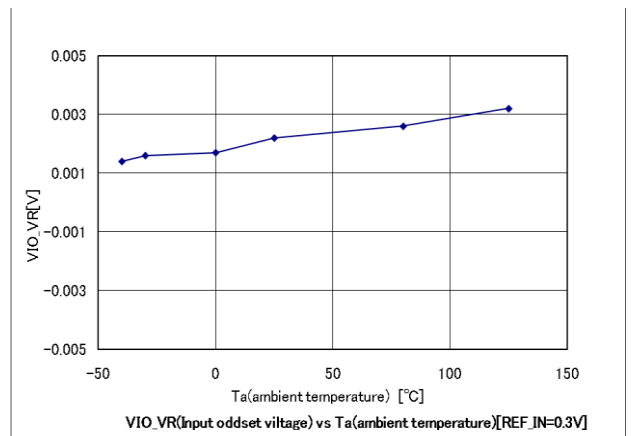
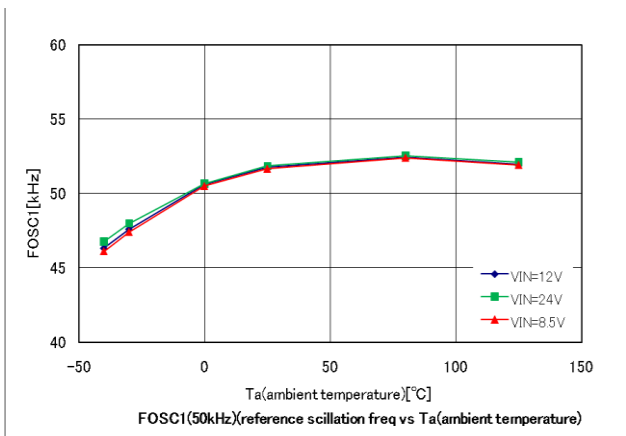
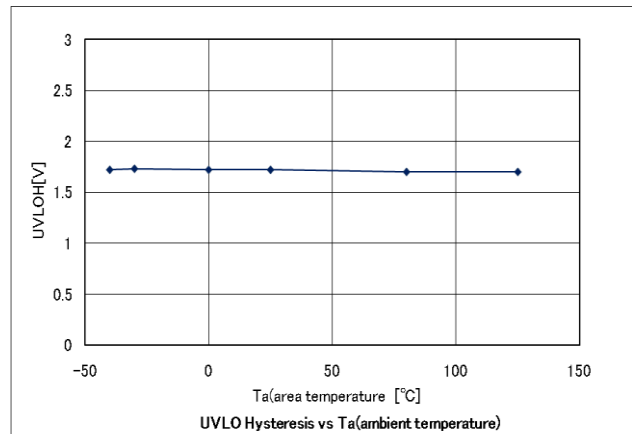
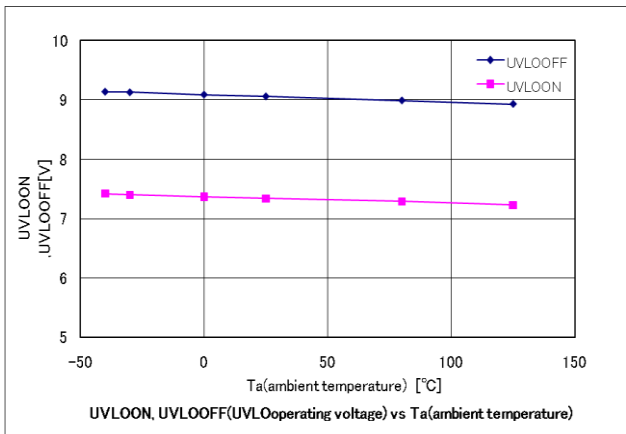
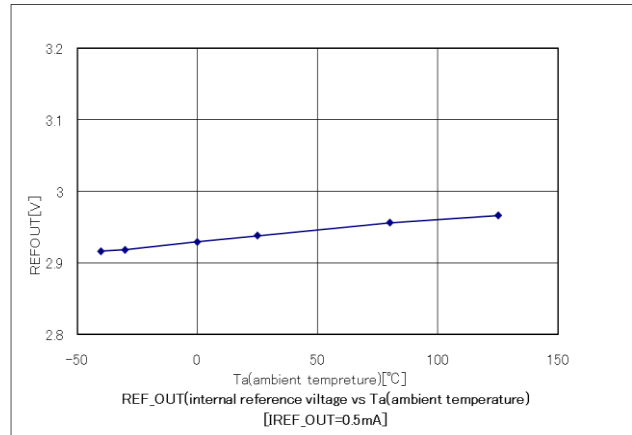
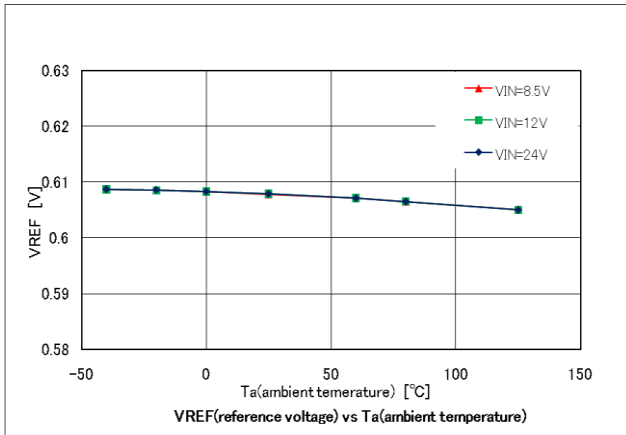
Electrical Characteristics at Ta = 25°C, V_{IN} = 12V, unless otherwise specified.

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Reference Voltage block						
Built-in Reference Voltage	VREF		0.585	0.605	0.625	V
VREF V _{IN} line regulation	VREF_LN	V _{IN} = 8.5 to 24V		±0.5		%
Reference Output Voltage	REFOUT			3.0		V
-Maximum load	REFOUT_MAX		0.5			mA
-equivalent output impedance	REFOUT_RO			10		ohm
Under Voltage Lockout						
Operation Start Input Voltage	UVLOON		8	9	10	V
Operation Stop Input Voltage	UVLOOFF		6.3	7.3	8.3	V
Hysteresis Voltage	UVLOH			1.7		V
Oscillation						
Frequency	FOSC		40	50	60	kHz
Maximum ON duty	MAXDuty			93		%
Comparator						
Input offset Voltage (Between CS and VREF)	V _{IO_VR}			1	10	mV
Input offset voltage (Between CS and REFOUT)	V _{IO_RI}			1	10	mV
Input current	I _{IOCS}			160		nA
	I _{IOREF}			80		nA
CS pin max voltage	VOM				1	V
malfunction prevention mask time	TMSK			150		ns
Thermal protection Circuit						
Thermal shutdown temperature	TSD	*Design guarantee		165		°C
Thermal shutdown hysteresis	ΔTSD	*Design guarantee		30		°C
Drive Circuit						
OUT sink current	I _{OI}		500	1000		mA
OUT source current	I _{OO}			120		mA
Minimum On time	TMIN			200	300	ns
TRIAC Stabilization Circuit						
Threshold of OUT2	VACS	OUT2=High [less than right record]	2.8	3.0	3.2	V
OUT2 sink current	I _{O2I}	V _{IN} =12V, OUT2=6V		0.6		mA
OUT2 source current	I _{O2O}	V _{IN} =12V, OUT2=6V		0.6		mA
V_{IN} current						
UVLO mode V _{IN} current	I _{CCOFF}	V _{IN} <UVLOON		80	120	μA
Normal mode V _{IN} current	I _{CCON}	V _{IN} >UVLOON, OUT = OPEN		0.6		mA
V_{IN} Over Voltage Protection Circuit						
V _{IN} over voltage protection voltage	V _{INOVP}		24	27	30	V
V _{IN} Current at OVP	I _{INOVP}	V _{IN} =30V	0.7	1.0	1.5	mA
CS terminal abnormal sensing circuit						
Abnormal sensing voltage	CSOCP			1.9		V

*: Design guarantee (value guaranteed by design and not tested before shipment)

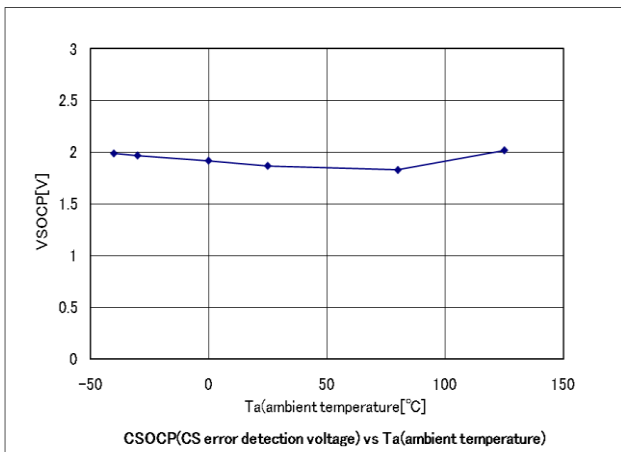
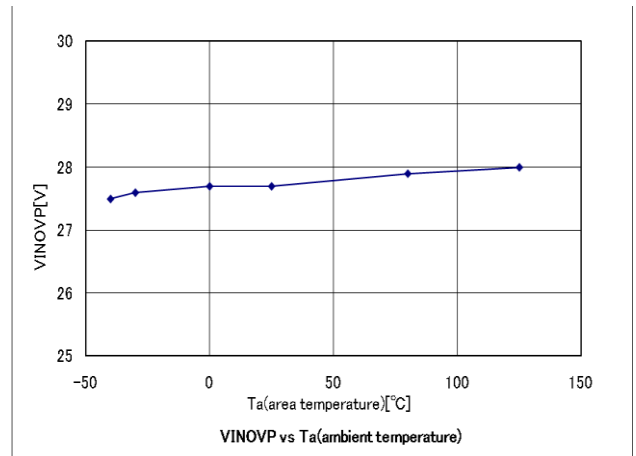
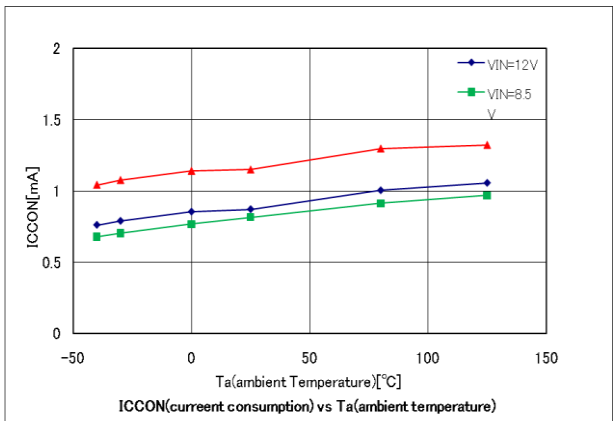
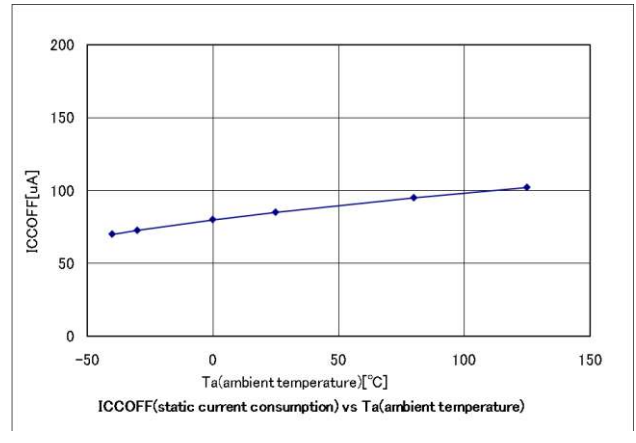
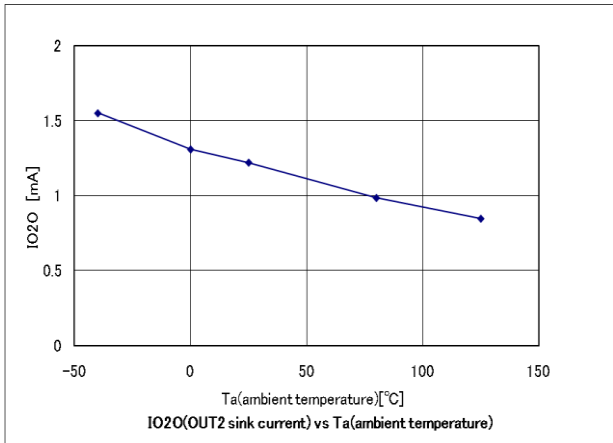
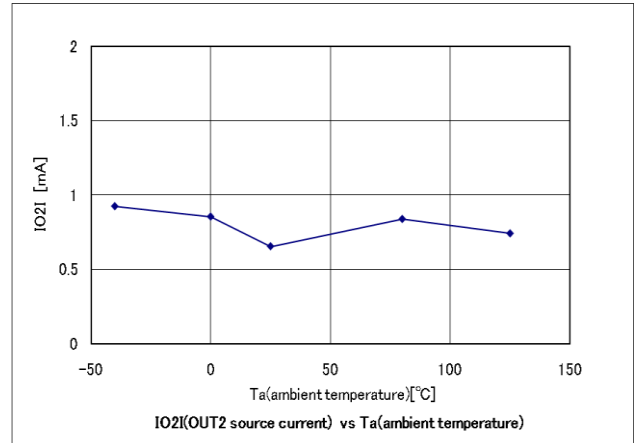
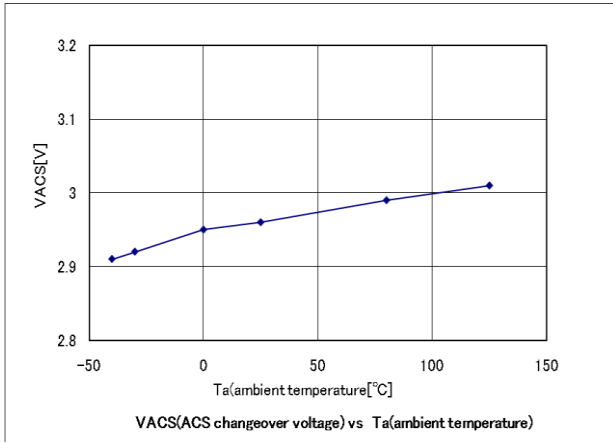
LV5028TT

TYPICAL CHARACTERISTICS



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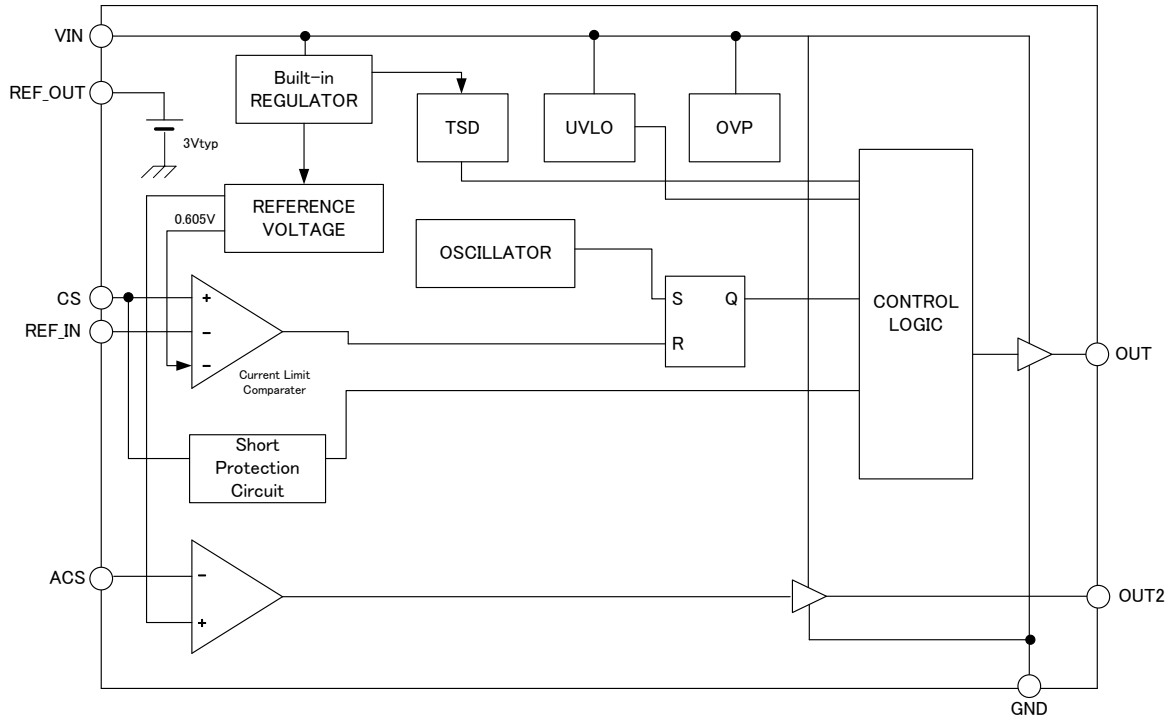
TYPICAL CHARACTERISTICS



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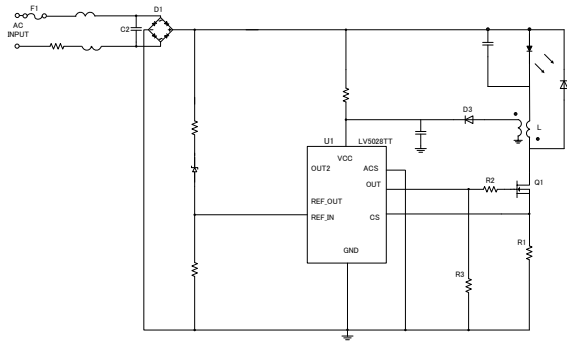
TYPICAL CHARACTERISTICS

Block Diagram

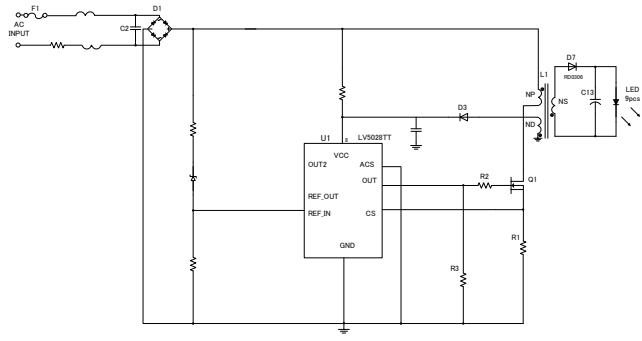


Sample Application Circuit

Non isolation



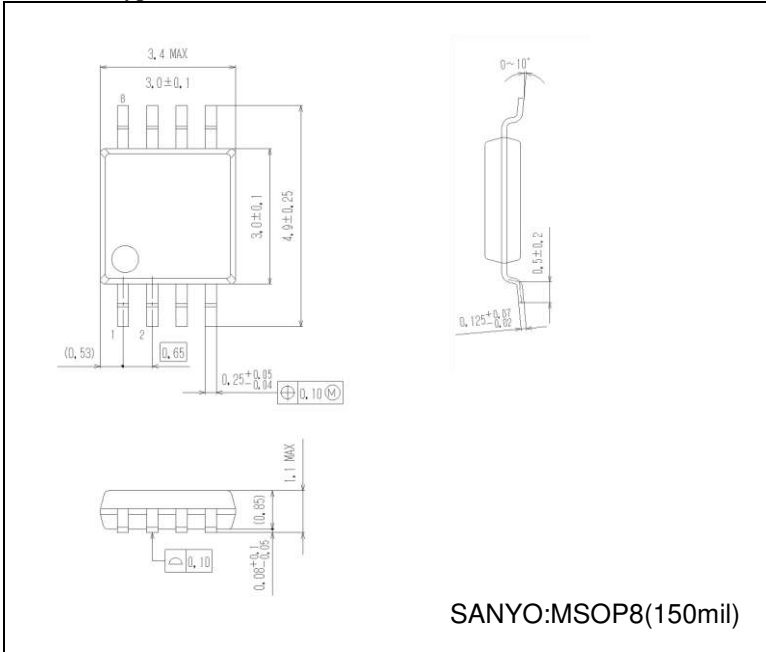
Isolation



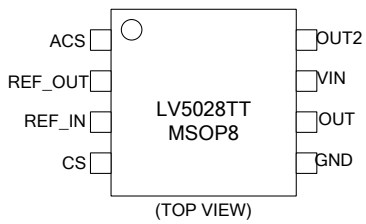
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Package Dimensions

unit: mm (typ)



Pin Assignment



LV5028TT

Pin Functions

pin No	Pin Name	Pin Function	Equivalent Circuit
1	ACS	ACS pin senses AC Voltage. If this function isn't used, please connect GND.	
2	REF_OUT	Built-in 3V Regulate out Pin. If this function isn't used, please connect to nothing.	
3	REF_IN	External LED current Limit Setting pin. If less than VREF (0.61V) voltage is input, Peak current value is used at the input voltage. If more than REF_IN voltage is input, it is done at VREF voltage. If this function isn't used, please connect nothing.	
4	CS	LED current sensing in. If this terminal voltage exceeds VREF, external FET is OFF. And if the voltage of the terminal exceeds 1.9V, LV5028TT turns to latch-off mode.	
5	GND	GND pin.	
6	OUT	Driving the external FET Gate Pin.	
7	VIN	Power supply pin. Operation : $VIN > UVLOON$ Stop: $VIN < UVLOOFF$ Switching Stop : $VIN > VINOVP$	
8	OUT2	This pin drive the FET which is stabilized the TRIAC dimming application. If ACS is less than 3V, OUT2 turn High voltage. If this function isn't used, please connect nothing.	

Relation ship between VREF and CS pin voltage

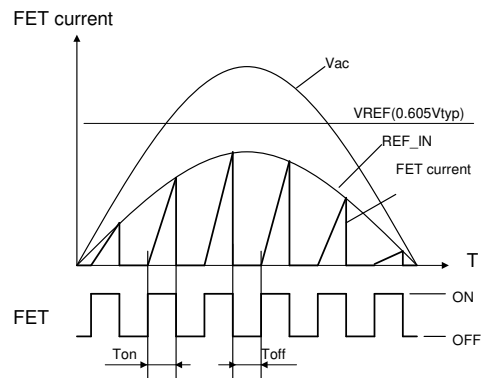
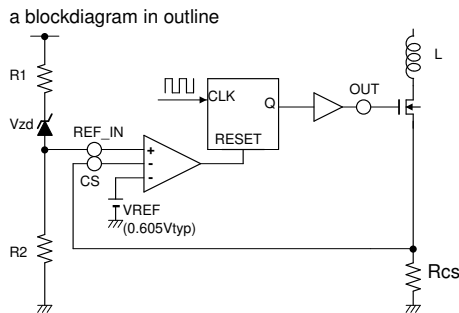
Relation ship between REF_IN and CS pin voltage(Power Factor Crrection(PFC))

The output current value is the average of the current value that flows during one cycle. The current value that flows into coil is a triangular wave shown in the figure below. Make sure to set Ipk so that (average of current value at one cycle) is equal to (LED current value).Ipk is set by the relationship between REF_IN voltage and Rcs voltage. This relationship make Power Factor Correction (PFC).Therefore, it is available to make LED current a sine curve.

Setting Zener voltage

Vzd depend on LED voltage (Vf). Choose Zener diode around Vf (LED voltage).When VAC voltage is lower than Vf, LED operation is not normal. Using Zener diode prevents incorrect operating during VAC voltage lower than Vf. In detail, refer to [LED current and inductance setting]

In case of REF_IN pin open, this error amplifier negative input(-) is under control of internal VREF voltage(0.605Vtyp).



$$I_{pk} = \frac{(V_{ac} - V_{zd}) \times \frac{R_2}{R_1 + R_2}}{R_{cs}}$$

Ipk: peak inductor current

Vf: LED forward voltage drop

Vac: effective value,R.M.S value

VREF: Built-in reference voltage (0.605V)

VREF_IN:REF_IN voltage(6 pin)

Rs: External sense resistor

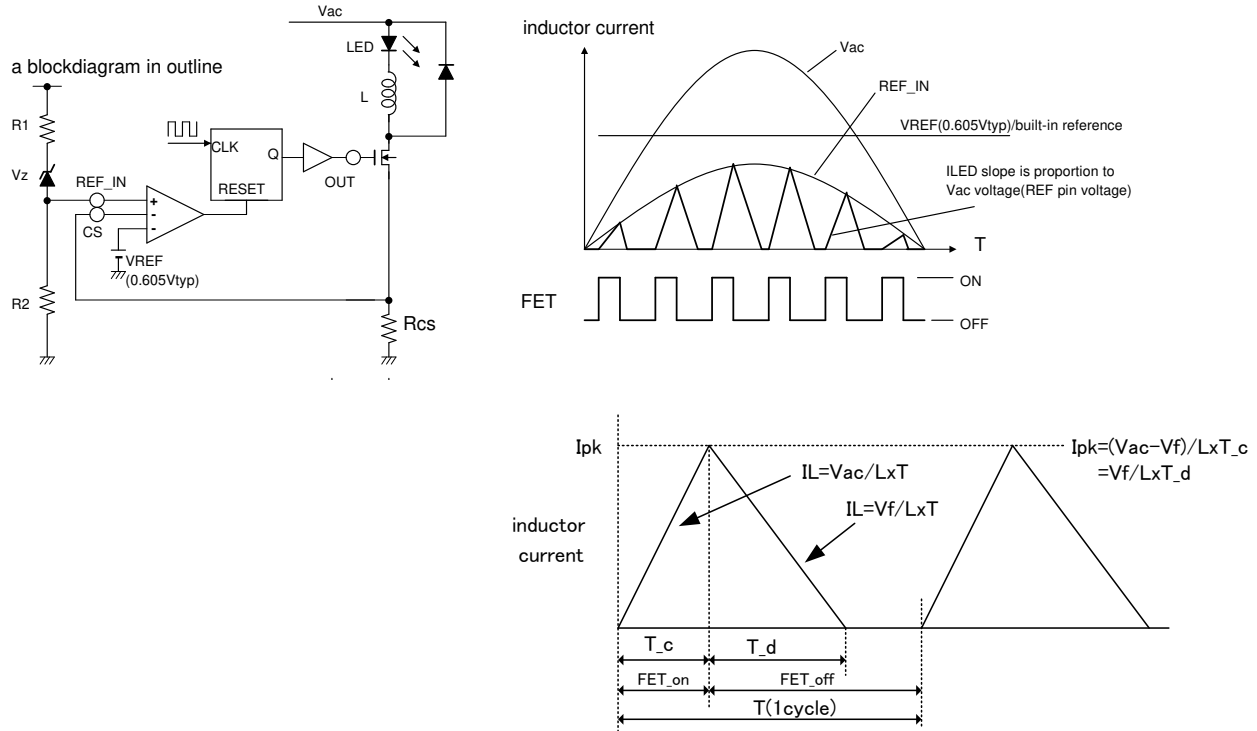
Vzd:Zener diode voltage(REF_IN pin)

LED current and inductance setting

It is available to use both no-isolation and isolation applications.

(For non-isolation application)

The output current value is the average of the inductor current value that flows during one cycle. The current value that flows into inductor is a triangular wave shown in the figure below. Make sure to set I_{pk} so that (average of inductor current value at one cycle) is equal to (LED current value).



Given that the period when current flows into coil is

$$\text{DutyI} = \frac{T_c + T_d}{T}$$

$$I_{pk} \times \frac{1}{2} \times (\text{DutyI} \times T) / T = I_{LED}$$

$$I_{pk} = \frac{2 \times I_{LED}}{\text{DutyI}} \quad (1) \quad \text{since} \quad I_{pk} = \frac{V_{REF_IN}}{R_{cs}}$$

$$R_{cs} = \frac{V_{REF_IN}}{I_{pk}} = \frac{\text{DutyI} \times V_{REF_IN}}{2 I_{LED}} \quad (2)$$

I_{pk} : peak inductor current

V_f : LED forward voltage drop

V_{ac} : effective value (R.M.S value)

V_{REF} : Built-in reference voltage (0.605V)

V_{REF_IN} : REF_IN voltage (6 pin)

R_s : External sense resistor

V_z : Zener diode voltage (REF_IN pin)

Since formula for LED current is different between on period and off period as shown above,

$$I_{pk} = \frac{V_{ac} - V_f}{L} \times T_c = \frac{V_f}{L} \times T_d \quad (3)$$

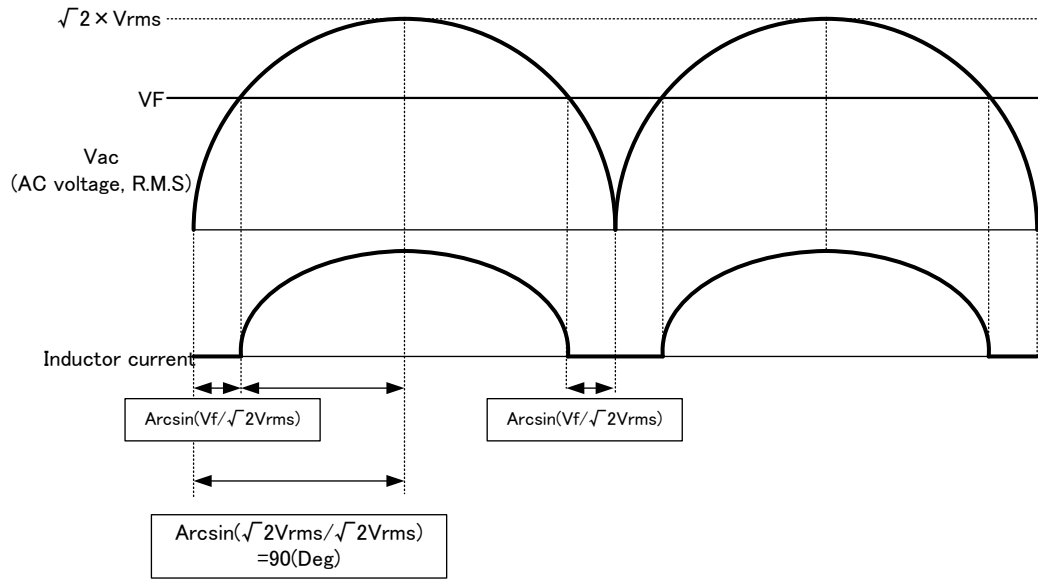
$$\text{Since } T_c + T_d = \text{DutyI} \times T, \quad T_c = \text{DutyI} \times T - T_d \quad (4)$$

$$\text{Based on the result of (3) and (4), } T_d = \text{DutyI} \times T \times \frac{V_{ac} - V_f}{V_{ac}} \quad (5)$$

To obtain L from the equation (1), (3), (5),

$$L = \frac{V_f \times \text{DutyI}}{2 \times I_{LED}} \times \text{DutyI} \times T \times \frac{V_{ac} - V_f}{V_{ac}} = \frac{V_f}{2 \times I_{LED}} \times \frac{1}{f_{osc}} \times \frac{V_{ac} - V_f}{V_{ac}} \times (\text{DutyI})^2 \quad (6)$$

Since LED and inductor are connected in serial in non-isolation mode, LED current flows only when AC voltage exceed V_f .



Given that the ratio of inductor current to AC input is DutyAC.

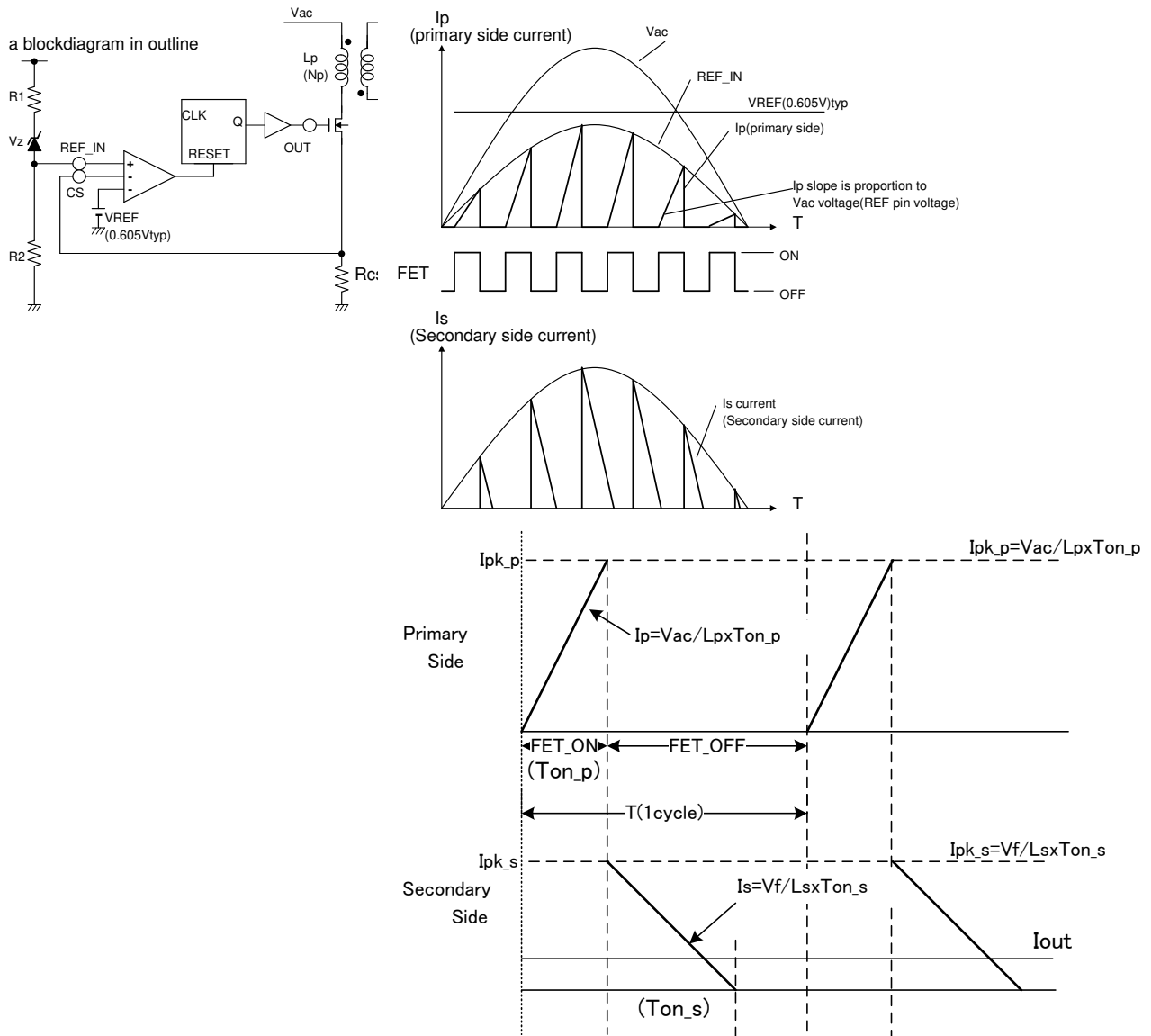
$$DutyAC = \frac{90 - \arcsin\left(\frac{V_f}{\sqrt{2} V_{rms}}\right)}{90}$$

Since the period when the inductor current flows are limited by DutyAC, the formula (6) is represented as follows:

$$L = \frac{V_f}{2 \times I_{LED}} \times \frac{1}{f_{osc}} \times \frac{V_{ac} - V_f}{V_{IN}} \times (DutyI)^2 \times \left(\frac{90 - \arcsin\left(\frac{V_f}{\sqrt{2} V_{rms}}\right)}{90} \right)^2 \quad (7)$$

(for Isolation application)

Using the circuit diagram below, the wave form of the current that flows to N_p and N_s is as follows.
Current waveform flows to primary side and secondary.



[Inductance L_p of primary side and sense resistor R_s]

If a peak current flow to transformer is represented as I_{pk_p} , the power (P_{in}) charged to the transformer on primary side can be represented as:

$$P_{in} = \frac{1}{2} \times L_p \times (I_{pk_p})^2 \times f_{osc} \quad (11)$$

$$\therefore I_{pk_p} = \frac{V_{ac}}{L_p} \times T_{on_p} \quad (12)$$

$$\therefore L_p = \frac{V_{ac}^2 \times T_{on_p}^2 \times f_{osc}}{2 \times P_{in}} = \frac{V_{ac}^2 \times D_{on_p}^2}{2 \times P_{in} \times f_{osc}} \quad (13)$$

$$(D_{on_p} = \frac{T_{on_p}}{T} = T_{on_p} \times f_{osc})$$

To substitute the following to the formula below,

$$\therefore \eta = \frac{P_{out}}{P_{in}} \quad (14)$$

$$\therefore L_p = \frac{V_{ac}^2 \times T_{on_p}^2 \times f_{osc} \times \eta}{2 \times P_{out}} = \frac{V_{ac}^2 \times D_{on}^2 \times \eta}{2 \times P_{out} \times f_{osc}} \quad (15)$$

Sense resistor is obtained as follows.

$$R_s = \frac{V_{ref}}{I_{pk_p}} = \frac{V_{REF_IN} \times L_p}{V_{ac} \times T_{on_p}} = \frac{V_{REF_IN} \times L_p}{V_{sc} \times Don_p \times T} \quad (16)$$

[Inductance L_s of secondary side]

Since output current I_{out} is the average value of current flows to transformer of secondary side

$$I_{out} = I_{pk_s} \times \frac{T_{on_s}}{T} \times \frac{1}{2} = \frac{I_{pk_s} \times Don_s}{2} \quad (Don_s = \frac{T_{on_s}}{T} = T_{on_s} \times f_{osc}) \quad (17)$$

$$I_{pk_s} = \frac{V_{out}}{L_s} \times T_{on_s} = \frac{V_{out}}{L_s} \times \frac{Don_s}{f} \quad (18)$$

$$L_s = \frac{V_{out} \times T \times Don_s^2}{2 \times I_{out}} = \frac{V_{out} \times Don_s^2}{2 \times I_{out} \times f_{osc}} = \frac{V_{out}^2 \times Don_s^2}{2 \times P_{out} \times f_{osc}} \quad (19)$$

Calculation of the ratio of transformer coil on primary side and secondary side

Since ratio and inductance of transformer coil is

$$\frac{N_s}{N_p} = \frac{\sqrt{L_s}}{\sqrt{L_p}} \quad (20)$$

substituted equations (15), (19) for (20)

$$\therefore \frac{N_p}{N_s} = \frac{V_{dc \min}}{V_{out}} \times \sqrt{\eta} \times \frac{Don_p}{Don_s} \quad (21)$$

Calculation of transformer coil on primary side and secondary side

$$N = \frac{V_{ac} \times 10^8}{2 \times \Delta B \times A_e \times f} \quad (22)$$

ΔB : variation range of core flux density [Gauss]

A_e : core section area [cm²]

To use Al (L value at 100T),

$$N = \sqrt{\frac{L}{Al}} \times 10^2 \quad (23)$$

L : inductance [uH]

Al: L value at 100T [uH/N²]

lg (Air gap) is obtained as follows:

$$lg = \frac{\mu_r \mu_0 N^2 A_e 10^2}{L} \quad (24)$$

μ_r : relative magnetic permeability, $\mu_r = 1$

μ_0 : vacuum magnetic permeability $\mu_0 = 4\pi \times 10^{-7}$

N: turn count [T]

A_e : core section area [m²]

L: inductance [H]

Bleeder current cuircuit for TRIAC dimmer

1. Operating voltage setting

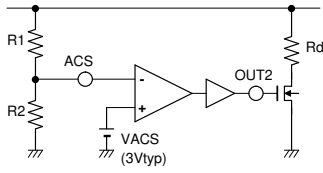
ACS pin voltage set operating voltage at OUT2. ACS pin threshold voltage is 3Vtyp.
 OUT2 operating voltage is set by R1 and R2. R1 and R2 is determined below.

$$ACS = Vac \times \frac{R2}{R1 + R2}$$

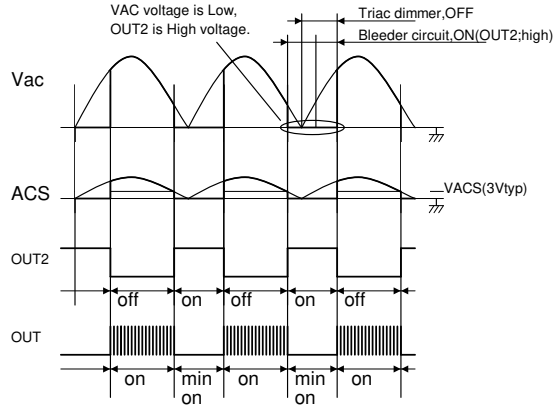
2. Bleeder current setting

Rd set hold current at Triac dimmer.
 Bleeder current is set at Rd depending on Triac dimmer.

a blockdiagram in outline



a blockdiagram in outline



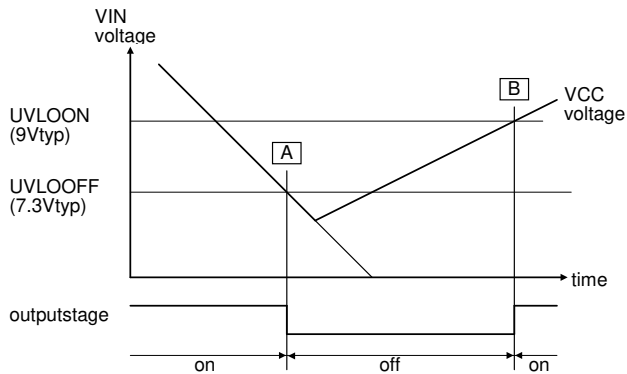
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Description of operating protection function

	tilte	outline	monitor point	note
1	UVLO	Under Voltage Lock Out	VIN voltage	
2	OGP	Over Current Protection	CS voltage	equivalent FET current
3	OVP	Over Voltage Protection	VIN voltage	
4	OTP (TSD)	Over Temperature Protection (Thermal Shut Down)	PN Junction temperature	

1.UVLO(Under Voltage Lock Out)

If VIN voltage is 7.3V or lower, then UVLO operates and the IC stops. When UVLO operates, the power supply current of the IC is about 80uA or lower. If VIN voltage is 9V or higher, then the IC starts switching operation.

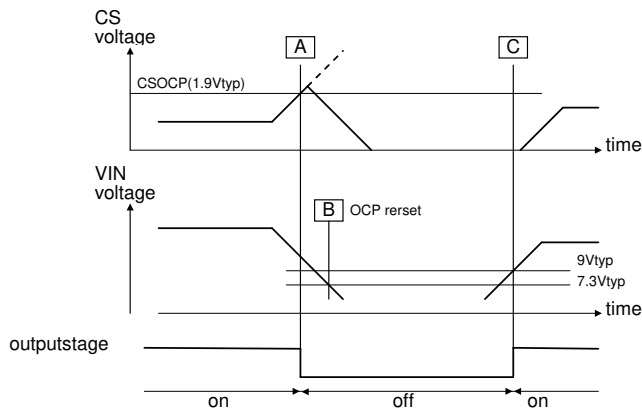


2.OCP(Over Current Protection)

The CS pin sense the current through the MOS FET switch and the primary side of the transformer.This provides an additional level of protection in the event of a fault. If the voltage of the CS pin exceeds VCSOCP(1.9Vtyp)(A), the internal comparator will detect the event and turn off the MOSFET. The peak switch current is calculated

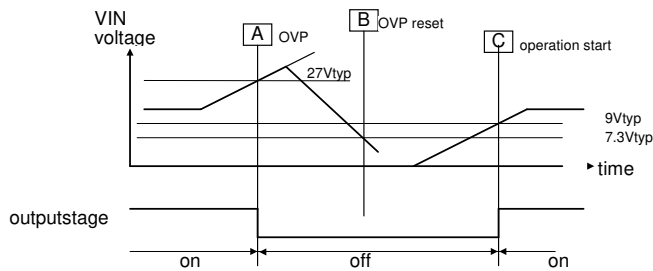
$$I_{o(peak)} [A] = V_{SOCP}[V]/R_{sense}[ohm]$$

The VIN pin is pulled down to fixed level, keeping the controller latched off.The lach reset occurs when the user disconnects LED from VAC and lets the VIN falls below the VIN reset voltage,UVLOOFF(7.3Vtyp)(B). Then VIN rise UVLOON(9Vtyp)(C),restart the switching.



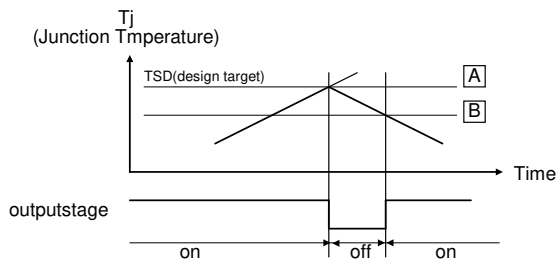
3.OVP(Over Voltage Protection)

If the voltage of VIN pin is higher than the internal reference voltage VINOVP(27Vtyp),switching operation is stopped. The stopping operation is kept until the voltage of VIN is lower than 7.3V. If the voltage of VIN pin is higher than 9V, the switching operation is restated.



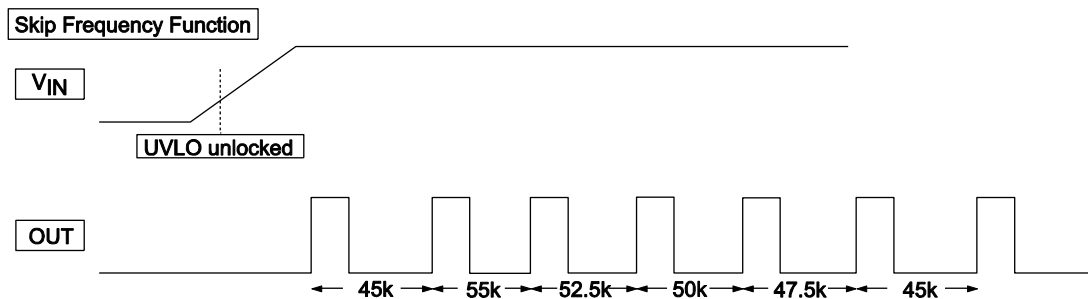
4.TSD(Thermal Shut Down protection)

The thermal shutdown function works when the junction temperature of IC is 165°C (typ) (A), and the IC switching stops. The IC starts switching operation again when the junction temperature is 135°Ctyp (B) or lower.



Skip frequency function

LV5028TT contains the skip frequency function for reduction of the peak value of conduction noise. This function changes the frequency as follows.



Switching frequency is changed as follows.
 ... ×0.9 → ×1.1 → ×1.05 → ×1 → ×0.95 → ×0.9 → ×1.1 ...
 It's repeated by this loop.

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