Voltage -20 V Current -0.6A	SOT-523	Unit : inch(mm
Features	0.043(1.10)	0.024(0.60)
 RDS(ON) , VGS@-4.5V, ID@-0.6A<340mΩ 	0.067(1.70) 0.064(1.10) 0.044(1.10) 0.036(9.80)	
 RDS(ON), VGS@-2.5V, ID@-0.4A<420mΩ 		
 RDS(ON) , VGS@-1.8V, ID@-0.2A<600mΩ 		0.007(0.17)
Advanced Trench Process Technology		
 Specially Designed for Switch Load, PWM Application, etc. 	0.067(1.70)	
ESD Protected 2KV HBM		
 Lead free in compliance with EU RoHS 2.0 		h
 Green molding compound as per IEC 61249 standard 		
	0.012(0.30) 0.004(0.10)	
Mechanical Data		D 3
Case : SOT-523 Package		
 Terminals : Solderable per MIL-STD-750, Method 2026 		
Approx. Weight : 0.002 grams		
Marking : E03	1	2

PANJIT

Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	-20	V
Gate-Source Voltage		V _{GS}	<u>+</u> 8	V
Continuous Drain Current		lo	-0.6	А
Pulsed Drain Current		Idm	-2.4	А
Power Dissipation	T _a =25°C	PD	300	mW
	Derate above 25°C		2.4	mW/∘C
Operating Junction and Storage Temperature Range		Tj,Tstg	-55~150	٥C
Typical Thermal Resistance - Junction to Ambient ^(Note 3)		Reja	417	°C/W



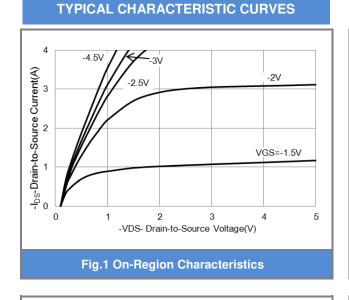
Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250uA	-20	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V _{DS} =V _{GS} , I _D =-250uA	-0.4	-0.64	-1.0	V
Drain-Source On-State Resistance	e R _{DS(on)}	V_{GS} =-4.5V, I _D =-0.6A	-	280	340	mΩ
		V_{GS} =-2.5V, I _D =-0.4A	-	330	420	
		V _{GS} =-1.8V, I _D =-0.2A	-	420	600	
Zero Gate Voltage Drain Current	IDSS	V_{DS} =-20V, V_{GS} =0V	-	-0.01	-1	uA
Gate-Source Leakage Current	lgss	V _{GS=<u>+</u>8V, V_{DS}=0V}	-	<u>+</u> 3.5	<u>+</u> 10	uA
Dynamic						
Total Gate Charge	Qg	V _{DS} =-10V, I _D =-0.6A, V _{GS} =-4.5V ^(Note 1,2)	-	2.2	-	nC
Gate-Source Charge	Qgs		-	0.4	-	
Gate-Drain Charge	Q_{gd}		-	0.5	-	
Input Capacitance	Ciss	V _{DS} =-10V, V _{GS} =0V, f=1.0MHZ	-	151	-	
Output Capacitance	Coss		-	27	-	pF
Reverse Transfer Capacitance	Crss		-	9	-	
Switching						
Turn-On Delay Time	td _(on)		-	9	-	
Turn-On Rise Time	tr	$V_{DD}=-10V, I_{D}=-0.6A,$	-	37	-	
Turn-Off Delay Time	td _(off)	$V_{GS}=-4.5V$,	-	128	-	ns
Turn-Off Fall Time	tf	$R_G=6\Omega^{(Note 1,2)}$	-	72	-	
Drain-Source Diode						
Maximum Continuous Drain-Source	Is				-0.4	А
Diode Forward Current	IS		-	-	-0.4	А
Diode Forward Voltage	V _{SD}	Is=-1A, V _{GS} =0V	-	-0.95	-1.2	v

NOTES :

- 1. Pulse width</br>
- 2. Essentially independent of operating temperature typical characteristics.
- 3. ReJA is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited





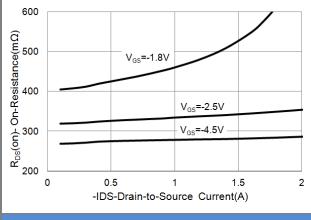
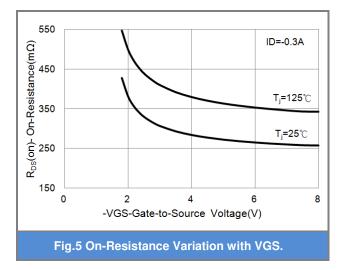
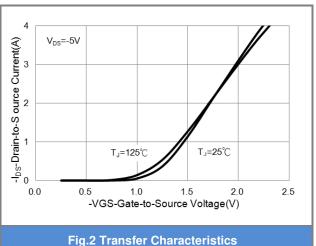


Fig.3 On-Resistance vs. Drain Current





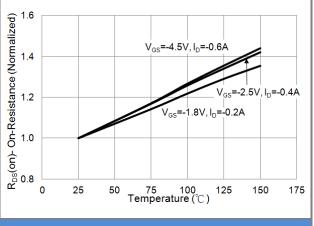
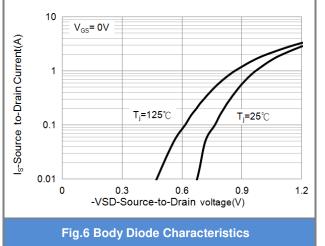
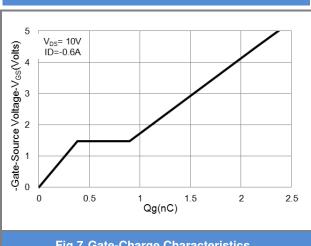


Fig.4 On-Resistance vs. Junction temperature







TYPICAL CHARACTERISTIC CURVES

Fig.7 Gate-Charge Characteristics

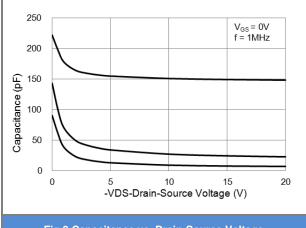


Fig.9 Capacitance vs. Drain-Source Voltage

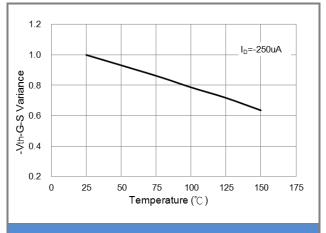


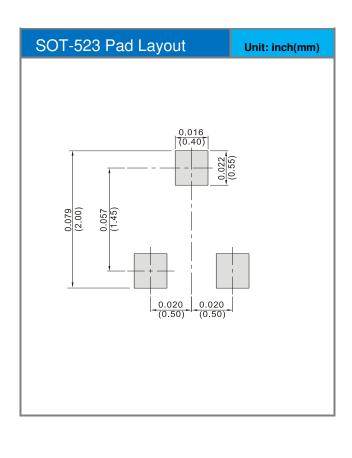
Fig.8 Threshold Voltage Variation with Temperature



PART NO. PACKING CODE VERSION

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJE8403_R1_00001	SOT-523	4K pcs / 7" reel	E03	Halogen free RoHS compliant

MOUNTING PAD LAYOUT







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