DEM-OPA-MSOP-2B Demonstration Fixture

1 Description

The DEM-OPA-MSOP-2B demonstration fixture is a generic, unpopulated printed circuit board (PCB) for dual high-speed operational amplifiers in MSOP-10 packages. Figure 1 shows the package pinout for this PCB. For more information on these op amps, as well as good PCB layout techniques, see the individual amplifier data sheets.

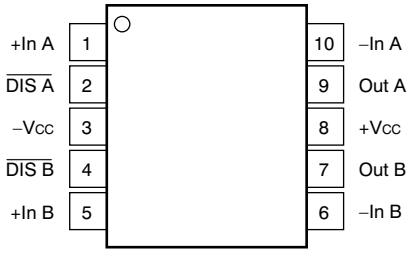


Figure 1. MSOP Package Pinout, Top View

2 Circuit

The circuit schematic in Figure 2 shows the connections for all possible components. Each configuration uses only some of the components.

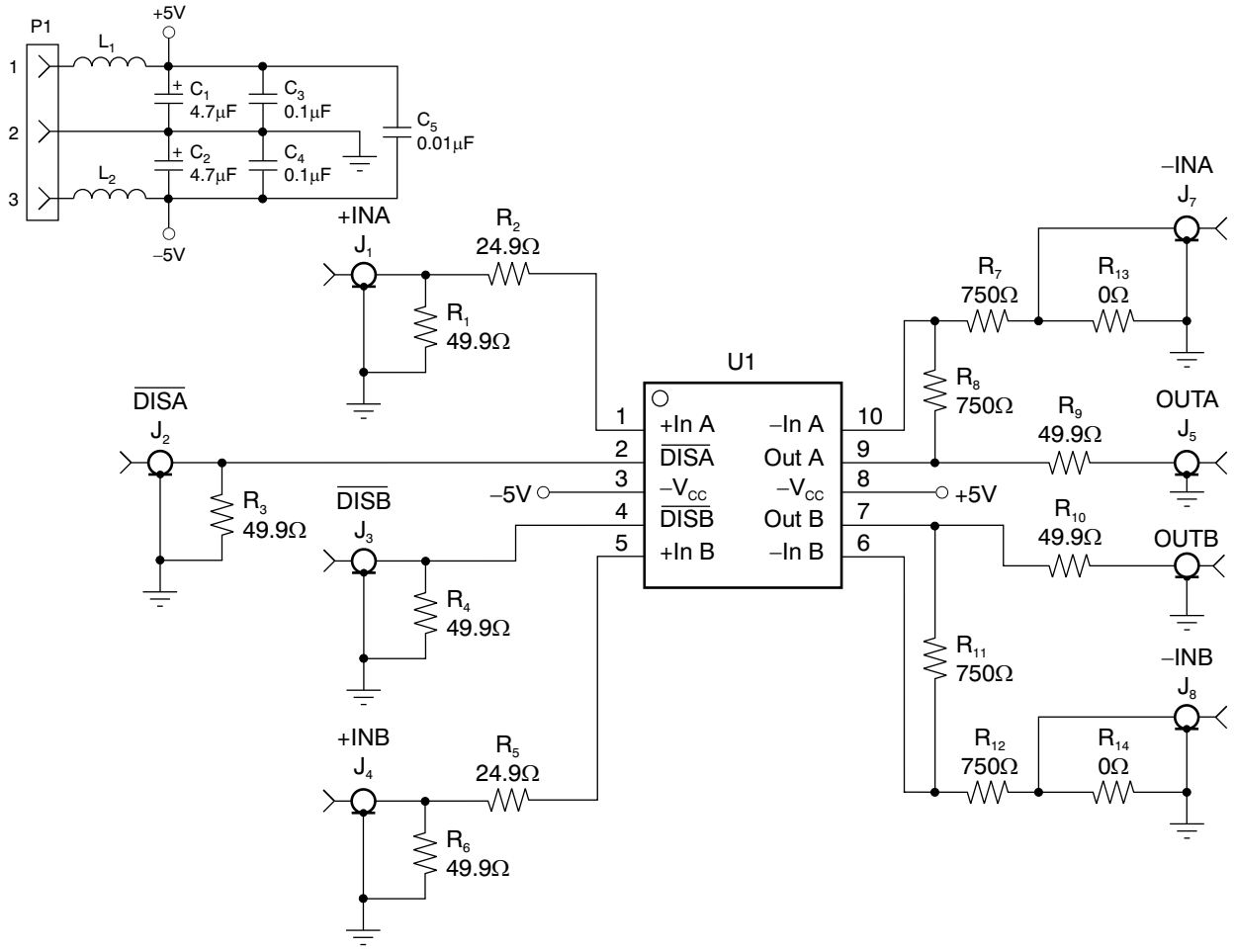


Figure 2. Schematic for DEM-OPA-MSOP-2B

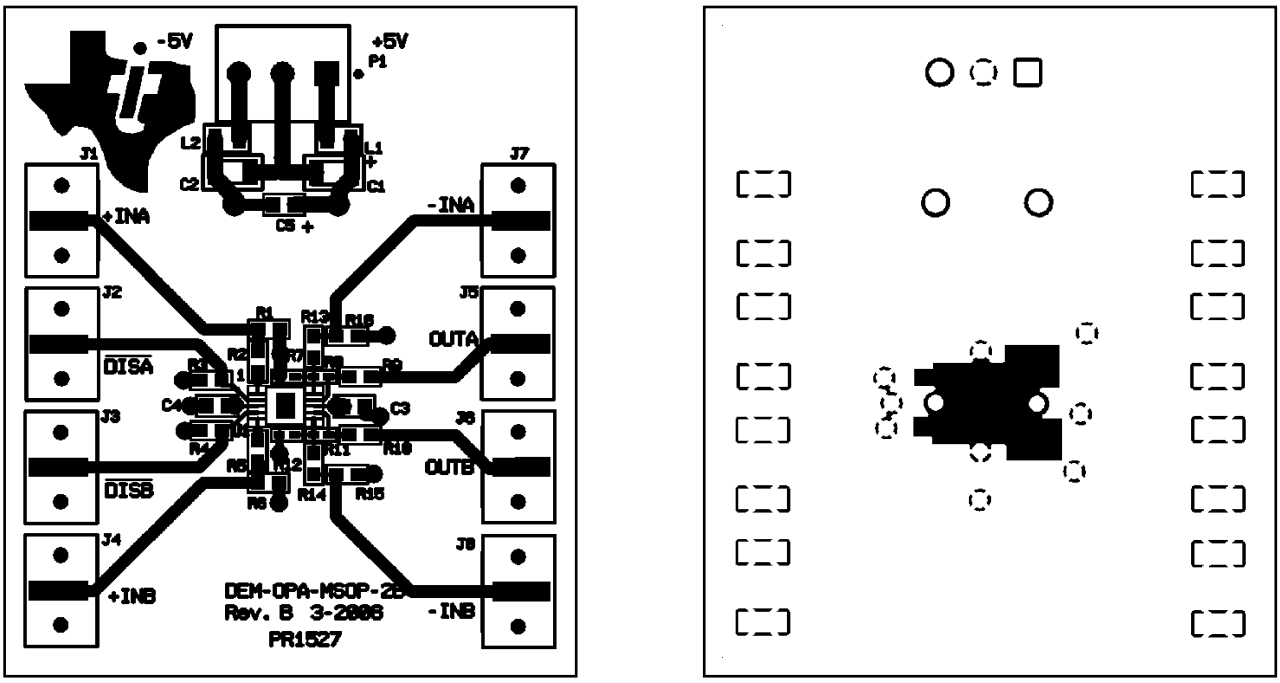
3 Components

Components that have RF performance similar to the ones listed in Table 1 may be substituted.

Table 1. Component Descriptions

PART	DESCRIPTION
C_1, C_2	Tantalum Chip Capacitor, SMA EIA Size 3216, 20V
C_5, C_3, C_4	Multilayer Ceramic Chip Capacitor, SMD 0603, 25V
+INA, +INB, –INA, –INB, OUTA, OUTB, $\overline{DISA}$, $\overline{DISB}$	SMA or SMB Board Jack (Amphenol 901-144-8)
L_1 , L_2	EMI-suppression ferrite chip, SMD 0805 (Steward LI 0805 B 900 R)
P_1	Terminal block, 3.5mm centers (On-Shore Technology ED555/3DS)
R_1 – R_6, R_9, R_{12} – R_{14}	Metal film chip resistor, SMD 0603, 1/8W
R_7, R_8, R_{10}, R_{11}	Metal film chip resistor, SMD 0402, 1/16W

The location of the following components is illustrated in Figure 3. R_1 and R_2 are the input resistance matching the source impedance for each amplifier. R_2 and R_5 are series isolation resistance that may help isolate the input parasitic from the source. R_3 and R_4 are input resistance matching for the disable line. R_7 and R_9 are the gain resistors. Note that in order to have a noninverting configuration, R_{13} and R_{14} need to be 0Ω. If an inverting configuration is desired, R_{13} and R_{14} are the matching input resistance with R_1 and R_2 set at 0Ω. R_8 and R_{11} are the feedback resistors. R_9 and R_{10} are output matching resistance and should be set to 50Ω for a 50Ω environement. L_1 and L_2 are ferrite chips that can reduce interactions with the power supply at high frequencies; if not desired, they can be replaced with 0Ω resistors. The power supplies are each respectively bypassed with two capacitors: C_1 and C_3 for the positive supply, and C_2 and C_4 for the negative supply. C_4 and C_5 are usually set between 2.2µF and 6.8µF, where C_2 and C_3 are 0.1µF ceramic capacitors. C_5, usually set at 10nF, is connected between the positive and negative power supplies.



(a) Top Layer

(b) Mid Layer

(c) Bottom Layer

Figure 3. DEM-OPA-MSOP-2B Demonstration Fixture Layout

4 Board Layout

This demonstration fixture is a four-layer PCB. The ground plane has been opened up around op amp pins that are sensitive to capacitive loading. Power-supply traces are laid out to keep current loop areas to a minimum. The SMA (or SMB) connectors may be mounted either vertically or horizontally onto the board edge. The location and type of capacitors used for power-supply bypassing are crucial to high-frequency amplifiers. The tantalum capacitors, C_1 and C_2, do not need to be as close to pins 3 and 8 on the PCB and may be shared with other amplifiers. See the individual op amp data sheets for more information on proper board layout techniques and component selection.

5 Measurement Tips

This demonstration fixture, with the component values shown, is designed to operate in a 50Ω environment; most data sheet plots are obtained under these conditions. It is easy to change the component values for different input and output impedance levels. However, do not use high-impedance probes; they represent a heavy capacitive load to the operational amplifier, and will alter the amplifier response. Instead, use low-impedance (≤ 500Ω) probes with adequate bandwidth. The probe input capacitance and resistance set an upper limit on the measurement bandwidth. If a high-impedance probe must be used, place a 100Ω resistor on the probe tip to isolate its capacitance from the circuit.

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