

Working Together for a Greener Society

Future of Power Electronics and the Earth



Selection Guide of LED Driver

All information in this guide is as of the date of publication. Please make sure that you are using the latest version of the guide. If you need more product information, please refer to our data sheets.

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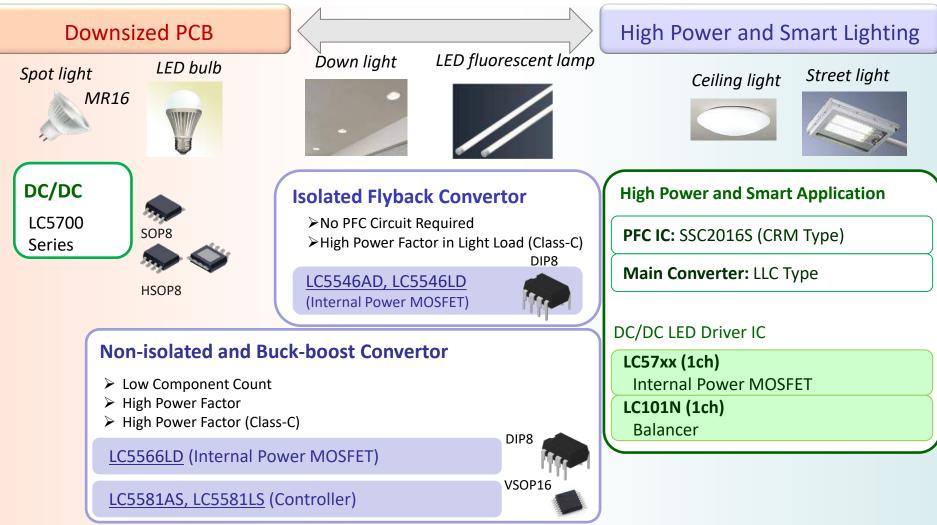
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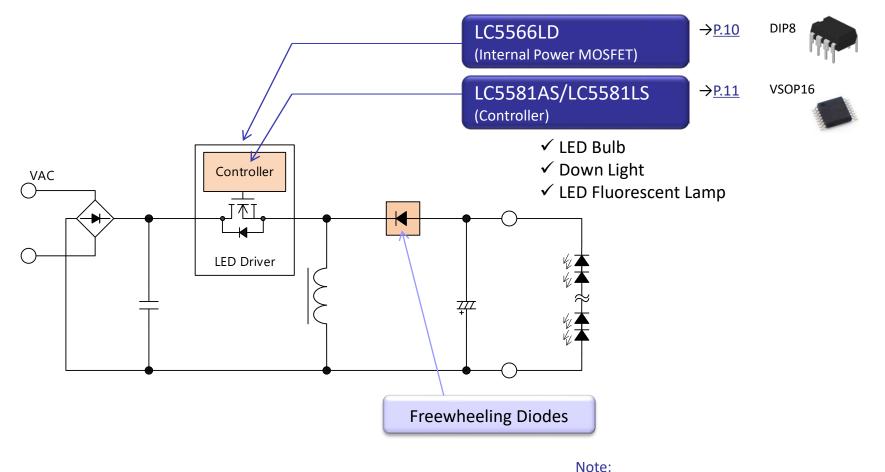


Products offers to meet various needs such as various form and loads of the lamp.

Off-line Buck Type (Low Power Application)



Buck-boost Converter



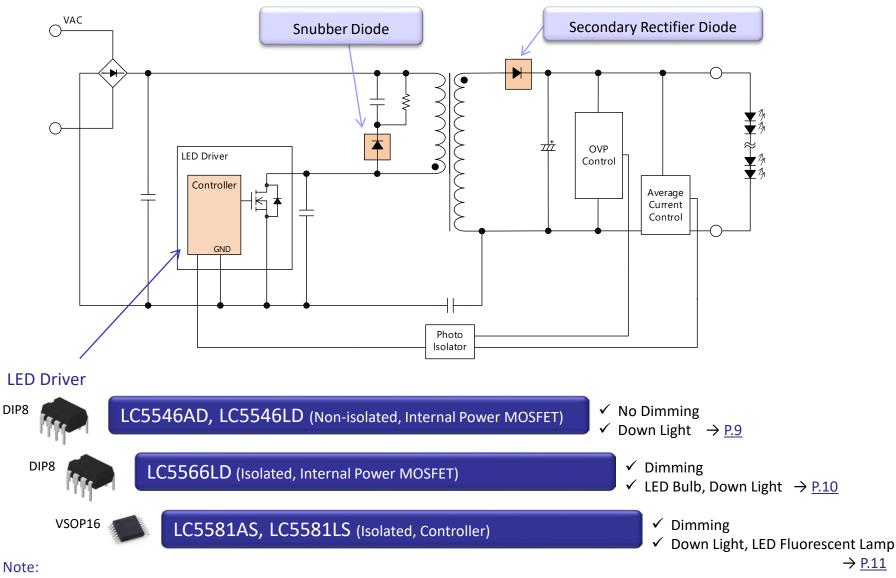
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Refer to the selection guide of diode about peripheral diodes.

Off-line Flyback Type (Low to Middle Power Application)



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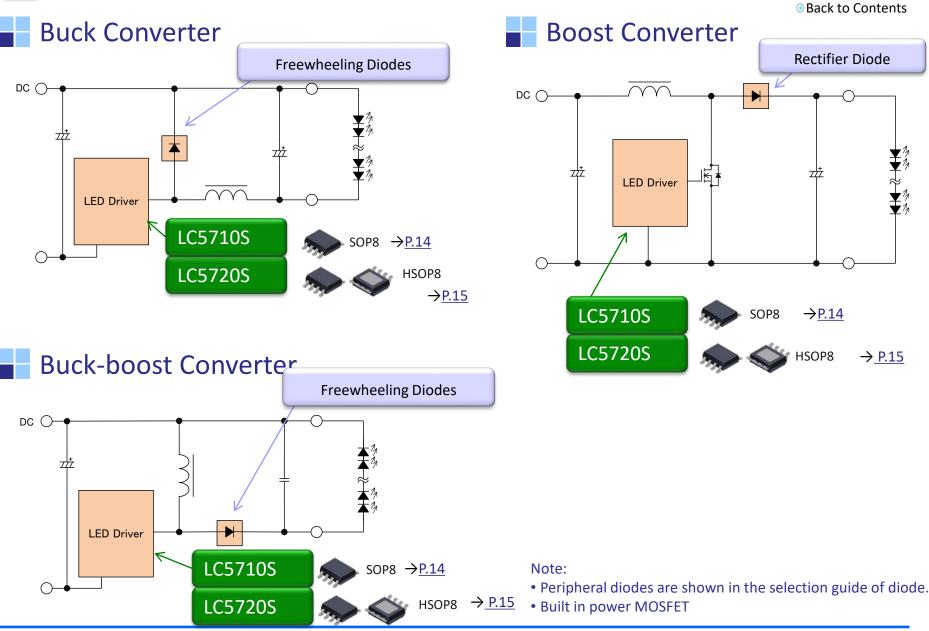


Peripheral diodes are shown in the selection guide of diode.

 $\rightarrow \underline{P.11}$



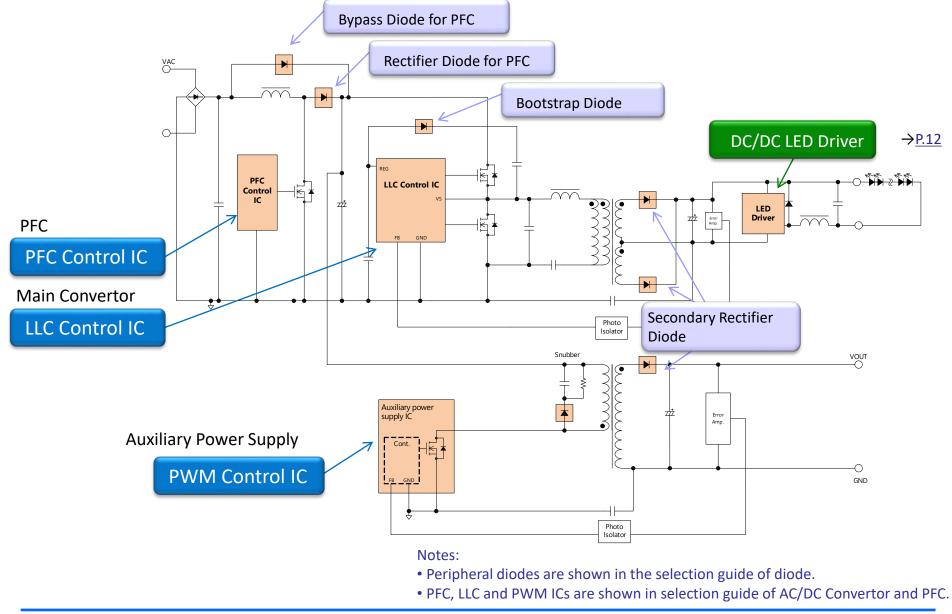




High Power and Smart Application



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Off-line LED Driver IC Selection Guide

- ➢ High Power Factor in Light Load (Class-C)
- > No Input Electrolytic Capacitor Required
- Isolated and Non-isolated Type





LED fluorescent lamp



Series	Package	Applications	Features	Page
LC5546AD LC5546LD	DIP8	 Down light LED fluorescent lamp 	 650 V power MOSFET Startup circuit Isolated flyback 	<u>P.9</u>
LC5566LD	DIP8	• LED bulb • Down light	 650 V power MOSFET Startup circuit Non-isolated flyback/buck-boost Analog dimming 	<u>P.10</u>
LC5581AS LC5581LS	VSOP16	 Down light LED fluorescent lamp 	 Controller type (external power MOSFET) 800 V startup circuit Non-isolated flyback/buck-boost Analog dimming Shorter time from startup to LED lighting 	<u>P.11</u>



No Input Electrolytic Capacitor Required, IEC61000-3-2 class-C Isolated LED Driver IC

LC5546AD, LC5546LD



Back to Product List

Package **Selection Guide** DIP8 Part MOSFET POUT OLP, OVP, **Operation Mode** AC230V Universal TSD Number V_{DSS} R_{DS(ON)} 72 kHz PWM/ Auto-**Features** LC5546AD restart Quasi-resonant \geq 650 V Startup Circuit 650 V 1.9 Ω 20 W 16 W 60 kHz PWM/ Latched **One-converter PFC Operation** \geq LC5546LD Shutdown Quasi-resonant Selectable Operation Mode from \geq PWM or Quasi-resonant Operation **Pin Configuration Definitions** High Power Factor in Light Load (IEC61000-3-2 class C) \geq Pin Protections Symbol **Functions** \geq Number **OCP:** Pulse-by-Pulse Power MOSFET source and OLP. OVP, and TSD: Auto-restart or Latched Shutdown S/GND 1 ground Supply voltage input and OVP **Typical Application** VCC 2 signal input OCP and QR signal input, and OCP 3 **OVP** signal input Feedback signal input and I FD FB 4 **OLP** signal input LC5546xD C13 R17 5 NF No function D/ST DZ2 C3 Control 015 Tc12 Block 6 OVP C17 OVP signal input D6 🛨 /GND VCC Pin removed 7

Rocp ≱

R3

Power MOSFET drain and

startup current input

D/ST

8

No Input Electrolytic Capacitor Required, IEC61000-3-2 Class-C Non-isolated LED Driver IC

LC5566LD



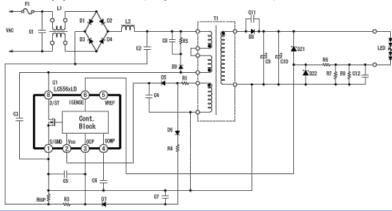
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Package	
DIP8	

Features

- 650 V Startup Circuit \geq
- **One-converter PFC Operation** \geq
- \geq Selectable Operation Mode from 60 kHz PWM or Quasi-resonant Operation
- High Power Factor in Light Load (IEC61000-3-2 class C) \geq
- Built-in Error Amplifier (Adjustable Reference Voltage) \geq
- \geq Protections **OCP:** Pulse-by-Pulse OLP, OVP, and TSD: Latched Shutdown

Typical Application (Flyback Circuit)



Selection Guide

Part	Operation Power MOSE		Power MOSFET		DUT
Number	Mode	V _{DSS} R _{DS(ON)}		AC230V	Universal
LC5566LD	60 kHz PWM/ Quasi-resonant	650V	1.9 Ω	20 W	16 W

Pin Configuration Definitions

Pin Number	Symbol	Functions
1	S/GND	Power MOSFET source and ground
2	VCC	Supply voltage input and OVP signal input
3	ОСР	OCP and QR signal input, and OVP signal input
4	COMP	Feedback phase-compensation input
5	VREF	Dimming control signal input
6	ISENSE	Output current sensing voltage input
7	_	Pin removed
8	D/ST	Power MOSFET drain and startup current input

No Input Electrolytic Capacitor Required, IEC61000-3-2 Class-C Non-isolated LED Driver IC LC5581AS, LC5581LS



Package

VSOP16



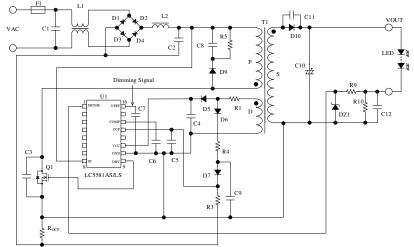
Features

- > 800 V High Voltage Startup Circuit
- Selectable Operation Mode from
 60 kHz PWM or Quasi-resonant Operation
- Standby Function
- Analog Dimming Function
- Protections
 OCP: Pulse-by-pulse
 OLP: Auto-restart
 OVP: Latched Shutdown/Auto-restart

Selection Guide

Part Number	Operation Mode	Protection Operation			
Fait Number	Operation Mode	OLP	OVP		
LC5581AS	60 kHz PWM/ Quasi-resonant	Auto-restart	Auto-restart		
LC5581LS		Auto-restart	Latched Shutdown		

Typical Application (Flyback Circuit)



Pin Configuration Definitions

Pin Number	Symbol	Functions
1	ISENSE	Feedback current detection
2 - 7	(NC)	No connection
8	ST	Startup current input
9	DRV	Drive output
10	GND	Ground
11	VCC	Power supply voltage input for control part and OCP pin overvoltage protection signal input
12	(NC)	No connection
13	ОСР	Input for overcurrent protection and quasi- resonant signal
14	СОМР	Feedback Phase compensation
15	(NC)	No connection
16	VREF	Dimming signal input and standby signal input

DC/DC LED Driver IC Selection Guide



- For Intelligent LED Lighting Application
- For LED Back Light Application
- Individual Channels Control



Series	Output Count	V _{IN(MAX)}	۱ _٥	V _{LED(MAX)}	Package	Features	Page
LC101N (Current Balancer)	1	35 V	150 mA	35 V	DFN8	Balancer	<u>P.13</u>
LC5710S	1	58 V	1.0 A	58 V	SOP8	 Allows buck, buck- boost, and boost circuit PWM dimming Built-in power MOSFET 	<u>P.14</u>
LC5720S	1	50 V	2.0 A	50 V	HSOP8	 Allows buck, buck- boost, and boost circuit PWM dimming Built-in power MOSFET 	<u>P.15</u>

I_{LED} = 150 mA LED Current Balancer LC101N



Package

DFN8



Features

- Current Balancer Across LED String
- Small Package (DFN8)
- Power Dissipation, P_D: 1.3 W
- No Input and Output Capacitor Required
- > Maximum Dropout Voltage, ΔV_{DIF} : 350 mV
- Protections
 - OCP
 - TSD: Activation Temperature is 130 °C without Hysteresis

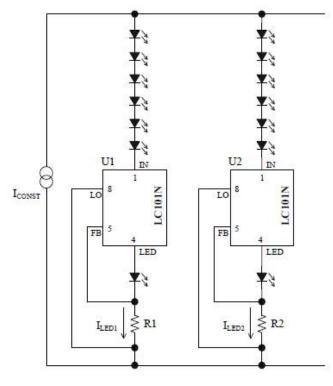
Pin Configuration Definitions

Pin Number	Symbol	Functions
1	IN	Input
2, 3	NC	—
4	LED	Output
5	FB	LED current detection signal input (positive side)
6, 7	NC	—
8	LO	LED current detection signal input (negative side)

Selection Guide

Part Number	I _{LED(MAX)}	V _{LED MAX}	V _{IN}	V_{FB}
LC101N	15 mA to 150 mA	35 V	2.4 V to 35 V	200 mV ± 3%

Typical Application



$I_{IFD} = 1.0 \text{ A}, V_{IN} = 5 \text{ V to } 58 \text{ V}$ LED Driver for Buck, Buck-boost, and Boost Converter

LC5710S

Package

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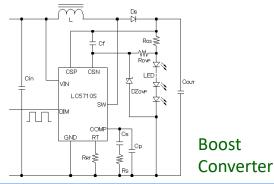
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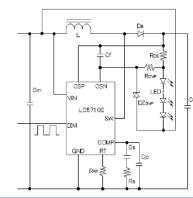
fosc

Selection Guide MOSFET Part Number V_{IN} ILED(MAX) SOP8 R_{DS(ON)} 0.550 Ω(typ.) LC5710S 1.0 A 5 V to 58 V 100 kHz to 500 kHz **Features Pin Configuration Definitions** Allows Buck, Buck-boost, and Boost Circuit Maximum LED Current, ILED : 1.0 A Adjustable Frequency Range: 100 kHz to 500 kHz

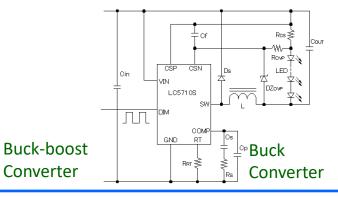
- \geq V_{cs} : 100 mV ± 3 % \geq
- High Accuracy Dimming Control \geq Maximum PWM Frequency : 20 kHz
 - DC Input Voltage: 0.2 V to 2 V
- Protections \geq UVLO, OCP, TSD, LED OVP, LED Open and LED Cross Connection Detection

Typical Applications





Pin Number	Symbol	Functions
1	COMP	Phase compensation
2	RT	Frequency adjust
3	GND	Ground
4	SW	Switch output
5	VIN	DC input
6	CSP	LED current sense (+)
7	CSN	LED current sense (-)
8	DIM	Dimming signal input



I_{LED} = 2.0 A, V_{IN} = 8.5 V to 50 V LED Driver for Buck, Buck-boost, and Boost Converter LC5720S



Back to Product List

Package Selection	Guide						
	Part Number I _{LED(MAX)}		D(MAX)	V	/ _{IN}	MOSFET R _{DS(ON)}	f _{osc}
LC572	20S	2	2.0 A	9.5 V t	to 50 V	/ 0.215 Ω(typ.)	500 kHz
Features			Pin Config	guratior	ו Defin [;]	itions	
 Allows Buck, Buck-boost, and Boost Circuit Maximum LED Current, I_{LED}: 1.0 A 		ļ	Pin Numbe	r Sy	vmbol	Funct	ions
 Frequency : 500 kHz 			1		OMP	Phase compensat	tion
			2		NC	—	
			3		GND	Ground	
→ High efficiency, $\eta > 90\%$ (typ.)	-		4		SW	Output	
Maximum PWM Dimming Frequency : 20 kH	۶Ζ		5	V	VIN	DC input	
Protections OCP : Pulse-by-Pulse			6	(CSP	Reference input detection	
OVP, TSD: Auto-restart			7	C	CSN	Negative input p detection	in of current
Typical Applications		Γ	8	Г	DIM	PWM dimming s	ignal input
Vin cin cin win cin win cin con con con con con con con co		CSP CSN 5720S SW - 5ND	Zovp	-boost verter		5 UN CSD 6	Buck Converter

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